

Silicon N-Channel Power MOSFET

Description

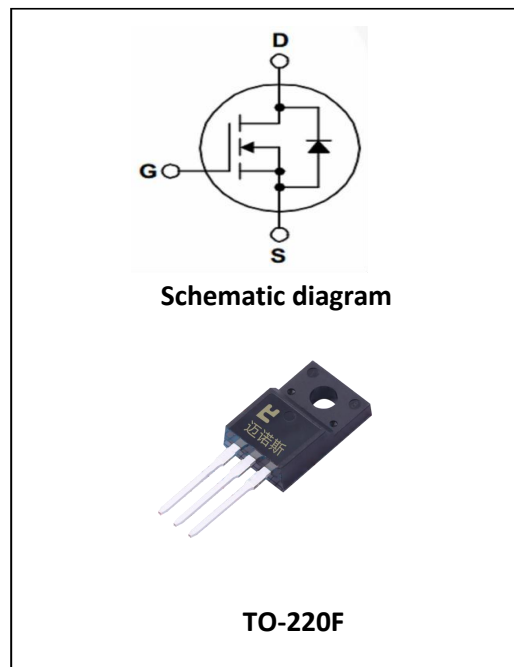
The MPF9N90 uses advanced technology and design to provide excellent $R_{DS(ON)}$. It can be used in a wide variety of applications.

General Features

- ① $V_{DS}=900V$, $R_{ds(on)} < 1.15m\Omega$ @ $V_{GS}=10V$, $I_D=9A$ (Typ: 0.97Ω)
- ② Low ON Resistance
- ③ Low Reverse transfer capacitances
- ④ 100% Single Pulse avalanche energy Test

Application

- ① Power switching application
- ② Adapter and charger



Package Marking And Ordering Information:

Ordering Codes	Package	Product Code	Packing
MPF9N90	TO220F	MPF9N90	Tube

ABSOLUTE RATINGS (at $T_C = 25^\circ C$, unless otherwise specified)

Symbol	Parameter	Rating	Units
V_{DSS}	Drain-to-Source Voltage	900	V
I_D	Continuous Drain Current	9	A
	Continuous Drain Current $T_C = 100^\circ C$	5.6	A
I_{DM}	Pulsed Drain Current(Note1)	36	A
V_{GS}	Gate-to-Source Voltage	± 30	V
E_{AS}	Single Pulse Avalanche Energy(Note2)	1000	mJ
dv/dt	Peak Diode Recovery dv/dt (Note3)	4.0	V/ns
P_D	Power Dissipation TO-220F	50	W
	Derating Factor above $25^\circ C$	0.4	W/ $^\circ C$
T_J, T_{stg}	Operating Junction and Storage Temperature Range	150, -55 to 150	$^\circ C$
T_L	Maximum Temperature for Soldering	300	$^\circ C$

Thermal characteristics TO-220F

Symbol	Parameter	RATINGS	Units
$R_{\theta JC}$	Junction-to-Case	2.5	$^{\circ}\text{C}/\text{W}$
$R_{\theta JA}$	Junction-to-Ambient	62.5	$^{\circ}\text{C}/\text{W}$

Electrical Characteristics (at $T_C = 25^{\circ}\text{C}$, unless otherwise specified)

OFF Characteristics

Symbol	Parameter	Test Conditions	Values			Units
			Min.	Typ.	Max.	
V_{DS}	Drain to Source Breakdown Voltage	$V_{GS}=0\text{V}$, $I_D=250\mu\text{A}$	900	--	--	V
$\Delta B V_{DS} / \Delta T_J$	Bvdss Temperature Coefficient	$I_D=250\mu\text{A}$, Reference 25°C	--	1	--	$\text{V}/^{\circ}\text{C}$
I_{DSS}	Drain to Source Leakage Current	$V_{DS}=900\text{V}$, $V_{GS}=0\text{V}$, $T_J = 25^{\circ}\text{C}$	--	--	1	μA
		$V_{DS}=720\text{V}$, $V_{GS}=0\text{V}$, $T_J = 125^{\circ}\text{C}$	--	--	10	μA
$I_{GSS(F)}$	Gate to Source Forward Leakage	$V_{GS}=+30\text{V}$	--	--	100	nA
$I_{GSS(R)}$	Gate to Source Reverse Leakage	$V_{GS}=-30\text{V}$	--	--	-100	nA

ON Characteristics

Symbol	Parameter	Test Conditions	Values			Units
			Min.	Typ.	Max.	
$R_{DS(ON)}$	Drain-to-Source On- Resistance	$V_{GS}=10\text{V}$, $I_D=4.5\text{A}(\text{Note4})$	--	0.97	1.15	Ω
$V_{GS(TH)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}$, $I_D = 250\mu\text{A}(\text{Note4})$	3.0	--	5.0	V

Dynamic Characteristics

Symbol	Parameter	Test Conditions	Values			Units
			Min.	Typ.	Max.	
R_g	Gate resistance	$f = 1.0\text{MHz}$	--	0.9	--	Ω
C_{iss}	Input Capacitance	$V_{GS} = 0\text{V}$ $V_{DS} = 25\text{V}$ $f = 1.0\text{MHz}$	--	2510	--	PF
C_{oss}	Output Capacitance		--	195	--	
C_{rss}	Reverse Transfer Capacitance		--	10	--	

Switching Characteristics

Symbol	Parameter	Test Conditions	Values			Units
			Min.	Typ.	Max.	
$t_{d(ON)}$	Turn-on Delay Time	$I_D = 9A$ $V_{DD} = 450V$ $V_{GS} = 10V$ $R_G = 20\Omega$	--	55	--	ns
t_r	Rise Time		--	103	--	
$t_{d(OFF)}$	Turn-Off Delay Time		--	110	--	
t_f	Fall Time		--	73	--	
Q_g	Total Gate Charge	$I_D = 9A$ $V_{DD} = 720V$ $V_{GS} = 10V$	--	52	--	nC
Q_{gs}	Gate to Source Charge		--	13	--	
Q_{gd}	Gate to Drain ("Miller") Charge		--	15	--	

Source-Drain Diode Characteristics

Symbol	Parameter	Test Conditions	Values			Units
			Min.	Typ.	Max.	
I_S	Continuous Source Current (Body Diode)	$T_C = 25^\circ C$	--	--	9	A
I_{SM}	Maximum Pulsed Current (Body Diode)		--	--	36	A
V_{SD}	Diode Forward Voltage	$I_S = 9A$, $V_{GS} = 0V$ (Note4)	--	--	1.2	V
T_{rr}	Reverse Recovery Time	$I_S = 9A$, $T_J = 25^\circ C$ $dI_F/dt = 100A/\mu s$, $V_{GS} = 0V$	--	610	--	ns
Q_{rr}	Reverse Recovery Charge		--	5500	--	nC

Note1: Pulse width limited by maximum junction temperature

Note2: $L = 20mH$, $V_{DS} = 50V$, Start $T_J = 25^\circ C$

Note3: $I_{SD} = 9A$, $dI/dt \leq 100A/\mu s$, $V_{DD} \leq BV_{DS}$, Start $T_J = 25^\circ C$

Note4: Pulse width $t_p \leq 300\mu s$, $\delta \leq 2\%$

Characteristics Curves

Figure 1a Safe Operating Area (No FullPAK)

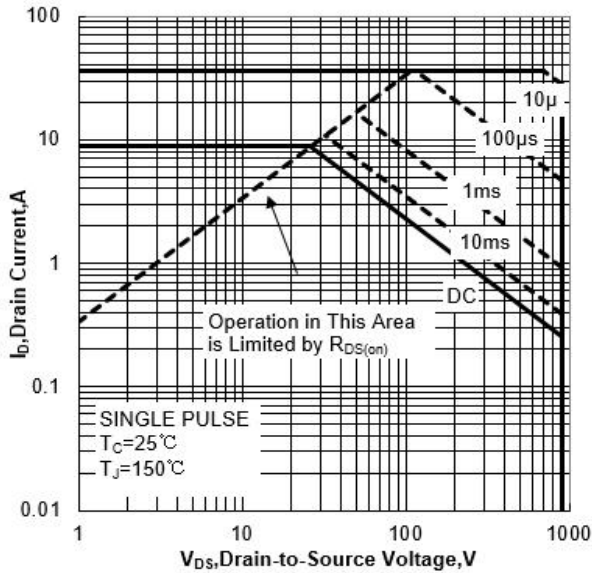


Figure 1b Safe Operating Area (FullPAK)

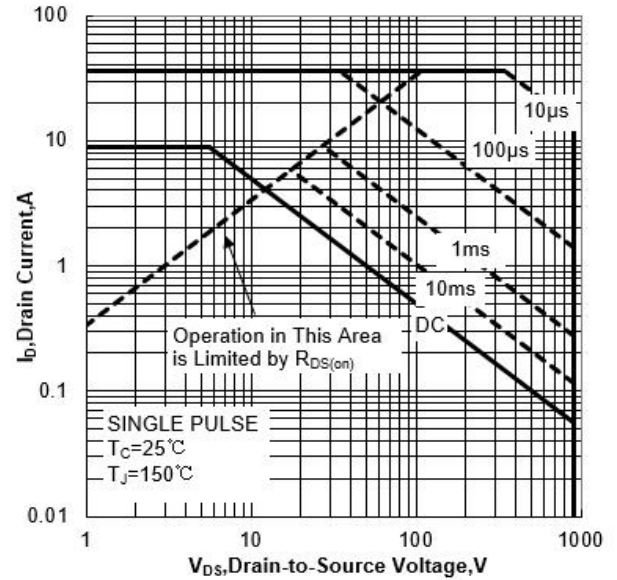


Figure 2a Power Dissipation (No FullPAK)

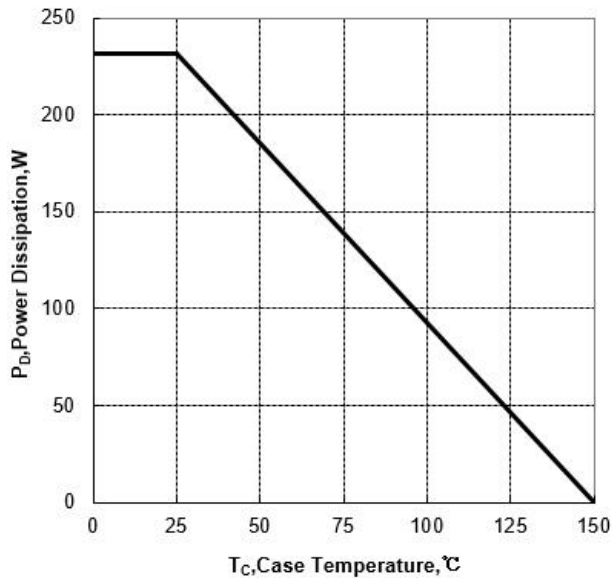


Figure 2b Power Dissipation (FullPAK)

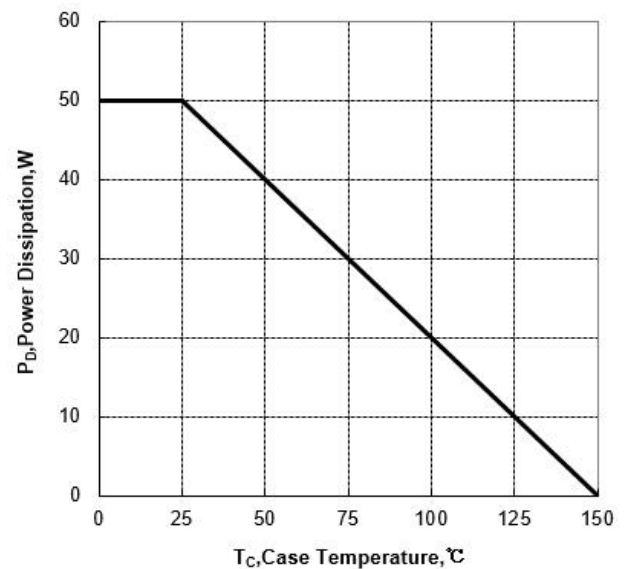


Figure 3a Max Thermal Impedance (No FullPAK)

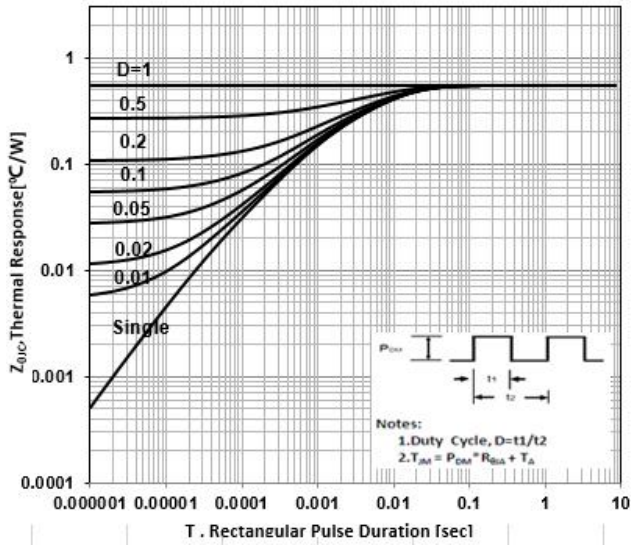


Figure 3b Max Thermal Impedance (FullPAK)

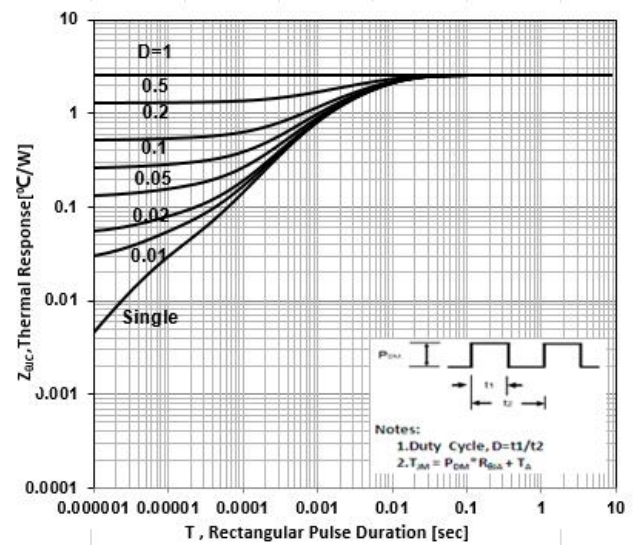


Figure 4 Typical Output Characteristics

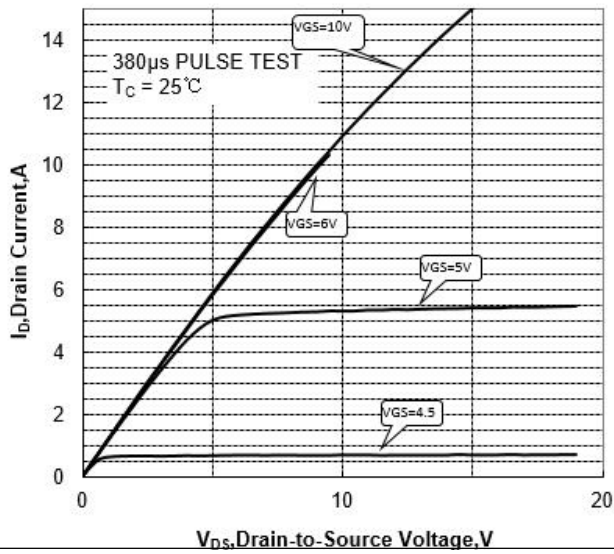


Figure 5 Typical Transfer Characteristics

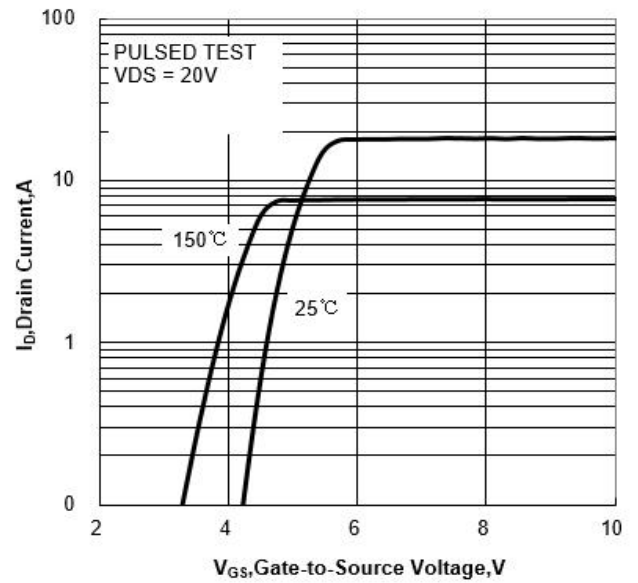


Figure 6 Typical Drain to Source ON Resistance vs Drain Current

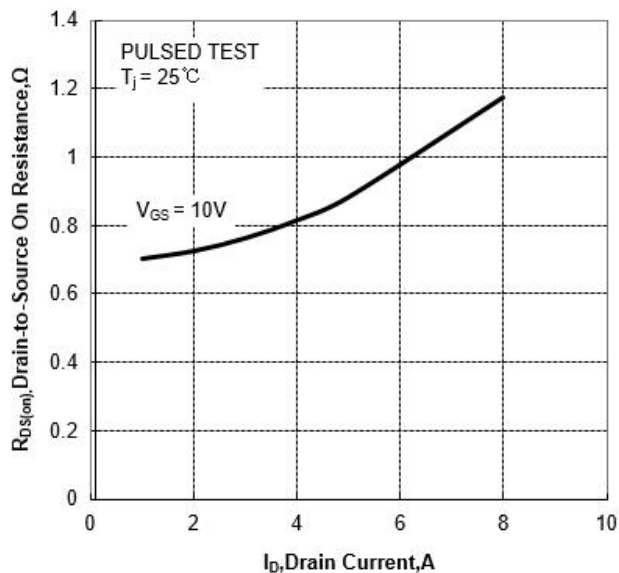


Figure 7 Typical Drain to Source on Resistance vs Junction Temperature

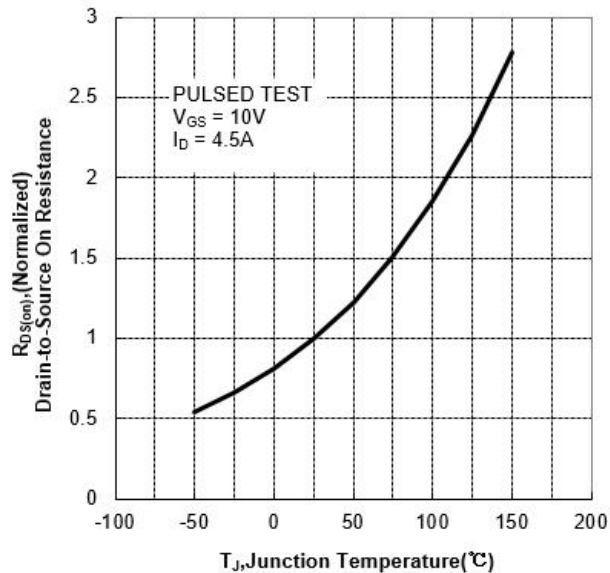


Figure 8 Typical Threshold Voltage vs Junction Temperature

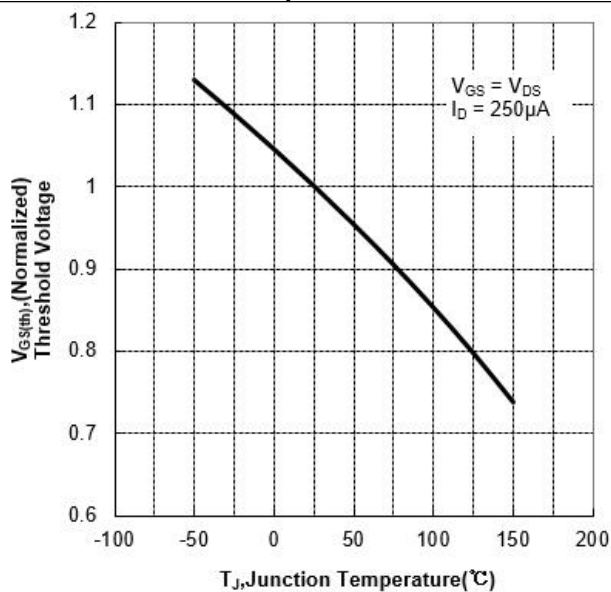


Figure 9 Typical Breakdown Voltage vs Junction Temperature

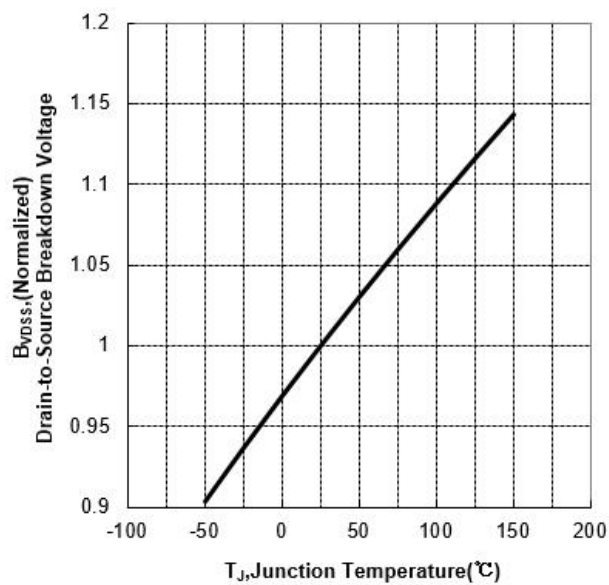


Figure 10 Typical Capacitance vs Drain to Source Voltage

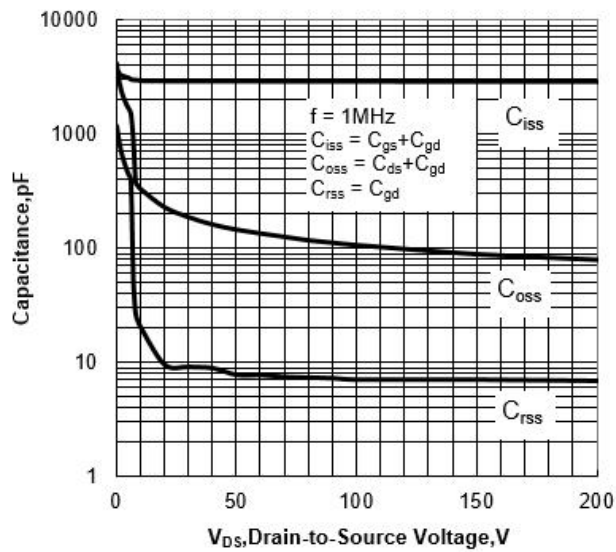
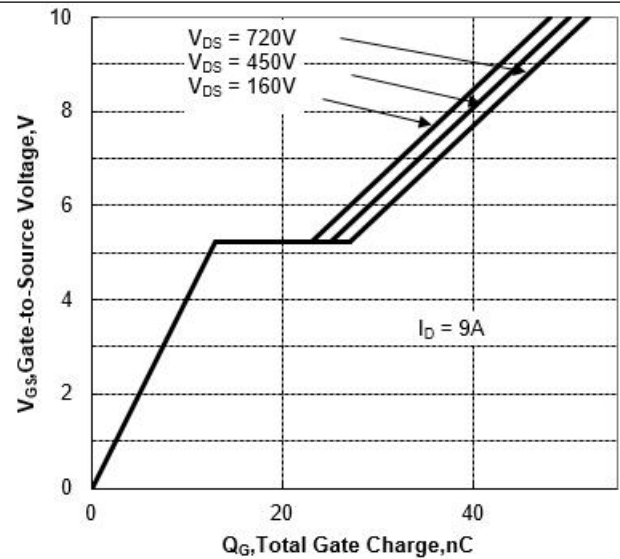


Figure 11 Typical Gate Charge vs Gate to Source Voltage



Test Circuit and Waveform

Figure 12 Gate Charge Test Circuit

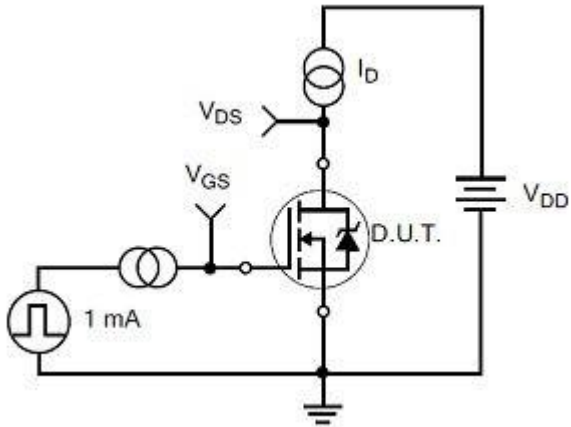


Figure 13 Gate Charge Waveforms

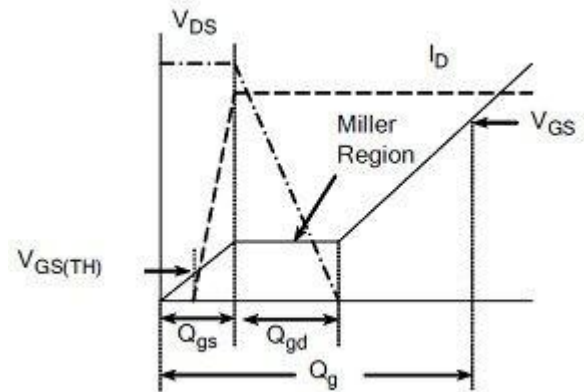


Figure 14 Resistive Switching Test Circuit

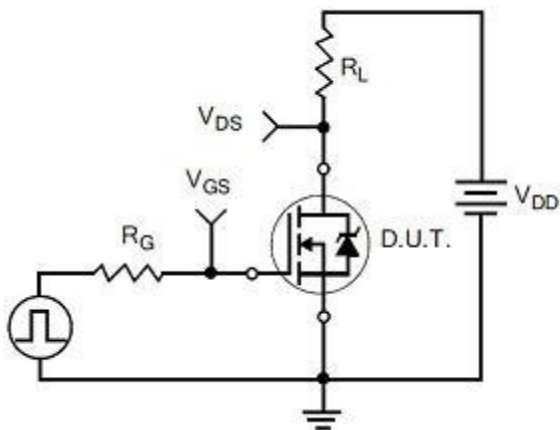


Figure 15 Resistive Switching Waveforms

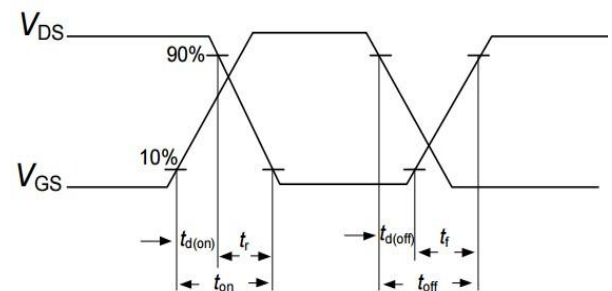


Figure 16 Diode Reverse Recovery Test Circuit

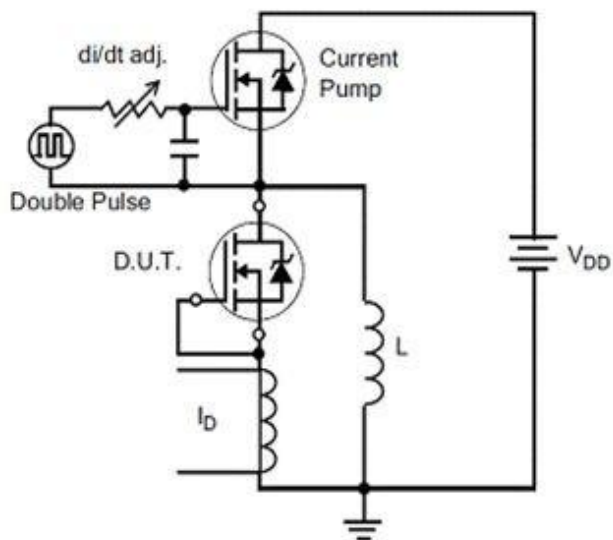


Figure 17 Diode Reverse Recovery Waveform

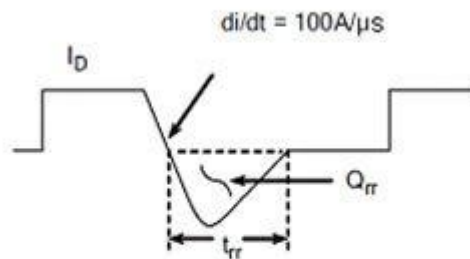


Figure 18 Unclamped Inductive Switching Test Circuit

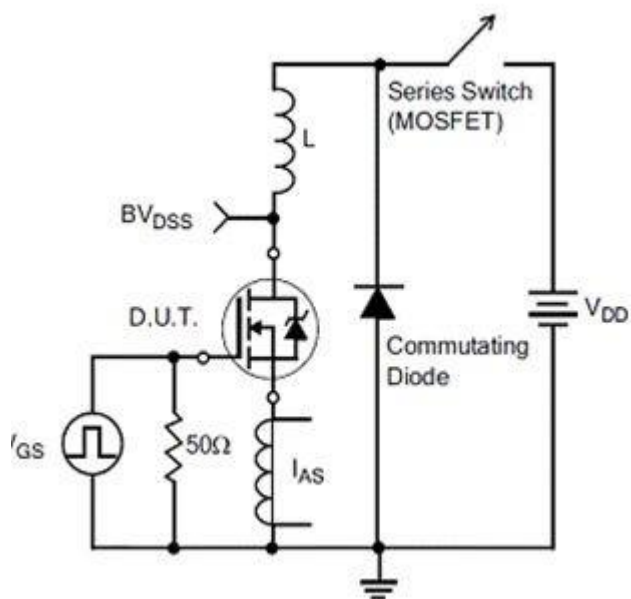
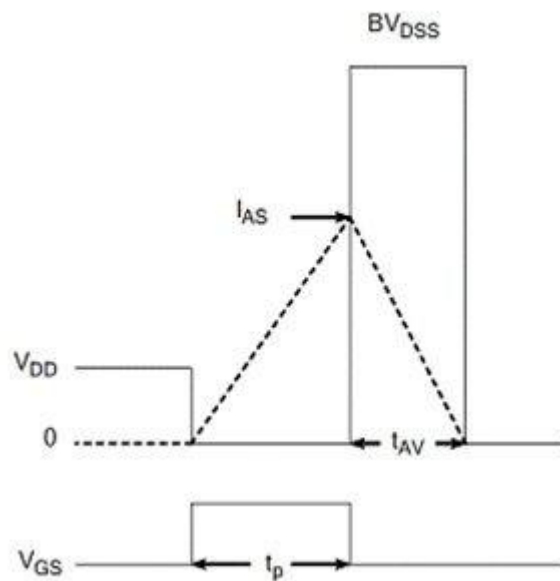
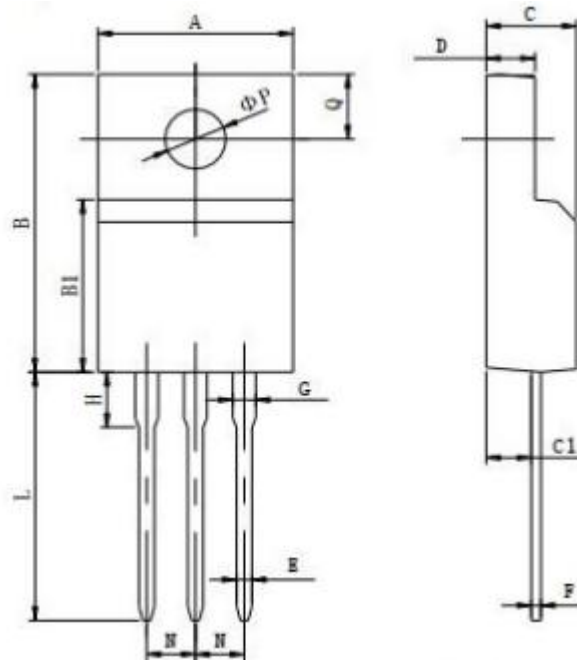


Figure 19 Unclamped Inductive Switching Waveform



Package Description



Items	Values(mm)	
	MIN	MAX
A	9.60	10.4
B	15.4	16.2
B1	8.90	9.50
C	4.30	4.90
C1	2.10	3.00
D	2.40	3.00
E	0.60	1.00
F	0.30	0.60
G	1.12	1.42
H	3.40	3.80
	1.60	2.90
L	12.0	14.0
N	2.34	2.74
Q	3.15	3.55
Φp	2.90	3.30

TO-220F Package



迈诺斯科技

MPF9N90

NOTE:

1. Exceeding the maximum ratings of the device in performance may cause damage to the device, even the permanent failure, which may affect the dependability of the machine. Please do not exceed the absolute maximum ratings of the device when circuit designing.
2. When installing the heat sink, please pay attention to the torsional moment and the smoothness of the heat sink.
3. MOSFETs is the device which is sensitive to the static electricity, it is necessary to protect the device from being damaged by the static electricity when using it.
4. Shenzhen Minos reserves the right to make changes in this specification sheet and is subject to change without prior notice.

CONTACT:

深圳市迈诺斯科技有限公司（总部）

地址：深圳市福田区华富街道田面社区深南中路4026号田面城市大厦22B-22C

邮编：518025

电话：0755-83273777