Specification Sheet for Approved

Customer Name:	
Customer Part No.:	
Ceaiya Part No:	CCM4532 Series
Spec No:	C4532

【For Customer Approval Only】

If you Approval, Please Stamp

[RoHS Compliant Parts **]**

Approved By	Checked By	Prepared By
李庆辉	苏惠峰	劳水花

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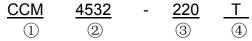
[Version of Changed Record]

Rev.	Effective Date	Changed Contents	Change Reasons	Approved By
A 0	2024-6-25	New release	1	Li qing hui

1. Features

- 1) High common mode impedance at high frequency effects excellent noise suppression perfomance.
- 2) CCM4532 series reealizes small size and low profile. 4.5*3.2*2.6mm
- 3) 100% Leas (Pb) & Halogen-Free and RoHs compliant.

2. Product Description and Identification (Part Number)



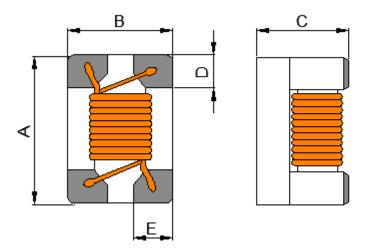
- ① Series
- 2 Dimension
- ③ Inductance 220=22uH
- 4 Taping and Reel

3. Shape and Dimensions (Unit:mm)

Dimensions and recommended PCB pattern for reflow soldering, please see Fig4-1 and Table4-1

Shape and Dimensions:

Recommended pad:



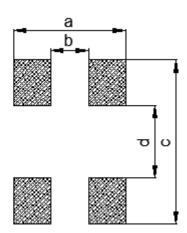


Fig4-1.

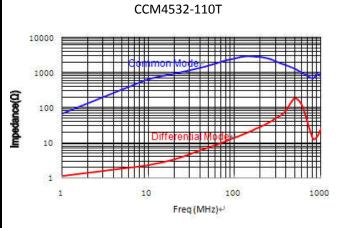
Table 4-1.

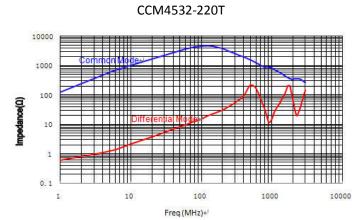
Α	В	С	D	Е	а	b	С	d
4.5±0.2	3.2±0.2	2.6±0.2	1.0 Ref	1.2 Ref	3.6 Ref	0.4 Ref	4.9 Ref	2.1 Ref

4. Electrical Characteristics

Part Number	Common mode Impedance(Ω) At 10MHz		Inductance(uH) +50%/-30% One winding	DC Resistance (Ω)Max.	Rated Current (mA)	Rated Volt. (Vdc)	IR (MΩ) Min.
	Min	Тур	One winding	(S2)IVIAX.	(111/4)	(vuc)	IVIII I.
CCM4532-110T	300	650	11 (@ 100KHz/0.1V)	0.5	360	50	10
CCM4532-220T	600	1000	22 (@ 100KHz/0.1V)	0.8	310	50	10
CCM4532-510T	1000	2900	51 (@ 1MHz/0.1V)	1.0	230	50	10
CCM4532-101T	2000	4800	100 (@ 1MHz/0.1V)	2.0	200	50	10

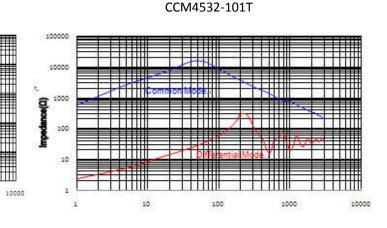
- a. Impedance: Keysight E4982A or equivalent.
- b. Inductance: METER 11050 or equivalent.
- c. DCR:Agilent HIOKI3540 or equivalent.
- d. IR: 4339 or equivalent.
- e. Measuring curcuits 2line and Frequency vs impedance curve

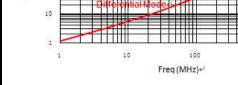




CCM4532-510T

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5. Material List

100000

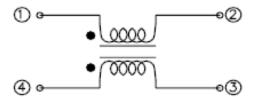
10000

1000

100

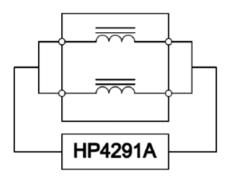
No	Part	Material
1	WIRE	Copper Wire
2	Core	Ferrite
3	Glue	Ероху

6. Schematic Diagram

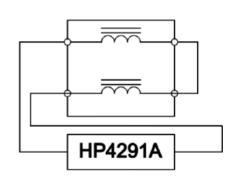


7. Measuring Circuits 2 line

Common mode



Differential mode



8. Reliability and Test Coondition

Item	Performance		Test	Condition					
Operating temperature	-40°C~+125°C (Including self - temperate	ture rise	:						
Storage temperature	-40°C~+125°C (on board)								
Electrical Performance Test									
L(common mode) Agilent: METER 11050 c									
DCR	Refer to standard electrical characteristi	cs list.	Agilen	t :HIOKI3540 or equivalent					
I.R.		Agilent 4339 or equivalent							
Temperature Rise Test	Rated Current < 1A △T 20°C Max.	1. Applied the allowed DC current.							
	Rated Current ≥ 1A △T 40°C Max.		2.Temperature measured by digital						
			surface thermometer.						
Reliability Test									
	Appearance: No damage.	Preeco	nditioning	g: Run through IR reflow for 2 times.					
	Inductance: within ±10% of initial value	(IPC/JE	DECJ-S	TD-020D Classiification Reflow					
	RDC: within ±15% of initial value and shall	Profiles	5)						
Life Test	not exceed the specification value	Temper	ature: 12	25±2°C					
Life lest		Applied c		d current: rated current					
		Duratio	n: 1000±	12hrs					
		Measur	ed at roc	om temperature after placing for					
		24±2hr	S						

Performance	Test	Cond	ition			
Appearance: No damage.	Preeconditioning: Run through IR reflow for 2 times. (IPC/JEDECJ-STD-020D Classification Reflow Profiles) Humidity: 85±2°C R.H. Temperature: 85±2°C Duration: 1000hrs Min. with 100% rated current. Measured at room temperature after placing for 24±2hrs Preconditioning: Run through IR reflow for 2 times. (IPC/JEDECJ-STD-020D Classification Reflow Profiles)					
Inductance: within ±10% of initial value RDC: within ±15% of initial value and shall not exceed the specification value	Step1: -4 Step2: 2 Step2: 1 Number	40±2°C 3 5±2°C ≤ 25±2°C of cycles	80±5min 0.5min 30±5min s: 500			ŕ
	Oscillation Frequency: 10~2K~10Hz for 20 minutes Equipment: Vibration checker Total Amplitude: 1.52mm ±10% Testing Time: 12 hours (20 minutes, 12 cycles each of 3 oorientations).					
Appearance: No damage. Inductance: within ±10% of initial value	Туре	Peak value (g's)	Duration	on(D)	Wave form	Velocity Change (Vi) ft/sec
RDC: within ±15% of initial value and shall not exceed the specification value	SMD Lead	50 50			Half-sine Half-sine	11.3
More than 95% of the terminal electrode should be covered with solder	Preheat: 150°C, 60sec. Solder: Sn99%, Ag0.3%, Cu0.7% Temperature: 245±5°C Flux for lead free: Rosin. 9.5% Dip time: 4 ± 1sec. Death: completely cover the termination					
	Depth: c	ompletel	y cover the	e termina	ation	
			Time(s)	ramp/	immersion	Number of heat cycles
	260 ±5 (solder temp) 10±1 25mm/s ± 6mm/s 1					1
Appearance: No damage. Inductance: within ±10% of initial value RDC: within ±15% of initial value and shall not exceed the specification value	Preconditioning: Run through IR reflow for 2 times. (IPC/JEDECJ-STD-020D Classification Reflow Profiles) With the component mounted on a PCB with the device to be tested, apply a force(>0805: 1kg, <=0805:0.5kg) to the side of a device being tested. This force shall be applied for 60+1 a shock to the component being tested.					
	Appearance: No damage. Inductance: within ±10% of initial value RDC: within ±15% of initial value and shall not exceed the specification value Appearance: No damage. Inductance: within ±10% of initial value RDC: within ±15% of initial value and shall not exceed the specification value More than 95% of the terminal electrode should be covered with solder Appearance: No damage. Inductance: within ±10% of initial value RDC: within ±15% of initial value and shall not	Appearance: No damage. Inductance: within ±10% of initial value RDC: within ±15% of initial value and shall not exceed the specification value Appearance: No damage. Inductance: within ±10% of initial value and shall not exceed the specification value Appearance: No damage. Inductance: within ±10% of initial value RDC: within ±15% of initial value and shall not exceed the specification value Type SMD Lead Preheat Solder: 3 Temper Flux for Dip time Depth: composition of the terminal electrode should be covered with solder Appearance: No damage. Inductance: within ±10% of initial value RDC: within ±15% of initial value and shall not exceed the specification value Appearance: No damage. Inductance: within ±10% of initial value RDC: within ±15% of initial value and shall not exceed the specification value	Appearance: No damage. Inductance: within ±10% of initial value RDC: within ±15% of initial value and shall not exceed the specification value Appearance: No damage. Appearance: No damage. Inductance: within ±10% of initial value and shall not exceed the specification value Appearance: No damage. Inductance: within ±15% of initial value RDC: within ±15% of initial value and shall not exceed the specification value Appearance: No damage. More than 95% of the terminal electrode should be covered with solder Appearance: No damage. Inductance: within ±15% of initial value and shall not exceed the specification value Appearance: No damage. Inductance: within ±15% of initial value and shall not exceed the specification value Appearance: No damage. Inductance: within ±10% of initial value and shall not exceed the specification value Appearance: No damage. Inductance: within ±15% of initial value and shall not exceed the specification value Appearance: No damage. Inductance: within ±10% of initial value and shall not exceed the specification value Appearance: No damage. Inductance: within ±10% of initial value and shall not exceed the specification value Appearance: No damage. Inductance: within ±10% of initial value and shall not exceed the specification value	Appearance: No damage. Inductance: within ±10% of initial value and shall not exceed the specification value Appearance: No damage. Appearance: No damage. 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Duration: 1000hrs Min, with 100% rated current. Measured at room temperature after placing for Preconditioning: Run through IR reflow for 2 tim (IPC/JEDECJ-STD-020D Classification Reflow I Measured at room temperature after placing for Preconditioning: Run through IR reflow for 2 tim (IPC/JEDECJ-STD-020D Classification Reflow I Stept: 40±2° 30±5min Step2: 155±2° C 30.5min Step2: 155±2° C 30.

9. Soldering and Mounting

9-1 Soldering

Mildly activated rosin fluxes are preferred. terminations are suitable for all wave and re-flow soldering systms.

If hand soldering cannot be avoided, the preferred technique is the utilization of hot aiir soldering tools.

9-1.1 Solder re-flow:

Reecommended temperature profiles for re-flow soldering in Figure 1.

9-1.2 Soldering Iron (Figure 2):

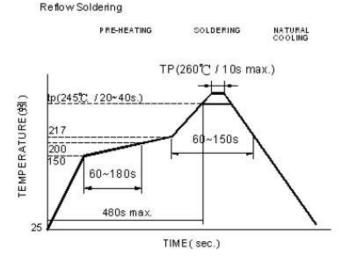
Products attachment with a soldering iron is discouraged due to the inherent process control limitations. In the event that a soldering iron must be employed the following precautions are recommended.

Preheat circuit and products to 150°C Never contact the ceramic with the iron tip

Use a 20 watt soldering iron with tip diameter of 1.0mm

355°C tip temperature (max)

Limit soldering time to 4~5 sec.



Reflow times: 3 times max.

Fig. 1

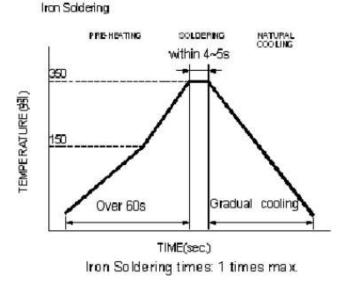
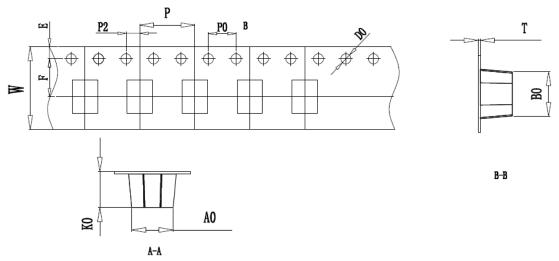


Fig.2

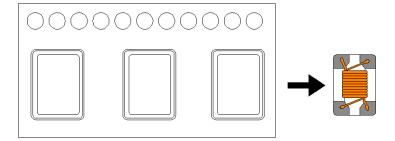
10.Packaging and Marking:

10-1. Carrier Tape Dimensions:

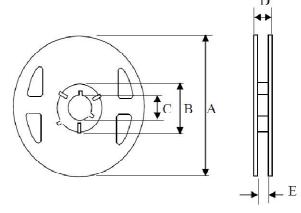


ITEM	W	A0	В0	K0	Р	P0	P2	D0	Т
DIM	12	3.75	4.85	3.0	8.0	4.00	2.00	1.5	0.35
TOLE	±0.3	±0.1	±0.1	±0.1	±0.1	±0.1	±0.1	±0.1	±0.05

10-2. Taping Dimensions:







Туре	Α	В	С	D	E
12mm	330	60±0.8	13±0.4	16	12.5

10-4. Packaging Quantity:

2.5KPCS/ Reel