

2025 | 芯祥科技产品手册

Product Selection Guide





公司简介 COMPANY PROFILE

芯祥科技致力于为清洁能源提供领先的芯片和解决方案（The Mathematic IC Solution For Clean Energy），总部位于合肥高新区，在上海、北京等地设有研发中心和办公室。

公司专注于工业和车规级BMS芯片，电源芯片及可编程模拟芯片的研发与销售。产品可以广泛应用于新能源储能、汽车、电脑、服务器、工业、家电、消费类及电动工具等。芯祥科技创始团队来自于全球知名模拟芯片公司，平均具有逾18年半导体经验，背景覆盖产品定义，芯片研发及销售。



- ◆ 国家级高新技术企业
- ◆ 2024年IC风云榜“创业芯星奖”
- ◆ 安徽省2024年科技型中小企业
- ◆ 2024年度合肥高新区雏鹰企业
- ◆ 2023年创响中国安徽省创新创业大赛创新组第四名
- ◆ 2023年制造业重点领域补短板产品和关键技术攻关任务揭榜企业
- ◆ 合肥市集成电路重点产业链企业
- ◆ 2023年合肥高新区深科技企业

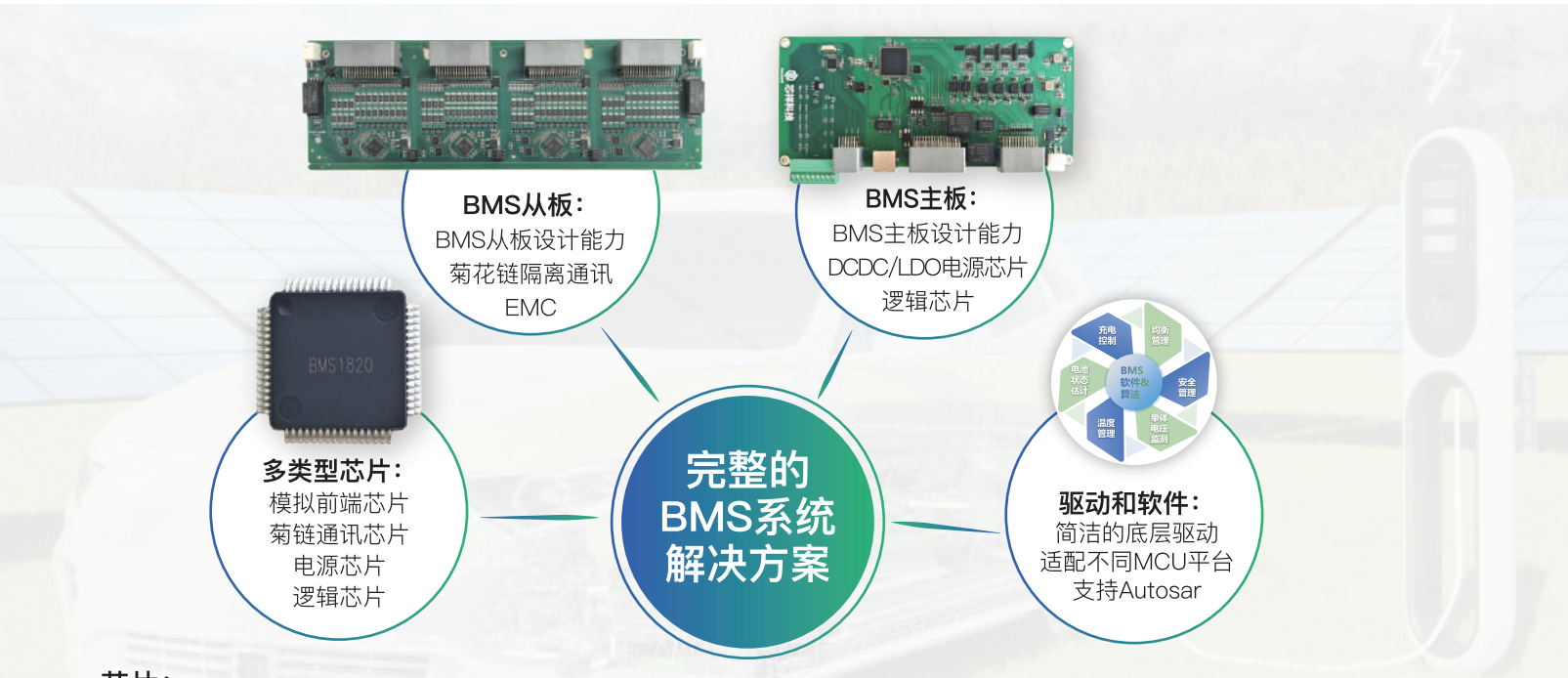
完整的BMS系统解决方案

Multicell Battery Stack Monitor and Balancer

Type number	Number of channels	Vin (min)(V)	Vin (max)(V)	Battery Chemistry	Communication Interface	Multi-IC Architecture	Description	Package
EMBMS1820	18	16	90	Multi-Chemistry: 0V to 5V	2.5Mbps (max) Emiso	Isolated Daisy Chained ICs	18-S AFE with Daisy Chain Interface	64-Lead LQFP_EP
EMBMS1217H	17	8.5	95	Multi-Chemistry: 0V to 5V	I2C, 400KHz, CRC-8	Via I2C Isolators	17-S AFE with high-side NMOS drivers	48-Lead LQFP_EP
EMBMS1217L	17	8.5	95	Multi-Chemistry: 0V to 5V	I2C, 400KHz, CRC-8	Via I2C Isolators	17-S AFE with low-side NMOS drivers	48-Lead LQFP_EP
EMBMS1205	5	8	40	Multi-Chemistry: 0V to 5V	I2C, 400KHz, CRC-8	Via I2C Isolators	5-S AFE with low-side NMOS drivers	20-Lead SSOP
EMBMS1208	8	6.5	55	Multi-Chemistry: 0V to 5V	I2C, 400KHz, CRC-8	Via I2C Isolators	8-S AFE with low-side NMOS drivers	24-Lead SSOP
EMBMS1226	26	8.5	110	Multi-Chemistry: 0V to 4.5V	SPI	Via SPI interface	26-S AFE	48-Lead LQFP_EP

Communication device

Type number	Number of Emiso channels	VDDS (min)(V)	VDDS (max)(V)	VDD (min)(V)	VDD (max)(V)	Data Rate (max) (bps)	Description	Package
EMS3010N	1	1.7	5.5	3.1	5.5	1M	Isolated Daisy Chain Interface	MSOP-16L, QFN3x3-16L
EMS3010F	1	1.7	5.5	3.1	5.5	2.5M	Isolated Daisy Chain Interface	MSOP-16L, QFN3x3-16L



芯片:

- 模拟前端AFE芯片: EMBMS1820/1226/1217H/1217L/1208/1205、分别为18/26/17/17/8/5串AFE芯片，覆盖多个应用场景
- 菊花链通讯芯片: 低功耗隔离菊花链桥接芯片，实现SPI与EMiso通讯方式的转换，传输速率分1Mbps和2.5Mbps两类
- 电源芯片: 宽输入高压DCDC/LDO，包括28V, 40V, 60V, 100V等
- 全系列逻辑芯片: EM74HC系列（5V逻辑），EM74LVC系列（3.3V逻辑），EMHEF系列（18V逻辑）

BMS系统: 系统团队具有丰富的车载/储能BMS量产经验，可提供准量产版的车规/储能BMS参考设计，给与客户硬件开发支持

BMS软件和算法: 简洁的底层驱动，可适配不同MCU平台。参考算法，可协助客户做应用层软件开发

逻辑芯片

Mini-Logic Packages (≤10 pins) Logic - Buffers / Inverters

Type number	Description	Features				Package								
		V _{cc} (V)	I _o (mA)	t _{pd} (ns)	T _{amb} (°C)	SOT353	SOT363	SOT23-5L	SOT23-6L	SOT553	SOT563	DFN1×1.45-6L	DFN1×1-6L	DFN0.8×0.8-4L
EM74AHC1G04	Single inverter	2.0-5.5	+/- 8	4.9	-40~125	•		•		•				
EM74AHC1G06	Inverter with open-drain output	2.0-5.5	8	4.9	-40~125	•		•		•				
EM74AHC1G07	Buffer with open-drain output	2.0-5.5	8	4.9	-40~125	•		•		•				
EM74AHC1G14	Single inverter; Schmitt-trigger	2.0-5.5	+/- 8	6.5	-40~125	•		•		•				
EM74AHC1G16	Single buffer	2.0-5.5	+/- 8	4.9	-40~125	•		•		•				
EM74AHC1G17	Single Schmitt trigger buffer	2.0-5.5	+/- 8	6.5	-40~125	•		•		•				
EM74AHC1G34	Single buffer	2.0-5.5	+/- 8	4.9	-40~125	•		•		•				
EM74AHC1G125	Single buffer/line driver (3-state)	2.0-5.5	+/- 8	5.6	-40~125	•		•		•				
EM74AHC1G126	Single buffer/line driver (3-state)	2.0-5.5	+/- 8	5.6	-40~125	•		•		•				
EM74AHC1G240	Single inverting buffer/line driver (3-state)	2.0-5.5	+/- 8	5.6	-40~125	•		•		•				
EM74AHC2G04	Dual inverter	2.0-5.5	+/- 8	4.9	-40~125		•		•		•			
EM74AHC2G06	Inverters with open-drain outputs	2.0-5.5	8	4.9	-40~125		•		•		•			
EM74AHC2G07	Buffers with open-drain outputs	2.0-5.5	8	4.9	-40~125		•		•		•			
EM74AHC2G14	Dual inverter; Schmitt-trigger	2.0-5.5	+/- 8	6.5	-40~125		•		•		•			
EM74AHC2G16	Dual buffer gate	2.0-5.5	+/- 8	4.9	-40~125		•		•		•			
EM74AHC2G17	Dual buffer; Schmitt-trigger	2.0-5.5	+/- 8	6.5	-40~125		•		•		•			
EM74AHC2G34	Dual buffer gate	2.0-5.5	+/- 8	4.9	-40~125		•		•		•			
EM74AHCT1G04	Single inverter; TTL-enabled	4.5-5.5	+/- 8	4.8	-40~125	•		•		•				
EM74AHCT1G06	Inverter with open-drain output; TTL-enabled	4.5-5.5	8	4.8	-40~125	•		•		•				
EM74AHCT1G07	Buffer with open-drain output; TTL-enabled	4.5-5.5	8	4.8	-40~125	•		•		•				
EM74AHCT1G14	Single inverter; Schmitt-trigger; TTL-enabled	4.5-5.5	+/- 8	8.7	-40~125	•		•		•				
EM74AHCT1G16	Single buffer; TTL-enabled	4.5-5.5	+/- 8	4.8	-40~125	•		•		•				
EM74AHCT1G17	Single Schmitt trigger buffer; TTL-enabled	4.5-5.5	+/- 8	8.7	-40~125	•		•		•				
EM74AHCT1G34	Single buffer; TTL-enabled	4.5-5.5	+/- 8	4.8	-40~125	•		•		•				
EM74AHCT1G125	Single buffer/line driver; TTL-enabled (3-state)	4.5-5.5	+/- 8	5.8	-40~125	•		•		•				
EM74AHCT1G126	Single buffer/line driver; TTL-enabled (3-state)	4.5-5.5	+/- 8	5.8	-40~125	•		•		•				
EM74AHCT1G240	Single inverting buffer/line driver; TTL-enabled (3-state)	4.5-5.5	+/- 8	5.8	-40~125	•		•		•				
EM74AHCT2G04	Dual inverter; TTL-enabled	4.5-5.5	+/- 8	4.8	-40~125		•		•		•			
EM74AHCT2G06	Inverters with open-drain outputs; TTL-enabled	4.5-5.5	8	4.8	-40~125		•		•		•			
EM74AHCT2G07	Buffers with open-drain outputs; TTL-enabled	4.5-5.5	8	4.8	-40~125		•		•		•			
EM74AHCT2G14	Dual inverter; Schmitt-trigger; TTL-enabled	4.5-5.5	+/- 8	8.7	-40~125		•		•		•			
EM74AHCT2G16	Dual buffer gate; TTL-enabled	4.5-5.5	+/- 8	4.8	-40~125		•		•		•			
EM74AHCT2G17	Dual buffer; Schmitt-trigger; TTL-enabled	4.5-5.5	+/- 8	8.7	-40~125		•		•		•			
EM74AHCT2G34	Dual buffer gate; TTL-enabled	4.5-5.5	+/- 8	4.8	-40~125		•		•		•			
EM74LVC1GU04	Single inverter; Unbuffered	1.65-5.5	+/- 32	1.3	-40~125	•		•				•	•	•
EM74LVC1G04	Single inverter	1.65-5.5	+/- 32	2.5	-40~125	•		•				•	•	•
EM74LVC1G06	Inverter with open-drain output	1.65-5.5	32	2.5	-40~125	•		•				•	•	•
EM74LVC1G07	Buffer with open-drain output	1.65-5.5	32	2.5	-40~125	•		•				•	•	•
EM74LVC1G14	Single inverter; Schmitt-trigger	1.65-5.5	+/- 32	4.3	-40~125	•		•				•	•	•

逻辑芯片

Logic - Buffers / Inverters

Type number	Description	Features				Package								
		V _{cc} (V)	I _o (mA)	t _{pd} (ns)	T _{amb} (°C)	SOT353	SOT363	SOT23-5L	SOT23-6L	SOT553	SOT563	DFN1 x1.45-6L	DFN1 x1-6L	DFN0.8x0.8-4L
EM74LVC1G16	Single buffer	1.65-5.5	+/- 32	2.5	-40~125	•		•				•	•	•
EM74LVC1G17	Single Schmitt trigger buffer	1.65-5.5	+/- 32	4.3	-40~125	•		•				•	•	•
EM74LVC1G34	Single buffer	1.65-5.5	+/- 32	2.5	-40~125	•		•				•	•	•
EM74LVC1G125	Single buffer/line driver (3-state)	1.65-5.5	+/- 32	3.6	-40~125	•		•				•	•	•
EM74LVC1G126	Single buffer/line driver (3-state)	1.65-5.5	+/- 32	3.6	-40~125	•		•				•	•	•
EM74LVC1G240	Single inverting buffer/line driver (3-state)	1.65-5.5	+/- 32	3.6	-40~125	•		•				•	•	•
EM74LVC2GU04	Dual inverter; Unbuffered	1.65-5.5	+/- 32	1.3	-40~125		•		•			•	•	
EM74LVC2G04	Dual inverter	1.65-5.5	+/- 32	2.5	-40~125		•		•			•	•	
EM74LVC2G06	Inverters with open-drain outputs	1.65-5.5	32	2.5	-40~125		•		•			•	•	
EM74LVC2G07	Buffers with open-drain outputs	1.65-5.5	32	2.5	-40~125		•		•			•	•	
EM74LVC2G14	Dual inverter; Schmitt-trigger	1.65-5.5	+/- 32	4.3	-40~125		•		•			•	•	
EM74LVC2G16	Dual buffer gate	1.65-5.5	+/- 32	2.5	-40~125		•		•			•	•	
EM74LVC2G17	Dual buffer; Schmitt-trigger	1.65-5.5	+/- 32	4.3	-40~125		•		•			•	•	
EM74LVC2G34	Dual buffer gate	1.65-5.5	+/- 32	2.5	-40~125		•		•			•	•	
EM74LV1T04	Single supply translating inverter	1.6-5.5	+/- 8	8	-40~125	•		•				•	•	•
EM74LV1T34	Single supply translating buffer	1.6-5.5	+/- 8	8	-40~125	•		•				•	•	•
EM74LV1T125	Single supply translating buffer/line driver (3-state)	1.6-5.5	+/- 8	8	-40~125	•		•				•	•	•
EM74LV1T126	Single supply translating buffer/line driver (3-state)	1.6-5.5	+/- 8	8	-40~125	•		•				•	•	•

Logic - Schmitt-trigger IC's

Type number	Description	Features				Package								
		V _{cc} (V)	I _o (mA)	t _{pd} (ns)	T _{amb} (°C)	SOT353	SOT363	SOT23-5L	SOT23-6L	SOT553	SOT563	DFN1 x1.45-6L	DFN1 x1-6L	DFN0.8x0.8-4L
EM74AHC1G14	Single inverter; Schmitt-trigger	2.0-5.5	+/- 8	6.5	-40~125	•		•		•				
EM74AHC1G17	Single Schmitt trigger buffer	2.0-5.5	+/- 8	6.5	-40~125	•		•		•				
EM74AHC2G14	Dual inverter; Schmitt-trigger	2.0-5.5	+/- 8	6.5	-40~125		•		•		•			
EM74AHC2G17	Dual buffer; Schmitt-trigger	2.0-5.5	+/- 8	6.5	-40~125		•		•		•			
EM74AHCT1G14	Single inverter; Schmitt-trigger; TTL-enabled	4.5-5.5	+/- 8	8.7	-40~125	•		•		•				
EM74AHCT1G17	Single Schmitt trigger buffer; TTL-enabled	4.5-5.5	+/- 8	8.7	-40~125	•		•		•				
EM74AHCT2G14	Dual inverter; Schmitt-trigger; TTL-enabled	4.5-5.5	+/- 8	8.7	-40~125		•		•		•			
EM74AHCT2G17	Dual buffer; Schmitt-trigger; TTL-enabled	4.5-5.5	+/- 8	8.7	-40~125		•		•		•			
EM74LVC1G14	Single inverter; Schmitt-trigger	1.65-5.5	+/- 32	4.3	-40~125	•		•				•	•	•
EM74LVC1G17	Single Schmitt trigger buffer	1.65-5.5	+/- 32	4.3	-40~125	•		•				•	•	•
EM74LVC2G14	Dual inverter; Schmitt-trigger	1.65-5.5	+/- 32	4.3	-40~125		•		•			•	•	
EM74LVC2G17	Dual buffer; Schmitt-trigger	1.65-5.5	+/- 32	4.3	-40~125		•		•			•	•	
EM74LVC1G132	Single 2-Input NAND Gate With Schmitt-Trigger Inputs	1.65-5.5	+/- 32	4.3	-40~125	•		•				•	•	•

逻辑芯片

Logic - Gates

Type number	Description	Features				Package								
		V _{cc} (V)	I _o (mA)	t _{pd} (ns)	T _{amb} (°C)	SOT353	SOT363	SOT23-5L	SOT23-6L	SOT553	SOT563	DFN1×1.45-6L	DFN1×1-6L	DFN0.8×0.8-4L
EM74AHC1G00	Single 2-input NAND gate	2.0-5.5	+/- 8	4	-40~125	•		•		•				
EM74AHC1G02	Single 2-input NOR gate	2.0-5.5	+/- 8	4	-40~125	•		•		•				
EM74AHC1G08	Single 2-input AND gate	2.0-5.5	+/- 8	4	-40~125	•		•		•				
EM74AHC1G09	2-input AND gate; open drain	2.0-5.5	8	4	-40~125	•		•		•				
EM74AHC1G10	Single 3-input NAND gate	2.0-5.5	+/- 8	4	-40~125		•		•		•			
EM74AHC1G11	Single 3-input AND gate	2.0-5.5	+/- 8	4	-40~125		•		•		•			
EM74AHC1G27	Single 3-input NOR gate	2.0-5.5	+/- 8	4	-40~125		•		•		•			
EM74AHC1G32	Single 2-input OR gate	2.0-5.5	+/- 8	4	-40~125	•		•		•				
EM74AHC1G38	2-input NAND gate; open drain	2.0-5.5	8	4	-40~125	•		•		•				
EM74AHC1G332	Single 3-input OR gate	2.0-5.5	+/- 8	4	-40~125		•		•		•			
EM74AHCT1G00	Single 2-input NAND gate; TTL-enabled	4.5-5.5	+/- 8	3.1	-40~125	•		•		•				
EM74AHCT1G02	Single 2-input NOR gate; TTL-enabled	4.5-5.5	+/- 8	3.1	-40~125	•		•		•				
EM74AHCT1G08	Single 2-input AND gate; TTL-enabled	4.5-5.5	+/- 8	3.1	-40~125	•		•		•				
EM74AHCT1G09	2-input AND gate; open drain; TTL-enabled	4.5-5.5	8	3.1	-40~125	•		•		•				
EM74AHCT1G10	Single 3-input NAND gate; TTL-enabled	4.5-5.5	+/- 8	3.1	-40~125		•		•		•			
EM74AHCT1G11	Single 3-input AND gate; TTL-enabled	4.5-5.5	+/- 8	3.1	-40~125		•		•		•			
EM74AHCT1G27	Single 3-input NOR gate; TTL-enabled	4.5-5.5	+/- 8	3.1	-40~125		•		•		•			
EM74AHCT1G32	Single 2-input OR gate; TTL-enabled	4.5-5.5	+/- 8	3.1	-40~125	•		•		•				
EM74AHCT1G38	2-input NAND gate; open drain; TTL-enabled	4.5-5.5	8	3.1	-40~125	•		•		•				
EM74AHCT1G332	Single 3-input OR gate; TTL-enabled	4.5-5.5	+/- 8	3.1	-40~125		•		•		•			
EM74LVC1G00	Single 2-input NAND gate	1.65-5.5	+/- 32	3.9	-40~125	•		•				•	•	•
EM74LVC1G02	Single 2-input NOR gate	1.65-5.5	+/- 32	3.9	-40~125	•		•				•	•	•
EM74LVC1G08	Single 2-input AND gate	1.65-5.5	+/- 32	3.9	-40~125	•		•				•	•	•
EM74LVC1G09	2-input AND gate; open drain	1.65-5.5	32	3.9	-40~125	•		•				•	•	•
EM74LVC1G10	Single 3-input NAND gate	1.65-5.5	+/- 32	3.9	-40~125		•		•			•	•	
EM74LVC1G11	Single 3-input AND gate	1.65-5.5	+/- 32	3.9	-40~125		•		•			•	•	
EM74LVC1G27	Single 3-input NOR gate	1.65-5.5	+/- 32	3.9	-40~125		•		•			•	•	
EM74LVC1G32	Single 2-input OR gate	1.65-5.5	+/- 32	3.9	-40~125	•		•				•	•	•
EM74LVC1G38	2-input NAND gate; open drain	1.65-5.5	32	3.9	-40~125	•		•				•	•	•
EM74LVC1G57	Low-power configurable multiple function gate	1.65-5.5	+/- 32	3.9	-40~125		•		•			•	•	
EM74LVC1G58	Low-power configurable multiple function gate	1.65-5.5	+/- 32	3.9	-40~125		•		•			•	•	
EM74LVC1G86	2-input EXCLUSIVE-OR gate	1.65-5.5	+/- 32	3.9	-40~125	•		•				•	•	•
EM74LVC1G87	2-input EXCLUSIVE-NOR gate	1.65-5.5	+/- 32	3.9	-40~125	•		•				•	•	•
EM74LVC1G97	Low-power configurable multiple function gate	1.65-5.5	+/- 32	3.9	-40~125		•		•			•	•	
EM74LVC1G98	Low-power configurable multiple function gate	1.65-5.5	+/- 32	3.9	-40~125		•		•			•	•	
EM74LVC1G132	Single 2-Input NAND Gate With Schmitt-Trigger Inputs	1.65-5.5	+/- 32	4.3	-40~125	•		•				•	•	•
EM74LVC1G332	Single 3-input OR gate	1.65-5.5	+/- 32	3.9	-40~125		•		•			•	•	
EM74LVC1G386	3-input EXCLUSIVE-OR gate	1.65-5.5	+/- 32	3.9	-40~125		•		•			•	•	
EM74LVC1G387	3-input EXCLUSIVE-NOR gate	1.65-5.5	+/- 32	3.9	-40~125		•		•			•	•	
EM74LV1T00	Single supply 2-input translating NAND gate	1.6-5.5	+/- 8	8	-40~125	•		•				•	•	•
EM74LV1T02	Single supply 2-input translating NOR gate	1.6-5.5	+/- 8	8	-40~125	•		•				•	•	•
EM74LV1T08	Single supply 2-input translating AND gate	1.6-5.5	+/- 8	8	-40~125	•		•				•	•	•
EM74LV1T32	Single supply 2-input translating OR gate	1.6-5.5	+/- 8	8	-40~125	•		•				•	•	•
EM74LV1T86	Single supply 2-input translating X-OR gate	1.6-5.5	+/- 8	7	-40~125	•		•				•	•	•
EM74LV1T87	Single supply 2-input translating X-NOR gate	1.6-5.5	+/- 8	7	-40~125	•		•				•	•	•

逻辑芯片

Standard Packages (>10 pins)

Logic - Buffer / Inverters

Type number	Description	Features				Package					
		V _{cc} (V)	I _O (mA)	t _{pd} (ns)	T _{amb} (°C)	SOP-14L	TSSOP-14L	SOP-16L	TSSOP-16L	SOP-20L	TSSOP-20L
EM74AHC240	Octal inverter/line driver (3-state)	2.0-5.5	+/- 8	4.5	-40~125					•	•
EM74AHC244	Octal buffer/line driver (3-state)	2.0-5.5	+/- 8	4.5	-40~125					•	•
EM74AHC540	Octal inverter/line driver (3-state)	2.0-5.5	+/- 8	4.5	-40~125					•	•
EM74AHC541	Octal buffer/line driver (3-state)	2.0-5.5	+/- 8	4.5	-40~125					•	•
EM74HC04	Hex inverter	2.0-6.0	+/- 5.2	8.5	-40~125	•	•				
EM74HC05	Hex inverter; open-drain	2.0-6.0	5.2	8.5	-40~125	•	•				
EM74HC07	Hex buffer; open-drain	2.0-6.0	5.2	8.5	-40~125	•	•				
EM74HC14	Hex inverter; Schmitt trigger	2.0-6.0	+/- 5.2	8	-40~125	•	•				
EM74HC125	Quad buffer/line driver (3-state)	2.0-6.0	+/- 5.2	8.5	-40~125	•	•				
EM74HC126	Quad buffer/line driver (3-state)	2.0-6.0	+/- 5.2	8.5	-40~125	•	•				
EM74HC240	Octal inverter/line driver (3-state)	2.0-6.0	+/- 7.8	4.5	-40~125					•	•
EM74HC241	Octal buffer/line driver (3-state)	2.0-6.0	+/- 7.8	4.5	-40~125					•	•
EM74HC244	Octal buffer/line driver (3-state)	2.0-6.0	+/- 7.8	4.5	-40~125					•	•
EM74HC365	Hex buffer/line driver (3-state)	2.0-6.0	+/- 5.2	8.5	-40~125			•	•		
EM74HC366	Hex inverter/line driver (3-state)	2.0-6.0	+/- 5.2	8.5	-40~125			•	•		
EM74HC367	Hex buffer/line driver (3-state)	2.0-6.0	+/- 5.2	8.5	-40~125			•	•		
EM74HC368	Hex inverter/line driver (3-state)	2.0-6.0	+/- 5.2	8.5	-40~125			•	•		
EM74HC540	Octal inverter/line driver (3-state)	2.0-6.0	+/- 7.8	4.5	-40~125					•	•
EM74HC541	Octal buffer/line driver (3-state)	2.0-6.0	+/- 7.8	4.5	-40~125					•	•
EM74HC7541	Octal buffer/line driver; Schmitt trigger (3-state)	2.0-6.0	+/- 7.8	3.4	-40~125					•	•
EM74AHCT240	Octal inverter/line driver; TTL-enabled (3-state)	4.5-5.5	+/- 8	4.5	-40~125					•	•
EM74AHCT244	Octal buffer/line driver; TTL-enabled (3-state)	4.5-5.5	+/- 8	4.5	-40~125					•	•
EM74AHCT540	Octal inverter/line driver; TTL-enabled (3-state)	4.5-5.5	+/- 8	4.5	-40~125					•	•
EM74AHCT541	Octal buffer/line driver; TTL-enabled (3-state)	4.5-5.5	+/- 8	4.5	-40~125					•	•
EM74HCT04	Hex inverter; TTL-enabled	4.5-5.5	+/- 5.2	9.8	-40~125	•	•				
EM74HCT05	Hex inverter; open-drain; TTL-enabled	4.5-5.5	5.2	9.8	-40~125	•	•				
EM74HCT07	Hex buffer; open-drain; TTL-enabled	4.5-5.5	5.2	9.8	-40~125	•	•				
EM74HCT14	Hex inverter; Schmitt trigger; TTL-enabled	4.5-5.5	+/- 5.2	11	-40~125	•	•				
EM74HCT125	Quad buffer/line driver; TTL-enabled (3-state)	4.5-5.5	+/- 5.2	11	-40~125	•	•				
EM74HCT126	Quad buffer/line driver; TTL-enabled (3-state)	4.5-5.5	+/- 5.2	11	-40~125	•	•				
EM74HCT240	Octal inverter/line driver; TTL-enabled (3-state)	4.5-5.5	+/- 7.8	4.5	-40~125					•	•
EM74HCT241	Octal buffer/line driver; TTL-enabled (3-state)	4.5-5.5	+/- 7.8	4.5	-40~125					•	•
EM74HCT244	Octal buffer/line driver; TTL-enabled (3-state)	4.5-5.5	+/- 7.8	4.5	-40~125					•	•
EM74HCT365	Hex buffer/line driver; TTL-enabled (3-state)	4.5-5.5	+/- 5.2	11	-40~125			•	•		
EM74HCT366	Hex inverter/line driver; TTL-enabled (3-state)	4.5-5.5	+/- 5.2	11	-40~125			•	•		
EM74HCT367	Hex buffer/line driver; TTL-enabled (3-state)	4.5-5.5	+/- 5.2	11	-40~125			•	•		
EM74HCT368	Hex inverter/line driver; TTL-enabled (3-state)	4.5-5.5	+/- 5.2	11	-40~125			•	•		
EM74HCT540	Octal inverter/line driver; TTL-enabled (3-state)	4.5-5.5	+/- 7.8	4.5	-40~125					•	•

Logic - Buffer / Inverters

Type number	Description	Features				Package					
		V _{cc} (V)	I _o (mA)	t _{pd} (ns)	T _{amb} (°C)	SOP-14L	TSSOP-14L	SOP-16L	TSSOP-16L	SOP-20L	TSSOP-20L
EM74HCT541	Octal buffer/line driver; TTL-enabled (3-state)	4.5-5.5	+/- 7.8	4.5	-40~125					•	•
EM74HCT7540	Octal buffer/line driver Schmitt trigger; TTL-enabled (3-state)	4.5-5.5	+/- 7.8	3.4	-40~125					•	•
EM74HCT7541	Octal buffer/line driver Schmitt trigger; TTL-enabled (3-state)	4.5-5.5	+/- 7.8	3.4	-40~125					•	•
EM74HCS04	Hex inverter; Schmitt trigger	2.0-6.0	+/- 5.2	8	-40~125	•	•				
EM74HCS05	Hex inverter; open-drain; Schmitt trigger	2.0-6.0	5.2	8	-40~125	•	•				
EM74HCS07	Hex buffer; open-drain; Schmitt trigger	2.0-6.0	5.2	8	-40~125	•	•				
EM74HCS125	Quad buffer/line driver (3-state); Schmitt trigger	2.0-6.0	+/- 5.2	8	-40~125	•	•				
EM74HCS126	Quad buffer/line driver (3-state); Schmitt trigger	2.0-6.0	+/- 5.2	8	-40~125	•	•				
EM74HCS244	Octal Buffers and Line Drivers with Schmitt-Trigger Inputs and 3-State Outputs	2.0-6.0	+/- 7.8	3.4	-40~125					•	•
EM74HCS365	Hex buffer/line driver (3-state); Schmitt trigger	2.0-6.0	+/- 5.2	8	-40~125			•	•		
EM74HCS366	Hex inverter/line driver (3-state); Schmitt trigger	2.0-6.0	+/- 5.2	8	-40~125			•	•		
EM74HCS367	Hex buffer/line driver (3-state); Schmitt trigger	2.0-6.0	+/- 5.2	8	-40~125			•	•		
EM74HCS368	Hex inverter/line driver (3-state); Schmitt trigger	2.0-6.0	+/- 5.2	8	-40~125			•	•		
EM74LVC04A	Hex inverter	1.65-5.5	+/- 32	4.5	-40~125	•	•				
EM74LVC06A	Hex inverter; open-drain	1.65-5.5	32	4.5	-40~125	•	•				
EM74LVC07A	Hex buffer with open-drain outputs	1.65-5.5	32	4.5	-40~125	•	•				
EM74LVC14A	Hex inverting Schmitt trigger with 5 V tolerant input	1.65-5.5	+/- 32	5	-40~125	•	•				
EM74LVC125A	Quad buffer/line driver with 5 V tolerant input/outputs; 3-state	1.65-5.5	+/- 32	3.5	-40~125	•	•				
EM74LVC126A	Quad buffer/line driver with 5 V tolerant input/outputs; 3-state	1.65-5.5	+/- 32	3.5	-40~125	•	•				
EM74LVC240A	Octal inverter/line driver with 5 V tolerant inputs/outputs; 3-state	1.2-3.6	+/- 24	4.5	-40~125					•	•
EM74LVC244A	Octal buffer/line driver (3-state)	1.2-3.6	+/- 24	4.5	-40~125					•	•
EM74LVC540A	Octal buffer/line driver with 5 V tolerant inputs/outputs; 3-state	1.2-3.6	+/- 24	4.5	-40~125					•	•
EM74LVC541A	Octal buffer/line driver with 5 V tolerant inputs/outputs; 3-state	1.2-3.6	+/- 24	4.5	-40~125					•	•
EM74LVCH244A	Octal buffer/line driver with bus hold (3-state)	1.2-3.6	+/- 24	4.5	-40~125					•	•

Logic - Transceivers

Type number	Description	Features				Package	
		V _{cc} (V)	I _o (mA)	t _{pd} (ns)	T _{amb} (°C)	SOP-20L	TSSOP-20L
EM74AHC245	Octal transceiver (3-state)	2.0-5.5	+/- 8	4.5	-40~125	•	•
EM74HC245	Octal transceiver (3-state)	2.0-6.0	+/- 7.8	4.5	-40~125	•	•
EM74HC640	Octal transceiver (3-state)	2.0-6.0	+/- 7.8	4.5	-40~125	•	•
EM74AHCT245	Octal transceiver (3-state)	4.5-5.5	+/- 8	4.5	-40~125	•	•
EM74HCT245	Octal transceiver; TTL-enabled (3-state)	4.5-5.5	+/- 7.8	4.5	-40~125	•	•
EM74HCT640	Octal transceiver; TTL-enabled (3-state)	4.5-5.5	+/- 7.8	4.5	-40~125	•	•
EM74HCS245	Octal transceiver Schmitt trigger (3-state)	2.0-6.0	+/- 7.8	3.4	-40~125	•	•
EM74LVC245A	Octal transceiver (3-state)	1.2-3.6	+/- 24	6.5	-40~125	•	•
EM74LVCH245A	Octal transceiver with bus hold (3-state)	1.2-3.6	+/- 24	6.5	-40~125	•	•

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Logic - Gates

Type number	Description	Features				Package	
		V _{cc} (V)	I _O (mA)	t _{pd} (ns)	T _{amb} (°C)	SOP-14L	TSSOP-14L
EM74HC00	Quad 2-input NAND gate	2.0-6.0	+/- 5.2	8.5	-40~125	•	•
EM74HC02	Quad 2-input NOR gate	2.0-6.0	+/- 5.2	8.5	-40~125	•	•
EM74HC03	Quad 2-input NAND gate; open-drain	2.0-6.0	5.2	8.5	-40~125	•	•
EM74HC08	Quad 2-input AND gate	2.0-6.0	+/- 5.2	8.5	-40~125	•	•
EM74HC10	Triple 3-input NAND gate	2.0-6.0	+/- 5.2	8.5	-40~125	•	•
EM74HC11	Triple 3-input AND gate	2.0-6.0	+/- 5.2	8.5	-40~125	•	•
EM74HC27	Triple 3-input NOR gate	2.0-6.0	+/- 5.2	8.5	-40~125	•	•
EM74HC32	Quad 2-input OR gate	2.0-6.0	+/- 5.2	8.5	-40~125	•	•
EM74HC86	Quad 2-input EXCLUSIVE-OR gate	2.0-6.0	+/- 5.2	8.5	-40~125	•	•
EM74HC132	Quad 2-input NAND Schmitt trigger	2.0-6.0	+/- 5.2	8	-40~125	•	•
EM74HC266	Quad 2-Input XNOR Gates with Open-Drain Outputs	2.0-6.0	5.2	8.5	-40~125	•	•
EM74HC4075	Triple 3-input OR gate	2.0-6.0	+/- 5.2	8.5	-40~125	•	•
EM74HC7001	Quad 2-Input AND Gates with Schmitt-Trigger Inputs	2.0-6.0	+/- 5.2	8	-40~125	•	•
EM74HC7002	Quad 2-Input NOR Gates with Schmitt-Trigger Inputs	2.0-6.0	+/- 5.2	8	-40~125	•	•
EM74HC7032	Quad 2-Input OR Gates with Schmitt-Trigger Inputs	2.0-6.0	+/- 5.2	8	-40~125	•	•
EM74HC7266	Quad 2-Input XNOR Gates	2.0-6.0	+/- 5.2	8.5	-40~125	•	•
EM74HCT00	Quad 2-input NAND gate; TTL-enabled	4.5-5.5	+/- 5.2	9.8	-40~125	•	•
EM74HCT02	Quad 2-input NOR gate; TTL-enabled	4.5-5.5	+/- 5.2	9.8	-40~125	•	•
EM74HCT03	Quad 2-input NAND gate; open-drain; TTL-enabled	4.5-5.5	5.2	9.8	-40~125	•	•
EM74HCT08	Quad 2-input AND gate; TTL-enabled	4.5-5.5	+/- 5.2	9.8	-40~125	•	•
EM74HCT10	Triple 3-input NAND gate; TTL-enabled	4.5-5.5	+/- 5.2	9.8	-40~125	•	•
EM74HCT11	Triple 3-input AND gate; TTL-enabled	4.5-5.5	+/- 5.2	9.8	-40~125	•	•
EM74HCT27	Triple 3-input NOR gate; TTL-enabled	4.5-5.5	+/- 5.2	9.8	-40~125	•	•
EM74HCT32	Quad 2-input OR gate; TTL-enabled	4.5-5.5	+/- 5.2	9.8	-40~125	•	•
EM74HCT86	Quad 2-input EXCLUSIVE-OR gate; TTL-enabled	4.5-5.5	+/- 5.2	9.8	-40~125	•	•
EM74HCT132	Quad 2-input NAND Schmitt trigger; TTL-enabled	4.5-5.5	+/- 5.2	8	-40~125	•	•
EM74HCT266	Quad 2-Input XNOR Gates with Open-Drain Outputs; TTL-enabled	4.5-5.5	+/- 5.2	9.8	-40~125	•	•
EM74HCT4075	Triple 3-input OR gate; TTL-enabled	4.5-5.5	+/- 5.2	9.8	-40~125	•	•
EM74HCT7001	Quad 2-Input AND Gates with Schmitt-Trigger Inputs; TTL-enabled	4.5-5.5	+/- 5.2	8	-40~125	•	•
EM74HCT7002	Quad 2-Input NOR Gates with Schmitt-Trigger Inputs; TTL-enabled	4.5-5.5	+/- 5.2	8	-40~125	•	•
EM74HCT7032	Quad 2-Input OR Gates with Schmitt-Trigger Inputs; TTL-enabled	4.5-5.5	+/- 5.2	8	-40~125	•	•
EM74HCT7266	Quad 2-Input XNOR Gates; TTL-enabled	4.5-5.5	+/- 5.2	9.8	-40~125	•	•
EM74HCS00	Quad 2-input NAND gate; Schmitt trigger	2.0-6.0	+/- 5.2	8	-40~125	•	•
EM74HCS02	Quad 2-input NOR gate; Schmitt trigger	2.0-6.0	+/- 5.2	8	-40~125	•	•
EM74HCS03	Quad 2-input NAND gate; open-drain; Schmitt trigger	2.0-6.0	5.2	8	-40~125	•	•
EM74HCS08	Quad 2-input AND gate; Schmitt trigger	2.0-6.0	+/- 5.2	8	-40~125	•	•
EM74HCS09	Quad 2-Input AND Gates with Open-Drain Outputs and SchmittTrigger Inputs	2.0-6.0	5.2	8	-40~125	•	•
EM74HCS10	Triple 3-input NAND gate; Schmitt trigger	2.0-6.0	+/- 5.2	8	-40~125	•	•
EM74HCS11	Triple 3-input AND gate; Schmitt trigger	2.0-6.0	+/- 5.2	8	-40~125	•	•

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Logic - Gates

Type number	Description	Features				Package	
		V _{cc} (V)	I _o (mA)	t _{pd} (ns)	T _{amb} (°C)	SOP-14L	TSSOP-14L
EM74HCS27	Triple 3-input NOR gate; Schmitt trigger	2.0-6.0	+/- 5.2	8	-40~125	•	•
EM74HCS32	Quad 2-input OR gate; Schmitt trigger	2.0-6.0	+/- 5.2	8	-40~125	•	•
EM74HCS86	Quad 2-input EXCLUSIVE-OR gate; Schmitt trigger	2.0-6.0	+/- 5.2	8	-40~125	•	•
EM74HCS266	Quad 2-Input XNOR Gates with Open-Drain Outputs; Schmitt trigger	2.0-6.0	5.2	8	-40~125	•	•
EM74HCS4075	Triple 3-input OR gate; Schmitt trigger	2.0-6.0	+/- 5.2	8	-40~125	•	•
EM74HCS7266	Quad 2-Input XNOR Gates; Schmitt trigger	2.0-6.0	+/- 5.2	8	-40~125	•	•
EM74LVC00A	Quad 2-input NAND gate	1.65-5.5	+/- 32	4.5	-40~125	•	•
EM74LVC02A	Quad 2-input NOR gate	1.65-5.5	+/- 32	4.5	-40~125	•	•
EM74LVC08A	Quad 2-input AND gate	1.65-5.5	+/- 32	4.5	-40~125	•	•
EM74LVC10A	Triple 3-input NAND gate	1.65-5.5	+/- 32	4.5	-40~125	•	•
EM74LVC11	Triple 3-input AND gate	1.65-5.5	+/- 32	4.5	-40~125	•	•
EM74LVC32A	Quad 2-input OR gate	1.65-5.5	+/- 32	4.5	-40~125	•	•
EM74LVC86A	Quad 2-input EXCLUSIVE-OR gate	1.65-5.5	+/- 32	4.5	-40~125	•	•
EM74LVC132A	Quad 2-input NAND Schmitt trigger	1.65-5.5	+/- 32	5	-40~125	•	•

Logic - Flip-flops

Type number	Description	Features				Package			
		V _{cc} (V)	I _o (mA)	t _{pd} (ns)	T _{amb} (°C)	SOP-14L	TSSOP-14L	SOP-16L	TSSOP-16L
EM74HC74	Dual D-type flip-flop with set and reset; positive-edge trigger; TTL enabled	2.0-6.0	+/- 5.2	8	-40~125	•	•		
EM74HCT74	Dual D-type flip-flop with set and reset; positive-edge trigger; TTL input	4.5-5.5	+/- 5.2	9.8	-40~125	•	•		
EM74HC174	Hex D-type flip-flop with reset; positive-edge trigger	2.0-6.0	+/- 5.2	7	-40~125			•	•
EM74HCT174	Octal D-type flip-flop; positive-edge trigger; TTL-enabled (3-state)	4.5-5.5	+/- 5.2	8	-40~125			•	•
EM74HC273	Octal D-type flip-flop with reset; positive-edge trigger	2.0-6.0	+/- 5.2	7	-40~125			•	•
EM74HCT273	Octal D-type flip-flop with reset; positive-edge trigger; TTL-enabled	4.5-5.5	+/- 5.2	8	-40~125			•	•
EM74HC374	Octal D-type flip-flop; positive-edge trigger (3-state)	2.0-6.0	+/- 5.2	7	-40~125			•	•
EM74HCT374	Octal D-type flip-flop; positive-edge trigger; TTL-enabled (3-state)	4.5-5.5	+/- 5.2	8	-40~125			•	•
EM74HC377	Octal D-type flip-flop with data enable; positive-edge trigger	2.0-6.0	+/- 5.2	7	-40~125			•	•
EM74HCT377	Octal D-type flip-flop with data enable; positive-edge trigger; TTL-enabled	4.5-5.5	+/- 5.2	8	-40~125			•	•
EM74HC574	Octal D-type flip-flop; positive-edge trigger (3-state)	2.0-6.0	+/- 5.2	7	-40~125			•	•
EM74HCT574	Octal D-type flip-flop; positive-edge trigger; TTL-enabled (3-state)	4.5-5.5	+/- 5.2	8	-40~125			•	•
EM74HCS74	Dual D-type flip-flop with set and reset; positive-edge trigger; TTL enabled Schmitt trigger	2.0-6.0	+/- 5.2	8	-40~125	•	•		
EM74LVC74A	Dual D-type flip-flop with set and reset; positive-edge trigger	1.65-5.5	+/- 32	4.5	-40~125	•	•		

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Logic - Schmitt-trigger IC's

Type number	Description	Features				Package					
		V _{cc} (V)	I _o (mA)	t _{pd} (ns)	T _{amb} (°C)	SOP-14L	TSSOP-14L	SOP-16L	TSSOP-16L	SOP-20L	TSSOP-20L
EM74HC14	Hex inverter; Schmitt trigger	2.0-6.0	+/- 5.2	8	-40~125	•	•				
EM74HC132	Quad 2-input NAND Schmitt trigger	2.0-6.0	+/- 5.2	8	-40~125	•	•				
EM74HC7001	Quad 2-Input AND Gates with Schmitt-Trigger Inputs	2.0-6.0	+/- 5.2	8	-40~125	•	•				
EM74HC7002	Quad 2-Input NOR Gates with Schmitt-Trigger Inputs	2.0-6.0	+/- 5.2	8	-40~125	•	•				
EM74HC7032	Quad 2-Input OR Gates with Schmitt-Trigger Inputs	2.0-6.0	+/- 5.2	8	-40~125	•	•				
EM74HC7540	Octal inverter/line driver; Schmitt trigger (3-state)	2.0-6.0	+/- 7.8	3.4	-40~125					•	•
EM74HC7541	Octal buffer/line driver; Schmitt trigger (3-state)	2.0-6.0	+/- 7.8	3.4	-40~125					•	•
EM74HCT14	Hex inverter; Schmitt trigger; TTL-enabled	4.5-5.5	+/- 5.2	11	-40~125	•	•				
EM74HCT132	Quad 2-input NAND Schmitt trigger; TTL-enabled	4.5-5.5	5.2	8	-40~125	•	•				
EM74HCT7001	Quad 2-Input AND Gates with Schmitt-Trigger Inputs; TTL-enabled	4.5-5.5	+/- 5.2	8	-40~125	•	•				
EM74HCT7002	Quad 2-Input NOR Gates with Schmitt-Trigger Inputs; TTL-enabled	4.5-5.5	+/- 5.2	8	-40~125	•	•				
EM74HCT7032	Quad 2-Input OR Gates with Schmitt-Trigger Inputs; TTL-enabled	4.5-5.5	+/- 5.2	8	-40~125	•	•				
EM74HCT7540	Octal buffer/line driver Schmitt trigger; TTL-enabled (3-state)	4.5-5.5	+/- 7.8	3.4	-40~125					•	•
EM74HCT7541	Octal buffer/line driver Schmitt trigger; TTL-enabled (3-state)	4.5-5.5	+/- 7.8	3.4	-40~125					•	•
EM74HCS00	Quad 2-input NAND gate; Schmitt trigger	2.0-6.0	5.2	8	-40~125	•	•				
EM74HCS02	Quad 2-input NOR gate; Schmitt trigger	2.0-6.0	+/- 5.2	8	-40~125	•	•				
EM74HCS03	Quad 2-input NAND gate; open-drain; Schmitt trigger	2.0-6.0	+/- 5.2	8	-40~125	•	•				
EM74HCS04	Hex inverter; Schmitt trigger	2.0-6.0	+/- 5.2	8	-40~125	•	•				
EM74HCS05	Hex inverter; open-drain; Schmitt trigger	2.0-6.0	5.2	8	-40~125	•	•				
EM74HCS07	Hex buffer; open-drain; Schmitt trigger	2.0-6.0	5.2	8	-40~125	•	•				
EM74HCS08	Quad 2-input AND gate; Schmitt trigger	2.0-6.0	+/- 5.2	8	-40~125	•	•				
EM74HCS09	Quad 2-Input AND Gates with Open-Drain Outputs and SchmittTrigger Inputs	2.0-6.0	+/- 5.2	8	-40~125	•	•				
EM74HCS10	Triple 3-input NAND gate; Schmitt trigger	2.0-6.0	+/- 5.2	8	-40~125	•	•				
EM74HCS11	Triple 3-input AND gate; Schmitt trigger	2.0-6.0	+/- 5.2	8	-40~125	•	•				
EM74HCS27	Triple 3-input NOR gate; Schmitt trigger	2.0-6.0	+/- 5.2	8	-40~125	•	•				
EM74HCS32	Quad 2-input OR gate; Schmitt trigger	2.0-6.0	+/- 5.2	8	-40~125	•	•				
EM74HCS86	Quad 2-input EXCLUSIVE-OR gate; Schmitt trigger	2.0-6.0	+/- 5.2	8	-40~125	•	•				
EM74HCS125	Quad buffer/line driver (3-state); Schmitt trigger	2.0-6.0	+/- 5.2	8	-40~125	•	•				
EM74HCS126	Quad buffer/line driver (3-state); Schmitt trigger	2.0-6.0	+/- 5.2	8	-40~125	•	•				
EM74HCS244	Octal Buffers and Line Drivers with Schmitt-Trigger Inputs and 3-State Outputs	2.0-6.0	+/- 7.8	3.4	-40~125					•	•
EM74HCS266	Quad 2-Input XNOR Gates with Open-Drain Outputs; Schmitt trigger	2.0-6.0	5.2	8	-40~125	•	•				
EM74HCS365	Hex buffer/line driver (3-state); Schmitt trigger	2.0-6.0	+/- 5.2	8	-40~125			•	•		
EM74HCS366	Hex inverter/line driver (3-state); Schmitt trigger	2.0-6.0	+/- 5.2	8	-40~125			•	•		
EM74HCS367	Hex buffer/line driver (3-state); Schmitt trigger	2.0-6.0	+/- 5.2	8	-40~125			•	•		
EM74HCS368	Hex inverter/line driver (3-state); Schmitt trigger	2.0-6.0	+/- 5.2	8	-40~125			•	•		
EM74HCS4075	Triple 3-input OR gate; Schmitt trigger	2.0-6.0	+/- 5.2	8	-40~125	•	•				
EM74HCS7266	Quad 2-Input XNOR Gates; Schmitt trigger	2.0-6.0	+/- 5.2	8	-40~125	•	•				
EM74LVC14A	Hex inverting Schmitt trigger with 5 V tolerant input	1.65-5.5	+/- 32	5	-40~125	•	•				
EM74LVC132A	Quad 2-input NAND Schmitt trigger	1.65-5.5	+/- 32	5	-40~125	•	•				

逻辑芯片

Logic - Shift Registers

Type number	Description	Features				Package			
		V _{cc} (V)	I _o (mA)	t _{pd} (ns)	T _{amb} (°C)	SOP-14L	TSSOP-14L	SOP-16L	TSSOP-16L
EM74HC164	8-bit serial-in, parallel-out shift register	2.0-6.0	+/- 5.2	7	-40~125	•	•		
EM74HC165	8-bit parallel-in/serial out shift register	2.0-6.0	+/- 5.2	7	-40~125			•	•
EM74HC166	8-bit parallel-in/serial out shift register	2.0-6.0	+/- 5.2	7	-40~125			•	•
EM74HC594	8-bit shift register with output register	2.5-6.0	+/- 5.2	7	-40~125			•	•
EM74HC595	8-bit serial-in, serial or parallel-out shift register with output latches; 3-state	2.0-6.0	+/- 5.2	7	-40~125			•	•
EM74HC597	8-bit shift register with input flip-flops	2.0-6.0	+/- 5.2	7	-40~125			•	•
EM74HC4094	8-stage shift-and-store bus register	2.5-6.0	+/- 5.2	7	-40~125			•	•
EM74HCT164	8-bit serial-in, parallel-out shift register	4.5-5.5	+/- 5.2	8	-40~125	•	•		
EM74HCT165	8-bit parallel-in/serial out shift register	4.5-5.5	+/- 5.2	8	-40~125			•	•
EM74HCT166	8-bit parallel-in/serial out shift register	4.5-5.5	+/- 5.2	8	-40~125			•	•
EM74HCT594	8-bit shift register with output register	4.5-5.5	+/- 5.2	8	-40~125			•	•
EM74HCT595	8-bit serial-in, serial or parallel-out shift register with output latches; 3-state	4.5-5.5	+/- 5.2	8	-40~125			•	•
EM74HCT597	8-bit shift register with input flip-flops	4.5-5.5	+/- 5.2	8	-40~125			•	•
EM74HCT4094	8-stage shift-and-store bus register	4.5-5.5	+/- 5.2	8	-40~125			•	•

Logic - Latches

Type number	Description	Features				Package	
		V _{cc} (V)	I _o (mA)	t _{pd} (ns)	T _{amb} (°C)	SOP-16L	TSSOP-16L
EM74HC259	8-bit addressable latch	2.0-6.0	+/- 5.2	7	-40~125	•	•
EM74HCT259	8-bit addressable latch; TTL-enabled	4.5-5.5	+/- 5.2	8	-40~125	•	•
EM74HC373	Octal D-type transparent latch (3-state)	2.0-6.0	+/- 5.2	7	-40~125	•	•
EM74HCT373	Octal D-type transparent latch; TTL-enabled (3-state)	4.5-5.5	+/- 5.2	8	-40~125	•	•
EM74HC573	Octal D-type transparent latch (3-state)	2.0-6.0	+/- 5.2	7	-40~125	•	•
EM74HCT573	Octal D-type transparent latch; TTL-enabled (3-state)	4.5-5.5	+/- 5.2	8	-40~125	•	•

Logic - Decoders / Demultiplexers

Type number	Description	Features				Package	
		V _{cc} (V)	I _o (mA)	t _{pd} (ns)	T _{amb} (°C)	SOP-16L	TSSOP-16L
EM74HC138	3-to-8 line decoder/demultiplexer; inverting	2.0-6.0	+/- 5.2	7	-40~125	•	•
EM74HCT138	3-to-8 line decoder/demultiplexer; inverting; TTL-enabled	4.5-5.5	+/- 5.2	8	-40~125	•	•

逻辑芯片

Voltage translators (Level-shifters)

Type number	Description	Features				Package	
		V _{cc} (V)	I _O (mA)	t _{pd} (ns)	T _{amb} (°C)	SOP-24L	TSSOP-24L
EM74LVCC3245A	Octal Bus Transceiver With Adjustable Output Voltage and 3-State Outputs	1.5-5.5	1.5-5.5	+/- 24	-40~125	•	•
EM74LVC4245A	8-bit dual-supply voltage translating transceiver (3-state)	1.5-5.5	1.5-5.5	+/- 24	-40~125	•	•
EM74LVC8T245	8-bit dual-supply voltage translating transceiver (3-state)	1.2-5.5	1.2-5.5	+/- 24	-40~125	•	•
EM74LVCH8T245	8-bit dual-supply voltage translating transceiver with bus hold (3-state)	1.2-5.5	1.2-5.5	+/- 24	-40~125	•	•

电源芯片

Step-Down Converter

Type number	V _{in} (min)(V)	V _{in} (max)(V)	FB(V)	I _{out} (Max)(A)	I _q (Typ)(mA)	Switching frequency(Min)(kHz)	Switching frequency(Max)(kHz)	Control mode	Package
EMP2112	2.5	5.5	0.6	1.2	0.035	1500	1500	Current mode	SOT23-5L, SOT23-6L
EMP2120	2.5	5.5	0.6	2	0.035	1500	1500	Current mode	SOT23-5L, SOT23-6L,DFN1.6X1.6-6L
EMP2130	2.5	5.5	0.6	3	0.035	1000	1000	Current mode	SOT23-5L, SOT23-6L,DFN2X2-8L
EMP2420	4	36	0.8	2	0.5	200	2400	Current mode	TSOT23-8L
EMP2810	6	100	1.2	1	0.01	75	1000	Constant on-time	ESOP-8L

LDO

Type number	V _{in} (min)(V)	V _{in} (max)(V)	V _{out} (min)(V)	V _{out} (max)(V)	I _{out} (Max)(A)	I _q (Typ)(uA)	I _{sc} (Typ)(uA)	Fixed output options(V)	Package
EMP1105	2	5.5	1.2	3.6	0.5	30	0.01	1.2, 1.5, 1.8, 2.5, 2.8, 3.0, 3.3, 3.6	SOT23-5L, DFN1x1-4L
EMP1402	2.7	40	0.6	5	0.2	10	0.8	1.2, 1.8, 2.5, 3.0, 3.3, 4.5, 5	MSOP-8L, SOT223, SOT893, SOT23-5L,QFN3x3-8L,QFN2x2-6L
EMP1403	2.7	40	0.6	5	0.3	10	0.8	1.2, 1.8, 2.5, 3.0, 3.3, 4.5, 5	MSOP-8L, SOT223, SOT893, SOT23-5L,QFN3x3-8L,QFN2x2-6L
EMP1502	2.7	60	0.6	5	0.2	10	0.8	1.2, 1.8, 2.5, 3.0, 3.3, 4.5, 5	MSOP-8L, SOT223, SOT893, SOT23-5L,QFN3x3-8L,QFN2x2-6L
EMP1503	2.7	60	0.6	5	0.3	10	0.8	1.2, 1.8, 2.5, 3.0, 3.3, 4.5, 5	MSOP-8L, SOT223, SOT893, SOT23-5L,QFN3x3-8L,QFN2x2-6L

电源芯片

OVP

Type number	Vin (min)(V)	Vin(max)(V)	Iout(Max)(A)	Iq(Typ)(uA)	Rdson(Typ)(mΩ)	Internal OVP trip level(V)	OVLO threshold (min)(V)	OVLO threshold (max)(V)	Package
EMP3230	2.6	20	3.1	65	63	6	4	20	DFN2x2-6L
EMP3235A	2.7	20	3.5	68	50	6.05	4	20	SOT23-6L
EMP3235B	2.7	20	3.5	68	50	6.05	6.05	6.05	SOT23-6L
EMP3245	2.6	20	4.5	51	31	6.5	4	20	FC-QFN-9L

eFuses and hot swap controllers

Type number	FET	Vin (min)(V)	Vin (max)(V)	Current limit(min)(A)	Current limit(max)(A)	Overcurrent response	Overvoltage response	Fault response	Features	Package
EMP51050	Internal	4.5	5.5	1	5	current limiting	-	Auto-retry	Quick output discharge	DFN3x3-10L
EMP51051	Internal	4.5	5.5	1	5	current limiting	-	Latch-off	Quick output discharge	DFN3x3-10L
EMP51052	Internal	4.5	5.5	1	5	current limiting	Clamp	Auto-retry	Quick output discharge	DFN3x3-10L
EMP51053	Internal	4.5	5.5	1	5	current limiting	Clamp	Latch-off	Quick output discharge	DFN3x3-10L
EMP51054	Internal	4.5	5.5	1	5	current limiting	Clamp	Auto-retry	Reverse current blocking when off	DFN3x3-10L
EMP51055	Internal	4.5	5.5	1	5	current limiting	Clamp	Latch-off	Reverse current blocking when off	DFN3x3-10L
EMP52050	Internal	4.5	13.8	1	5	current limiting	-	Auto-retry	Quick output discharge	DFN3x3-10L
EMP52051	Internal	4.5	13.8	1	5	current limiting	-	Latch-off	Quick output discharge	DFN3x3-10L
EMP52052	Internal	4.5	13.8	1	5	current limiting	Clamp	Auto-retry	Quick output discharge	DFN3x3-10L
EMP52053	Internal	4.5	13.8	1	5	current limiting	Clamp	Latch-off	Quick output discharge	DFN3x3-10L
EMP52054	Internal	4.5	13.8	1	5	current limiting	Clamp	Auto-retry	Reverse current blocking when off	DFN3x3-10L
EMP52055	Internal	4.5	13.8	1	5	current limiting	Clamp	Latch-off	Reverse current blocking when off	DFN3x3-10L

Analog Switch

USB Switches

Type number	Description	Target Applications	Features				Package						
			V _{CC} (V)	V _{PASS} (V)	R _{ON} (Ω)	T _{amb} (°C)	MSOP-10L	SOT363	SOT23-6L	QFN1.5x2.0-10L	QFN1.4x1.8-10L	QFN1.8x2.6-16L	DFN3x3-10L
EMS3157	Low Voltage and High Speed SPDT Analog Switch with True Isolation	Wearables and mobile devices;Portable computing;Remote radio unit	1.5-5.5	3.3	3.4	-40~125		•	•				
EMS3900	Dual high-speed USB 2.0 double-pole double-throw analog switch	Sample-and-Hold Circuits;Battery-Powered Equipment;Audio and Video Signal Routing	1.5-5.5	3.3	3.4	-40~125					•		
EMS4000	Dual 2:1 USB2.0 Mux/De-Mux with DC 30V Over-Voltage Protection	Cellular Phones;Digital Cameras;Portable Equipment	2.5-4.5	3.3	10	-40~125	•			•	•		•
EMS4100	Two-Channel Differential 2:1/1:2 USB 3.1 Super Speed 10Gbps Mux/DeMux	USBType-C™ Ecosystem;Desktop and Notebook PCs;Server/Storage Area Networks	1.5-5	3.3	10	-40~125						•	

Bus Switches and Multiplexers

Type number	Description	Features				Package			
		V _{cc} (V)	V _{PASS} (V)	R _{ON} (Ω)	T _{amb} (°C)	SOP-14L	TSSOP-14L	SOP-16L	TSSOP-16L
EM74CBTLV3125	Quad bus switch	2.3-3.6	3.3	3	-40~125	•	•		
EM74CBTLV3126	Quad bus switch	2.3-3.6	3.3	3	-40~125	•	•		
EM74CBTLV3253	Dual 1-of-4 multiplexer/demultiplexer	2.3-3.6	3.3	3	-40~125			•	•
EM74CBTLV3257	Quad 1-of-2 multiplexer/demultiplexer	2.3-3.6	3.3	3	-40~125			•	•

Applications:



PC



Phone/PAD/POS



Monitor



Dongle/Hub



Portable Energy Storage



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