

N-MOSFET 60V 5.4mΩ 72A

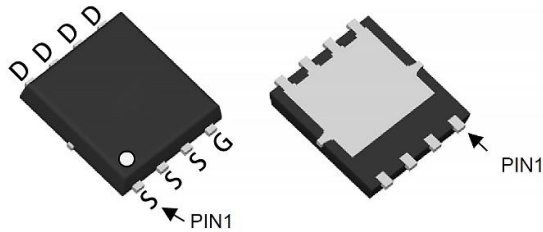
■ Product Summary

- V_{DS} 100V
- I_D 72A
- R_{DS(ON)} (at V_{GS}=10V) <6.6 mΩ (Typ: 5.4 mΩ)
- R_{DS(ON)} (at V_{GS}=4.5V) <8.6 mΩ (Typ: 6.6 mΩ)

■ Naming convention

M	G	C	0	5	4	N	0	6	A	L
Megain		B: PDFN3X3 C: PDFN5X6 P: TO220 H: TO263 S: SOP8 D: TO252	R _{DS(on)} Typ. @V _{GS} =10V		N: N P: P C: N+P D: N+N		10: 100V 08: 80V 06: 60V 04: 40V 03: 30V		A:V _{GS} 20/-12 w/o A:V _{GS} ±20	L:V _{th} (1~2.5V) N:V _{th} (2~4V)

■ Pin configuration



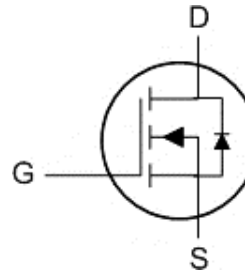
■ Features

- Fast switching speed
- Reliable and Rugged
- ROHS Compliant & Halogen-Free
- 100% UIS and Rg Tested

■ Application

- DC/DC converter

■ Symbol



■ Ordering Information

Order code	Package	Form	Quantity (PCS)	Marking
MGC054N06L	PDFN 5x6	Tape & Reel	5000 / Tape & Reel	MGC054N06L

■ Absolute Maximum Ratings

T_C=25°C Unless Otherwise Noted.

Symbol	Parameter	Value	Units
V _{DS}	Drain-Source Voltage	60	V
V _{GS}	Gate-Source Voltage	±20	V
I _D	Drain Current – Continuous ¹ (T _C =25°C)	72	A
	Drain Current – Continuous (T _C =100°C)	46	
I _{DM}	Drain Current – Pulsed ¹	180	A
I _S	Diode Continuous Forward Current (T _C =25°C)	17	A
I _{AS}	Single Pulse Avalanche Current ² (L=0.1mH)	30	A
	Single Pulse Avalanche Current ² (L=0.5mH)	18	
E _{AS}	Single Pulse Avalanche Energy ² (L=0.1mH)	45	mJ
	Single Pulse Avalanche Energy ² (L=0.5mH)	81	
P _D	Power Dissipation(T _C =25°C)	2.5	W
	Power Dissipation (T _C =70°C)	1.6	
T _J	Operating Junction Temperature Range	-55 to 150	°C
T _{STG}	Storage Temperature Range	-55 to 150	°C

■ Thermal Characteristics

Symbol	Parameter	Value	Units
$R_{\theta JA}$	Thermal Resistance Junction to Ambient ³	50	°C/W
$R_{\theta JC}$	Thermal Resistance Junction to Case	2.5	°C/W

■ Electrical Characteristics

T_J=25°C Unless Otherwise Noted.

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
Static Electrical Characteristics						
B _V D _{SS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250uA	60	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =48V, V _{GS} =0V	-	-	1	uA
V _{GS} (th)	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =250uA	1	1.7	2.5	V
I _{GSS}	Gate Leakage Current	V _{GS} =±20V, V _{DS} =0V	-	-	±100	nA
R _{DS} (ON)	Drain-Source On-state Resistance ⁴	V _{GS} =10V, I _D =20A	-	5.4	6.6	mΩ
		V _{GS} =4.5V, I _D =10A	-	8.2	10.5	
g _{fs}	Forward Transconductance	V _{DS} =5V, I _D =10A	-	21	-	S
Dynamic Characteristics ⁵						
R _G	Gate Resistance	V _{GS} =0V, V _{DS} =0V, F=1MHz	-	1.2	3	Ω
C _{iss}	Input Capacitance	V _{GS} =0V, V _{DS} =30V, F=1MHz	-	1609	-	pF
C _{oss}	Output Capacitance		-	484	-	
C _{rss}	Reverse Transfer Capacitance		-	77	-	
T _d (ON)	Turn-on Delay Time	V _{GS} =10V, V _{DS} =30V, I _D =1A, R _G =1Ω	-	10.9	-	nS
T _r	Turn-on Rise Time		-	6.9	-	
T _d (OFF)	Turn-off Delay Time		-	24.9	-	
T _f	Turn-off Fall Time		-	37.4	-	
Q _g	Total Gate Charge	V _{GS} =4.5V, V _{DS} =30V I _D =20A	-	18.8	-	nC
Q _g	Total Gate Charge	V _{GS} =10V, V _{DS} =30V, I _D =20A	-	37	-	
Q _{gs}	Gate-Source Charge		-	7.25	-	
Q _{gd}	Gate-Drain Charge		-	8.49	-	
Source-Drain Characteristics						
V _{SD}	Diode Forward Voltage ⁴	I _{SD} =10A, V _{GS} =0V	-	0.78	1.1	V
t _{rr}	Reverse Recovery Time	I _F =10A, V _R =30V dI _F /dt=100A/us	-	21.9	-	nS
Q _{rr}	Reverse Recovery Charge		-	9.4	-	nC

Note :

1. Max. current is limited by junction temperature.
2. UIS tested and pulse width are limited by maximum junction temperature 150 ° C
3. Pulse test (pulse width 300us, duty cycle 2%).
4. Guaranteed by design, not subject to production testing.

■ Typical Characteristics

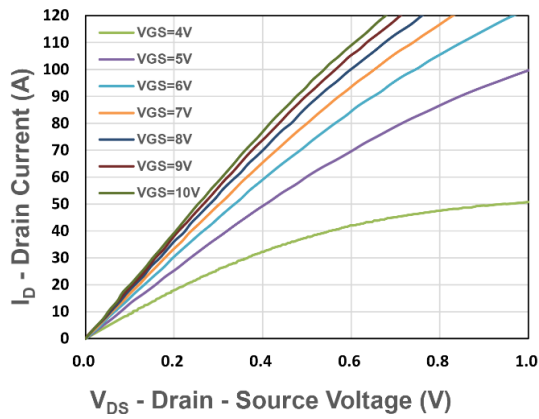


Figure 1. Output Characteristics

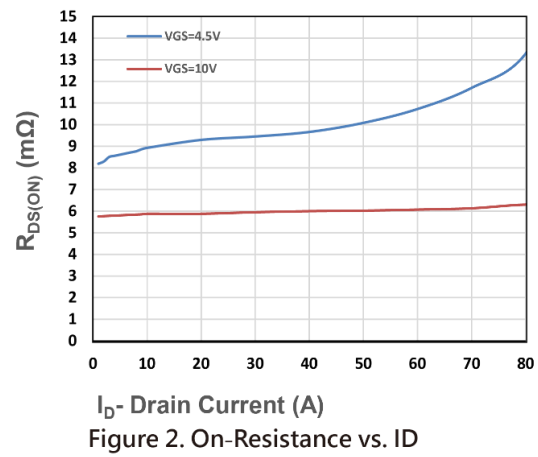


Figure 2. On-Resistance vs. I_D

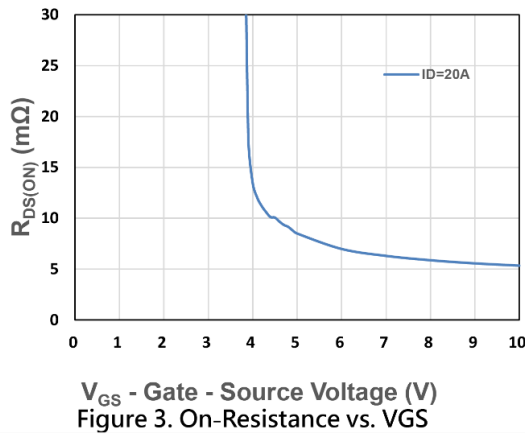


Figure 3. On-Resistance vs. V_{GS}

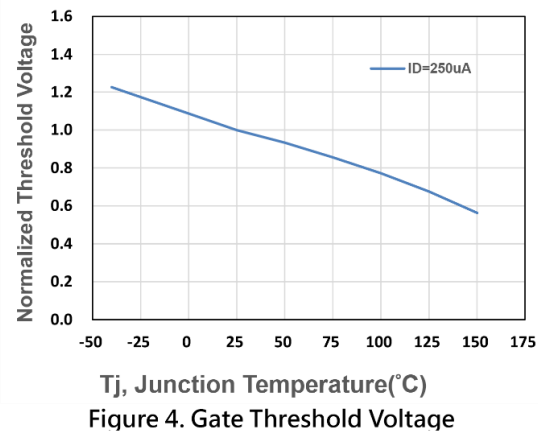


Figure 4. Gate Threshold Voltage

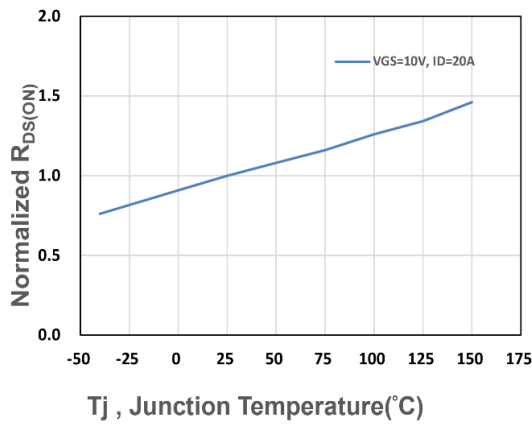


Figure 5. Drain-Source On Resistance

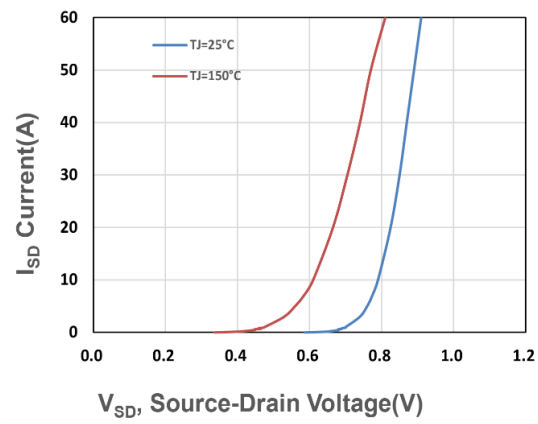
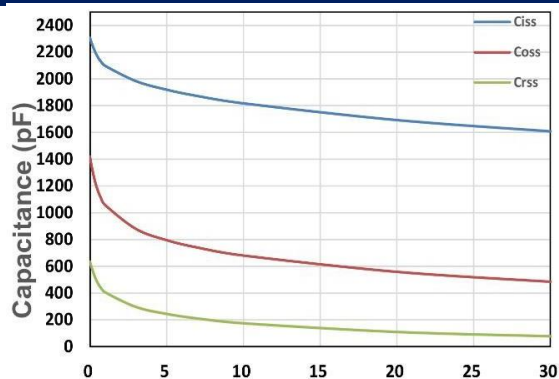
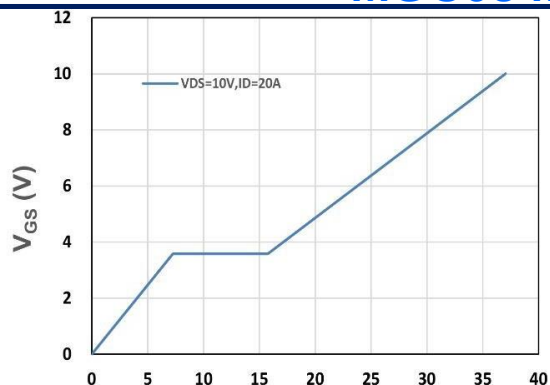


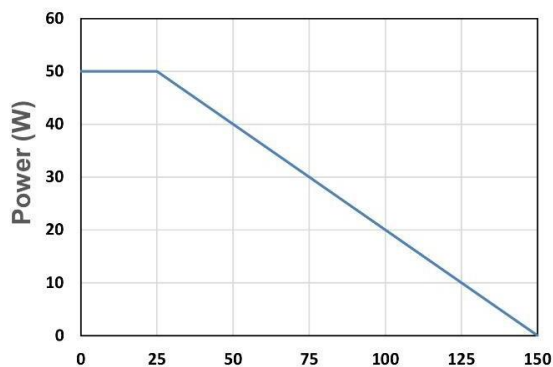
Figure 6. Source-Drain Diode Forward



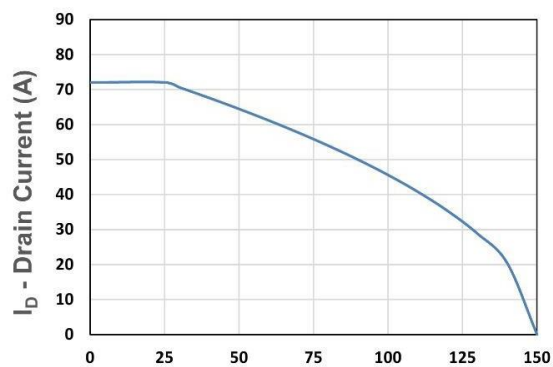
V_{DS} - Drain - Source Voltage (V)
Figure 7. Capacitance



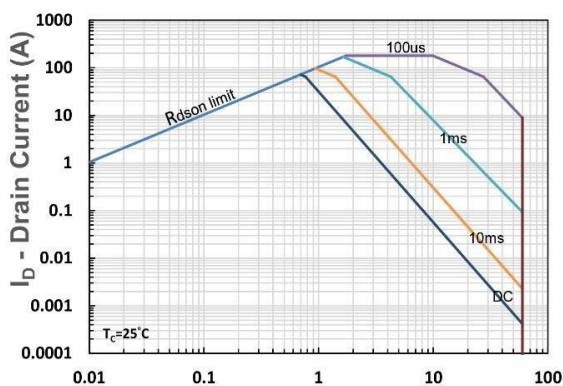
Q_g , Total Gate Charge (nC)
Figure 8. Gate Charge Characteristics



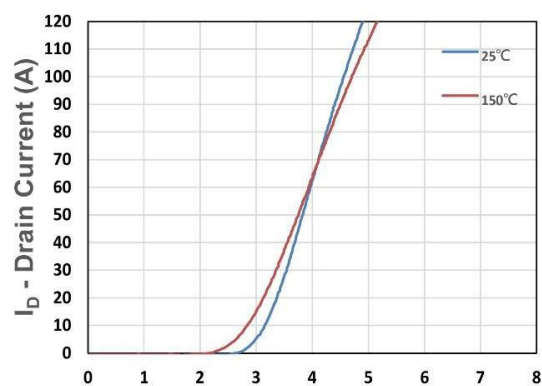
T_c - Case Temperature (°C)
Figure 9. Power Dissipation



T_c - Case Temperature (°C)
Figure 10. Drain Current



V_{DS} - Drain-Source Voltage (V)
Figure 11. Safe Operating Area



V_{GS} - Gate - Source Voltage (V)
Figure 12. Transfer Characteristics

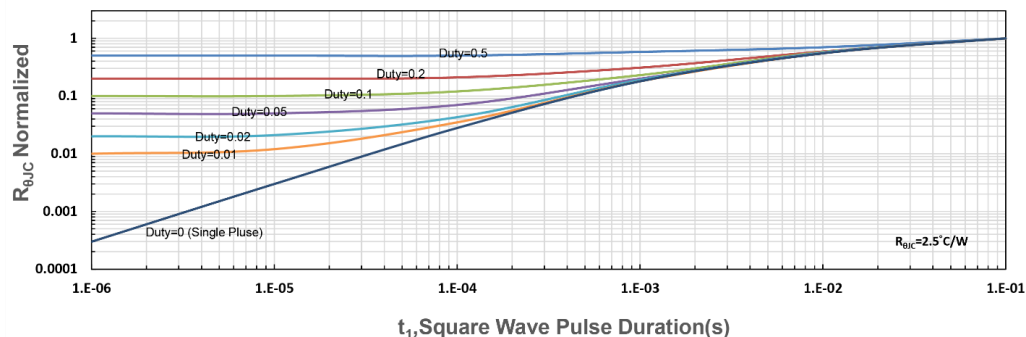
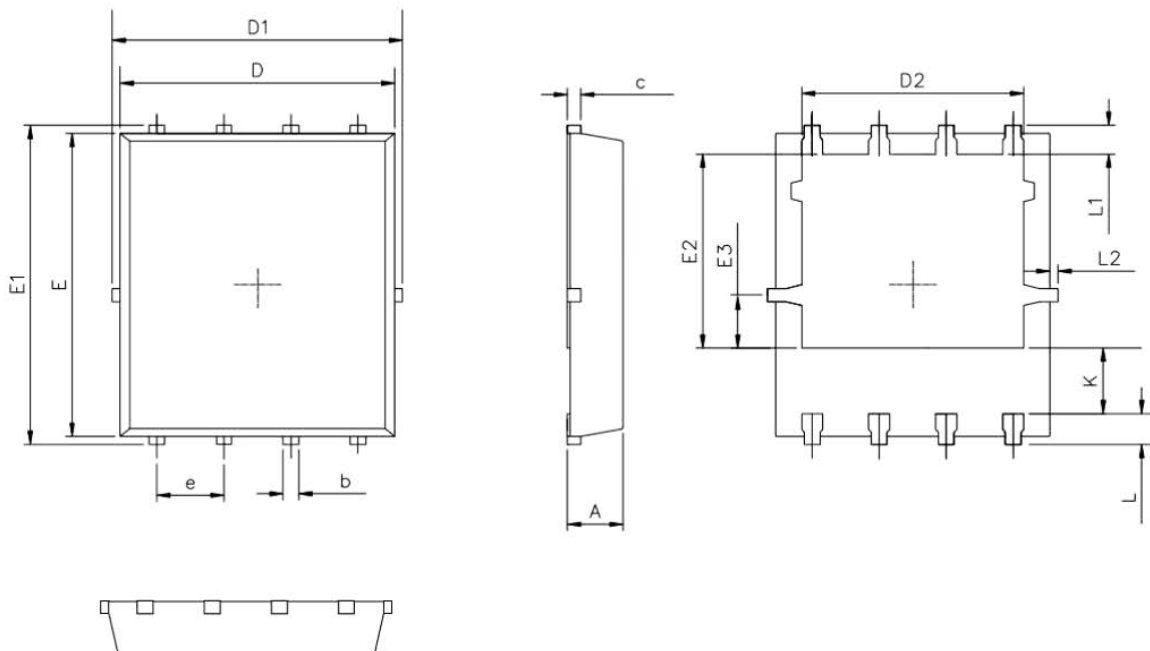


Figure 13. $R_{\theta JC}$ Transient Thermal Impedance

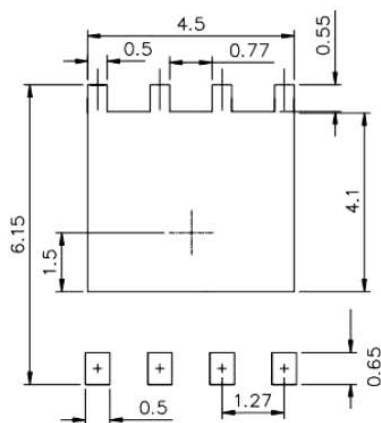
■ Package size

Unit: mm。

PDFN 5X6:



RECOMMENDED LAND PATTERN



UNIT:mm

	MIN	NOM	MAX
A	0.90	1.00	1.10
b	0.25	0.35	0.50
c	0.10	0.20	0.30
D	4.80	5.00	5.30
D1	4.90	5.10	5.50
D2	3.92	4.02	4.20
E	5.65	5.75	5.85
E1	5.90	6.05	6.20
E2	3.325	3.525	3.775
E3	0.80	0.90	1.00
e		1.27	
L	0.40	0.55	0.70
L1		0.65	
L2	0.00		0.15
K	1.00	1.30	1.50