

DFN1610-2L Plastic-Encapsulate ESD Protection Diodes

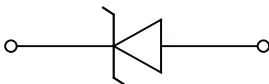
Features

- Low leakage current
- DFN1610-2L surface mount package
- IEC 61000-4-2 (ESD Air): ±30kV
- IEC 61000-4-2 (ESD Contact): ±30kV
- IEC 61000-4-5 (Lightning 8/20μs): 32A

Applications

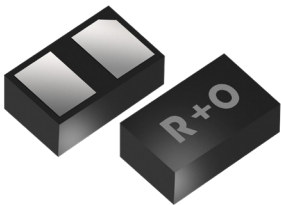
- Mobile Phone, Digital cameras
- Battery Protection
- Power Line Protection
- Vbat pin for Mobile Devices
- Hand Held Portable Applications

Function Diagram



Reverse Working Voltage
20V Max.
High capacitance
120pF(Max.)

DFN1610-2L



Maximum Ratings (Ta=25°C Unless otherwise specified)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V _{ESD}	Electrostatic Discharge Voltage	ESD per IEC 61000-4-2(Air)	±30	KV
		ESD per IEC 61000-4-2(Contact)	±30	KV
P _{PP}	Peak Pulse Power	tp = 8/20 μs	1200	W
I _{PP}	Rated Peak Pulse Current	tp = 8/20 μs	32	A
T _J	Operating JunctionTemperature Range	—	-55 to +125	°C
T _{STG}	Operating JunctionTemperature Range	—	-55 to +150	°C

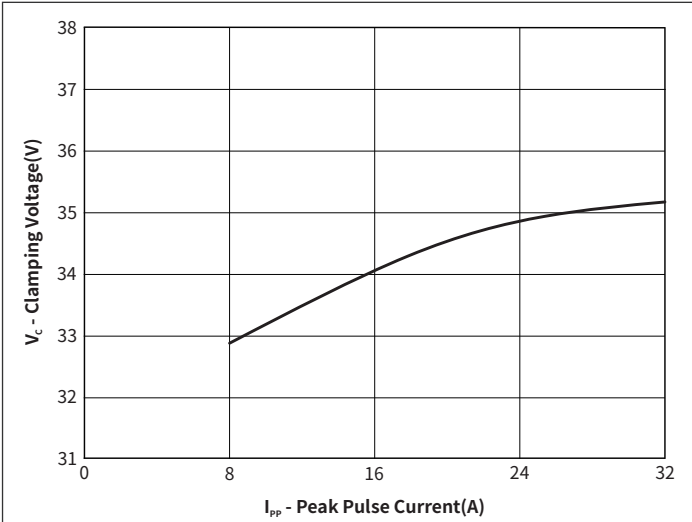
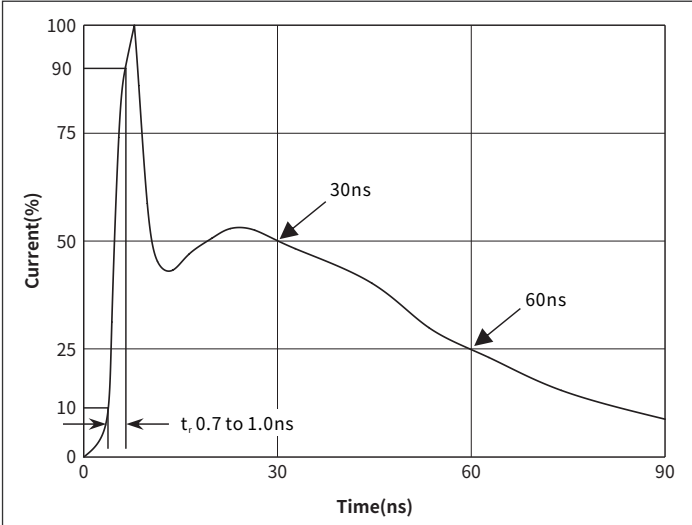
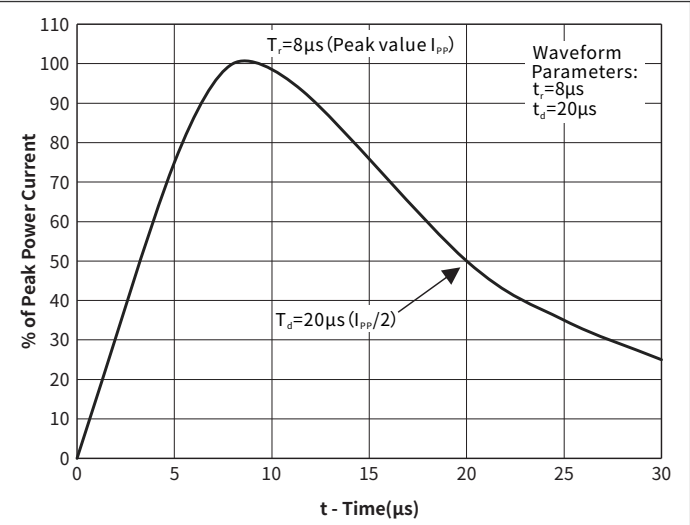
Electrical Parameter

SYMBOL	PARAMETER
V _C	Clamping Voltage @ I _{pp}
V _{BR}	Breakdown Voltage @ I _T
I _{PP}	Peak Pulse Current
I _T	Test Current
I _R	Reverse Leakage Current @ VRWM
V _{RWM}	Peak Reverse Working Voltage
P _{PP}	Peak Pulse Power Dissipation
C _J	Junction Capacitance @ V _R =0V,f=1MHz
I _F	Forward Current
V _F	Forward Voltage @I _F

● Electrical Characteristics (Ta=25°C Unless otherwise specified)

PARAMETER	SYMBOL	CONDITION	Min	Typ	Max	UNIT
Peak Reverse Working Voltage	V_{RWM}	$T_a=25^{\circ}C$	—	—	20	V
Breakdown Voltage	V_{BR}	$I_R=1mA, T_a=25^{\circ}C$	21.8	—	25.2	V
Reverse Leakage Current	I_R	$V_R=20V, T_a=25^{\circ}C$	—	—	0.5	μA
Clamping Voltage	V_C	$I_{PP}=20A, t_p=8/20\mu s$	—	—	35	V
		$I_{PP}=32A, t_p=8/20\mu s$	—	—	38	
Junction Capacitance	C_J	$V_R=0V, f=1MHz$	—	60	120	pF

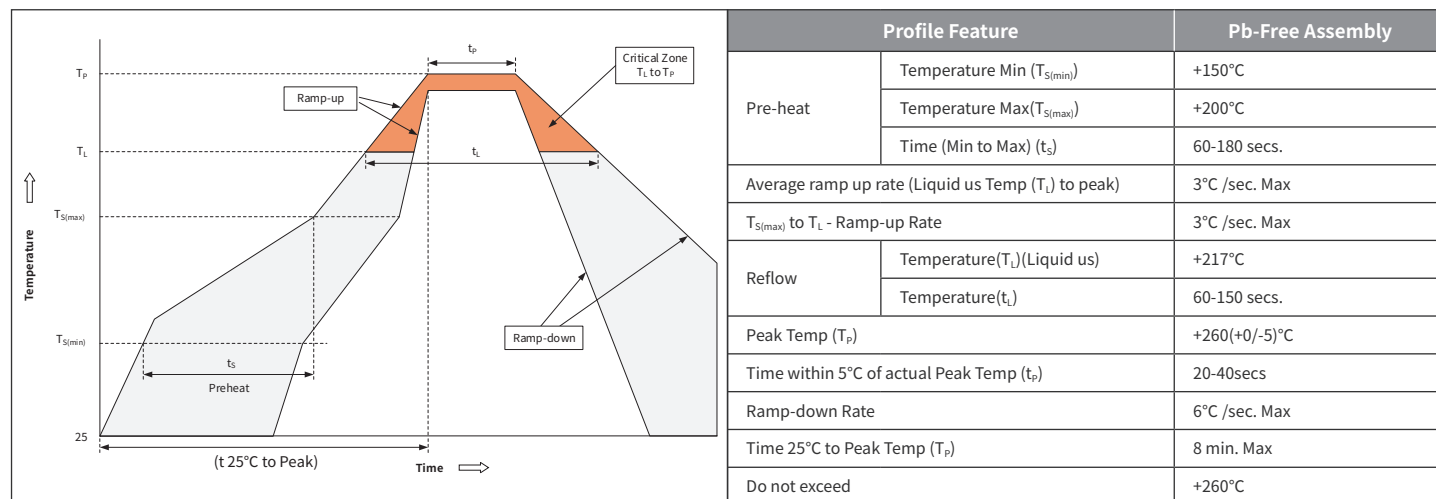
● Ratings And Characteristics Curves (Ta=25°C Unless otherwise specified)



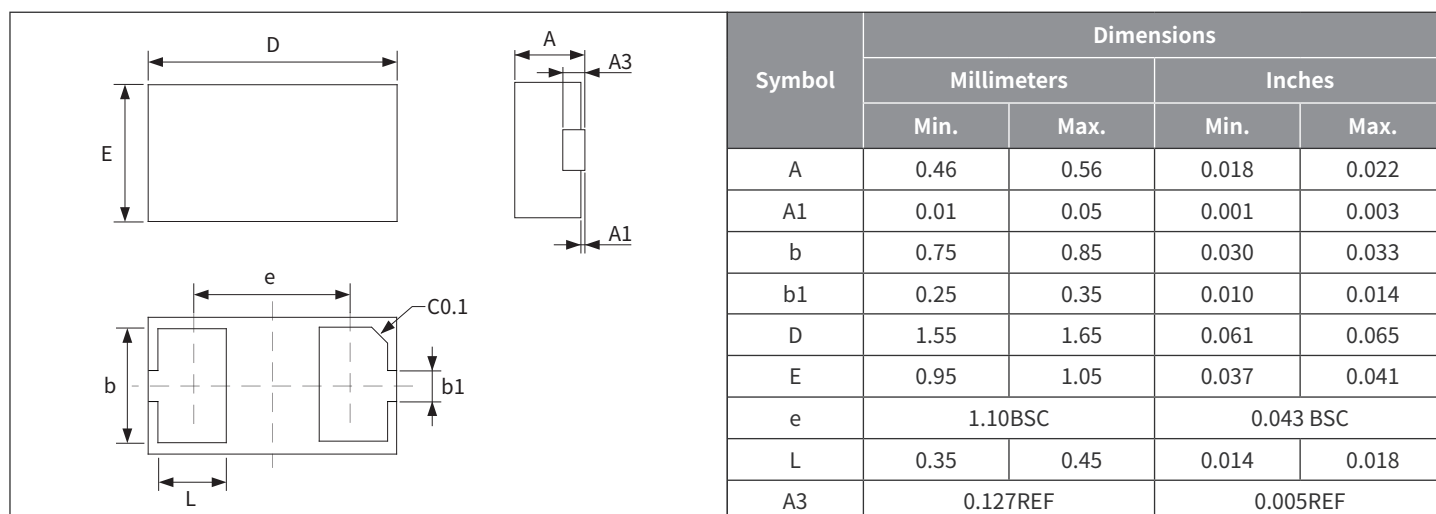
● Ordering Information

PREFERED P/N	PACKAGE	SIZE(mm)	DELIVERY MODE	MPQ(PCS)
H20VH16U	DFN1610-2L	1.575×0.975×0.485	7" REEL	3000

● Recommended Soldering Conditions



● Package Outline Dimensions (DFN1610-2L)



● Suggested Pad Layout

