

Features

- 3rd generation SiC MOSFET technology
- Optimized package with separate driver source pin
- High blocking voltage with low on-resistance
- High-speed switching with low capacitances
- Fast intrinsic diode with low reverse recovery (Q,,)
- Halogen free, RoHS compliant

Benefits

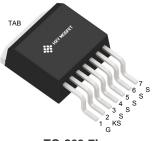
- · Reduce switching losses and minimize gate ringing
- Higher system efficiency
- Reduce cooling requirements
- · Increase power density
- Increase system switching frequency

Applications

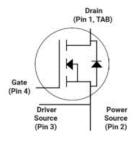
- Renewable energy
- EV battery chargers
- High voltage DC/DC converters
- Switch Mode Power Supplies







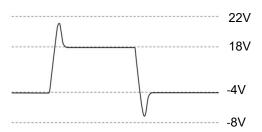
TO-263-7L Package



Maximum Ratings (T_c = 25 °C unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain-source voltage	V _{DS}	1200	V
Continuous drain current			
$T_{C} = 25^{\circ}C$, $V_{GS} = 15V$	I _D	65	Α
$T_{C} = 100^{\circ}C, V_{GS} = 15V$		46	
Pulsed drain current ($T_C = 25^{\circ}C$, t_p limited by T_{jmax})	I _{D pulse}	120	Α
Gate-Source voltage	V_{GS}	-4/+18	V
Gate-Source voltage(Absolute maximum values)	V_{GSmax}	-8/+22	V
Power dissipation (T _C = 25°C)	P _{tot}	326	W
Operating junction and storage temperature	$T_{\rm j}$, $T_{ m stg}$	-40+175	°C

•Example of acceptable V_{GS} waveform





SiC Power MOSFET N-Channel Enhancement Mode

Thermal Resistance

Parameter	Symbol	Value	Unit
Thermal resistance, junction – case. Max	R_{thJC}	0.46	°C/W
Thermal resistance, junction – ambient. Max	R_{thJA}	40	C/VV

Electrical Characteristics (at Tj = 25 °C, unless otherwise specified)

Barranatan	Or much at	Value				-	
Parameter	Symbol	min.	typ.	max.	Unit	Test Condition	
Static Characteristics							
Drain-source breakdown voltage	BV _{DSS}	1200	-	-	V	V _{GS} =0V, I _D =100uA	
Gate threshold voltage	V _{GS(th)}	2.2	3	4	V	$V_{DS}=V_{GS},I_{D}=10mA$	
						V _{DS} =1200V,V _{GS} =0V	
Zero gate voltage drain current	I _{DSS}	-	1	20	μA	T _C =25°C	
		1	5	-		T _C =175°C	
Gate-source leakage current	I _{GSS}	-		100	nA	V _{GS} =18V,V _{DS} =0V	
						V _{GS} =15V,	
Drain-source on-state resistance	R _{DS(on)}	-	40	52	mΩ	I _D =33.3A, T _J =25°C	
		-	62	-		T _J =175°C	
						V _{GS} =18V,	
Drain-source on-state resistance	R _{DS(on)}	-	32	40	mΩ	I _D =33.3A, T _J =25°C	
		-	59	-		T _J =175°C	
Transconductance	9 _{fs}	-	20		S	V _{DS} =20V,I _D =33.3A	



Dynamic Characteristics

Input Capacitance	C _{iss}	-	2766	-		$V_{DS} = 1000V$ $V_{GS} = 0V$
Output Capacitance	C _{oss}	-	125	-	pF nC	v _{GS} – 0v T _J = 25°C
Reverse Transfer Capacitance	C _{rss}	-	14	1		$V_{AC} = 25mV$ f = 1MHz
Gate Total Charge	Q_{G}	-	112	-		V _{DS} = 800V
Gate-Source charge	Q_{gs}	-	28	-		$V_{GS} = 0/15V$ $I_{D} = 33.3A$
Gate-Drain charge	Q_{gd}	-	51	-		
Turn-On Switching Energy	E _{ON}	-	701	-	μЈ	$V_{DD} = 800V$ $V_{GS} = -4/+15V$ $I_{D} = 20A$ $R_{G} = 2.5\Omega$ $L = 120uH$
Turn-Off Switching Energy	E _{OFF}	-	79	-		
Turn-on delay time	t _{d(on)}	-	13.4	-	ns	
Rise time	t _r	-	5.4	-		
Turn-off delay time	t _{d(off)}	-	32	-		
Fall time	t _f	-	19	-		
Gate resistance	R_G	-	0.6	-	Ω	V _{AC} = 25mV, f=1MHz

Body Diode Characteristics

Parameter		Value				
	Symbol	min.	typ.	max.	Unit	Test Condition
Dody Diede Feminard Voltons	5.3			V_{GS} =-4 V , I_{SD} =20A, T_J =25°C		
Body Diode Forward Voltage	V _{SD}		4.8		V	V _{GS} =-4V,I _{SD} =20A, T _J =175°C
Body Diode Reverse Recovery Time	t _{rr}	-	55	-	ns	$V_R = 800V$ $I_D = 33.3A$
Body Diode Reverse Recovery Charge	Q _{rr}	-	288	-	nC	di/dt = 1070A/µS T _J = 25°C



Typical Performance Characteristics

Fig 1. Output Characteristics (T_J=-55°C)

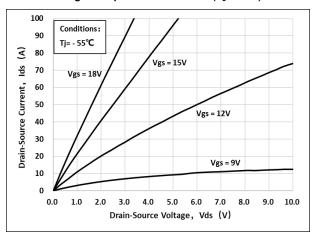


Fig 2. Output Characteristics (T_J=25°C)

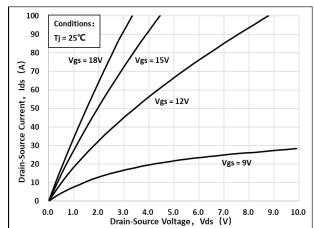


Fig 3. Output Characteristics (T_J=175°C)

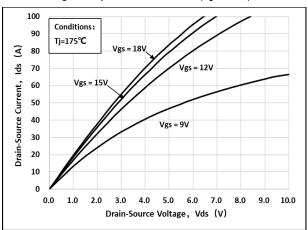


Fig 4: Rdson Vs Ids Characteristics

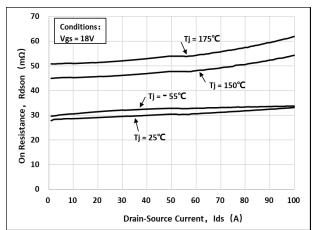


Fig 5: Rds(on) vs. Temperature

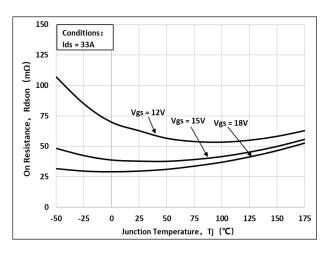
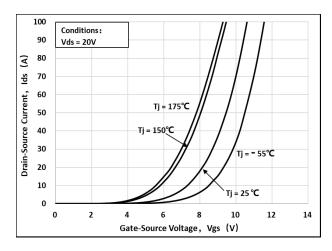


Fig 6: Transfer Characteristics



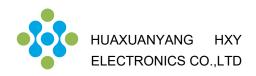


Fig 7: Body-diode Characteristics (T_J=-55°C)

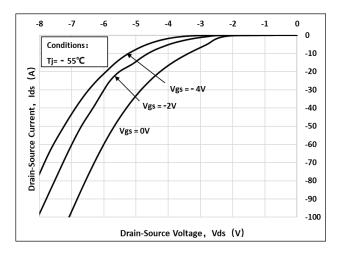


Fig 8: Body-diode Characteristics (T_J=25℃)

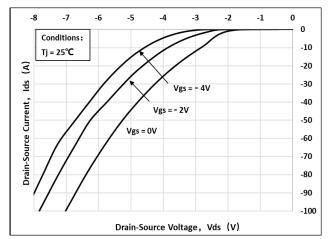


Fig 9: Body-diode Characteristics (T_J=175℃)

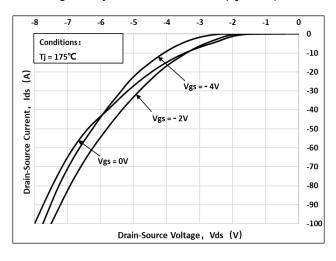


Fig 10: V_{TH} Vs T_J Temperature Characteristics

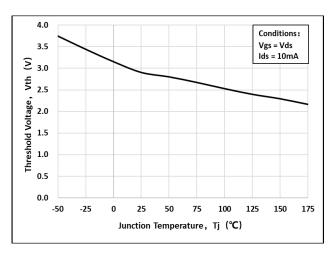


Fig 11: Gate Charge Characteristics

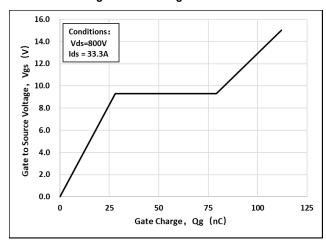


Fig 12: 3rd Quadrant Characteristics (T_J=-55°C)

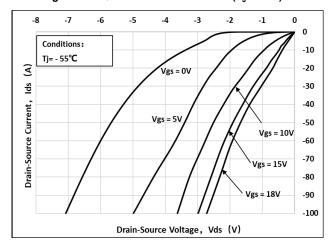




Fig 13: 3rd Quadrant Characteristics (T_J=25°C)

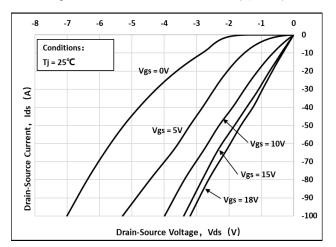


Fig 14: 3rd Quadrant Characteristics (T_J=175°C)

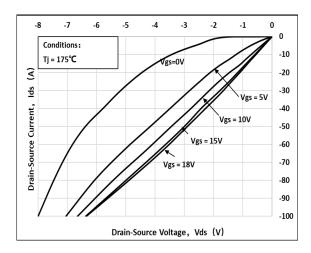


Fig 15: Capacitance Characteristics

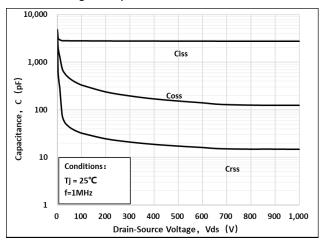


Fig 16: Safe Operating Area

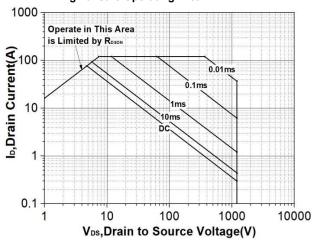
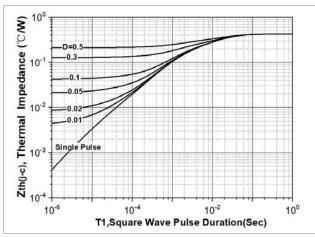
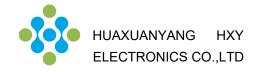


Fig 17: Transient Thermal Impedance





Test Circuit & Waveform

Figure A. Definition of switching times

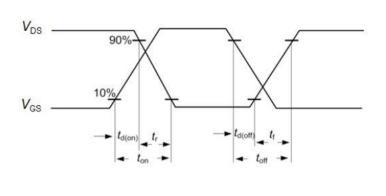


Figure B. Dynamic test circuit

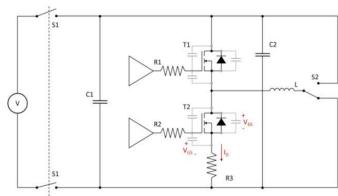


Figure C. Definition of body diode switching characteristics

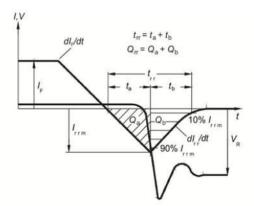
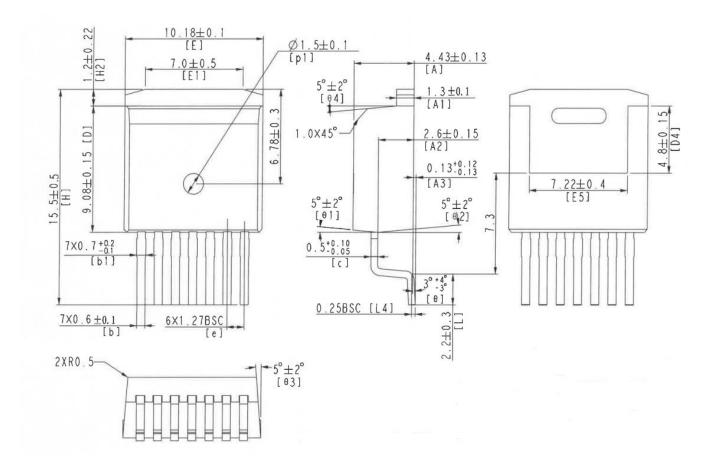


Figure C. Definition of diode switching characteristics



Package Dimensions

Package TO-263-7L





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