

Features

- Low on resistance
- Low reverse transfer capacitances
- 100% single pulse avalanche energy test
- 100% ΔV_{DS} test
- Pb-Free plating / Halogen-Free / RoHS compliant

Key Parameters

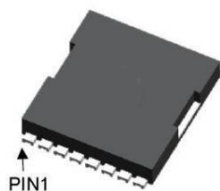
V_{DS}	40V
$R_{DS(on)typ.}$	0.5mΩ
I_D (Silicon limit)	475A
I_D (Package limit)	360A
V_{th}	2.9V
$C_{iss@10V}$	9669pF
Q_{gd}	11.5nC

Applications

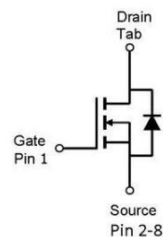
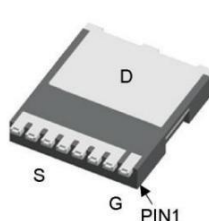
- Motor Control and Drive
- Charge/Discharge for Battery Management System
- Synchronous Rectifier for SMPS
- Automotive application



TOLL Top View



TOLL Bottom View



Marking & Packing Information

Part #	Package	Marking	Tube/Reel	Qty(pcs)
SFT007N40C3	TOLL	007N40C3	Tape & Reel	2000/box

Absolute Maximum Ratings

Parameter	Symbol	Value	Unit
Drain-source voltage	V_{DS}	40	V
Gate-Source voltage	V_{GS}	±20	V
Continuous drain current $T_C = 25^{\circ}\text{C}$ (Silicon limit) $T_C = 25^{\circ}\text{C}$ (Package limit) $T_C = 100^{\circ}\text{C}$ (Package limit)	I_D	475 360 336	A
Pulsed drain current ($T_C = 25^{\circ}\text{C}$, t_p limited by T_{jmax})	$I_{D\ pulse}$	1440	A
Avalanche energy, single pulse ($L=0.5\text{mH}$, $R_g=25\Omega$)	E_{AS}	2209	mJ
Power dissipation $T_C = 25^{\circ}\text{C}$ $T_A = 25^{\circ}\text{C}$	P_{tot}	333	W
		2.3	W
Operating junction and storage temperature	T_j, T_{stg}	-55...+175	$^{\circ}\text{C}$

Thermal Resistance

Parameter	Symbol	Max	Unit
Thermal resistance, junction – case.	R_{thJC}	0.45	$^{\circ}\text{C/W}$
Thermal resistance, junction – ambient(min. footprint)	R_{thJA}	65	

Electrical Characteristic (at $T_j = 25^{\circ}\text{C}$, unless otherwise specified)

Static Characteristic

Parameter	Symbol	Value			Unit	Test Condition
		min.	typ.	max.		
Drain-source breakdown voltage	BV_{DSS}	40	-	-	V	$V_{GS}=0\text{V}$, $I_D=250\mu\text{A}$
Gate threshold voltage	$V_{GS(th)}$	2.5	2.9	3.5	V	$V_{DS}=V_{GS}$, $I_D=250\mu\text{A}$
Zero gate voltage drain current	I_{DSS}	-	-	1	μA	$V_{DS}=40\text{V}$, $V_{GS}=0\text{V}$ $T_j=25^{\circ}\text{C}$
		-	-	100		$T_j=125^{\circ}\text{C}$
Gate-source leakage current	I_{GSS}	-	-	100	nA	$V_{GS}=20\text{V}$, $V_{DS}=0\text{V}$
Drain-source on-state resistance	$R_{DS(on)}$	-	0.5	0.7	mΩ	$V_{GS}=10\text{V}$, $I_D=100\text{A}$, $T_j=25^{\circ}\text{C}$

Dynamic Characteristic

Parameter	Symbol	Value			Unit	Test Condition
		min.	typ.	max.		
Input Capacitance	C_{iss}	-	9669	-	pF	$V_{GS}=0V$, $V_{DS}=20V$, $f=1MHz$
Output Capacitance	C_{oss}	-	5403	-		
Reverse Transfer Capacitance	C_{rss}	-	171	-		
Gate Total Charge	Q_G	-	118	-	nC	$V_{GS}=10V$, $V_{DS}=20V$, $I_D=50A$, $f=1MHz$
Gate-Source charge	Q_{gs}	-	49	-		
Gate-Drain charge	Q_{gd}	-	11.5	-		
Turn-on delay time	$t_{d(on)}$	-	21	-	ns	$V_{GS}=10V$, $V_{DD}=20V$, $R_{G_ext}=3\Omega$, $I_D=100A$
Rise time	t_r	-	64	-		
Turn-off delay time	$t_{d(off)}$	-	85	-		
Fall time	t_f	-	112	-		

Body Diode Characteristic

Parameter	Symbol	Value			Unit	Test Condition
		min.	typ.	max.		
Diode Max Current	I_S		-	360	A	-
Diode Forward Voltage	V_{SD}	-	-	1.2	V	$V_{GS}=0V$, $I_{SD}=100A$
Diode Reverse Recovery Time	t_{rr}	-	86	-	ns	$T_j=25^\circ C$, $I_F=20A$, $dI/dt=100A/\mu s$
Diode Reverse Recovery Charge	Q_{rr}	-	153	-	nC	

Typical Characteristics Diagram

Fig1. Output Characteristics

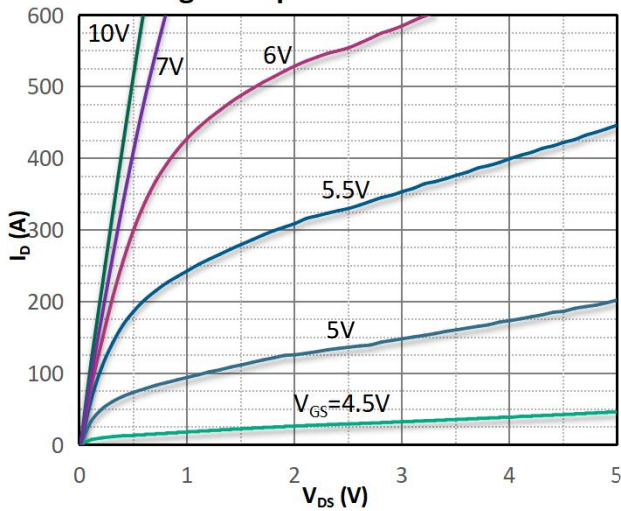


Fig2. Transfer Characteristics

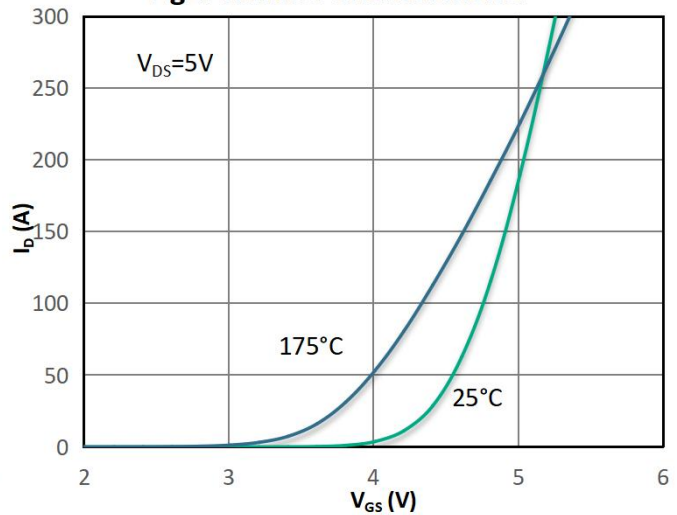


Fig3. Rds(on) vs Drain Current

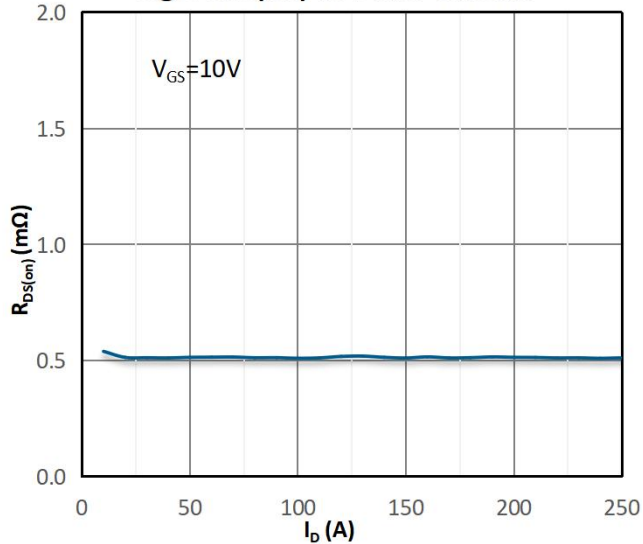


Fig 4. Rds(on) vs Gate Voltage

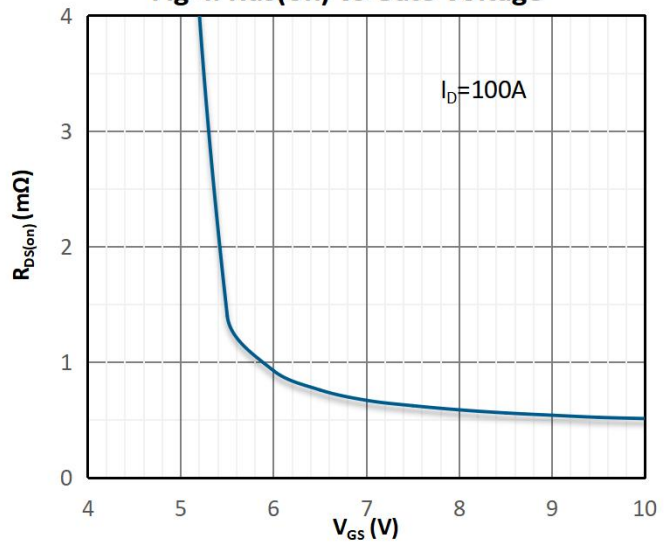


Fig5. Rds(on) vs. Temperature

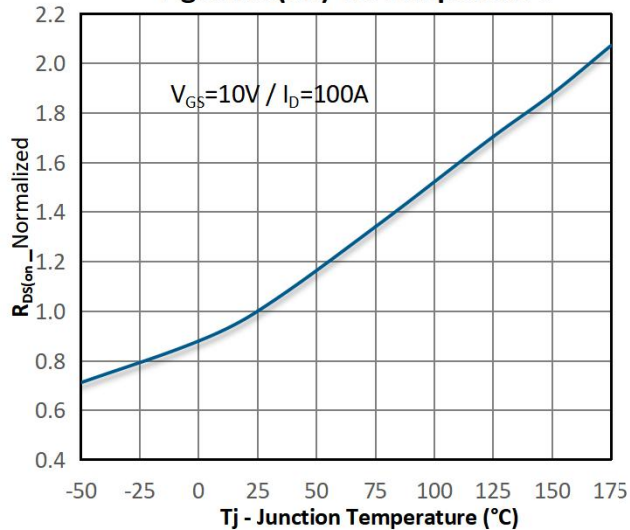
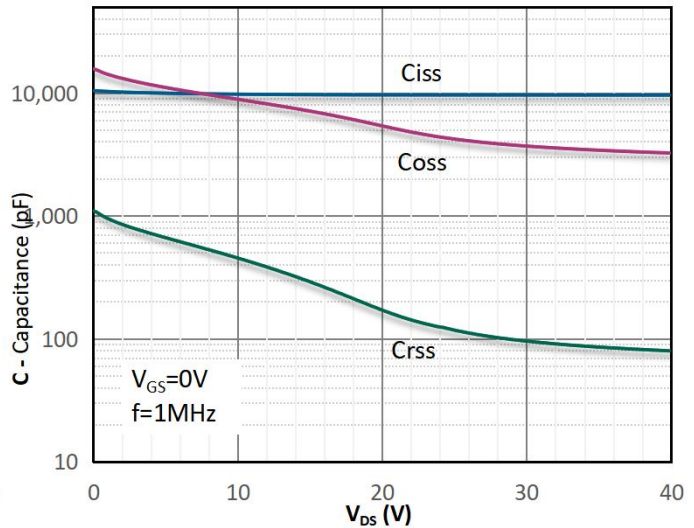


Fig6. Capacitance Characteristics



Typical Characteristics Diagram

Fig7. Gate Charge Characteristics

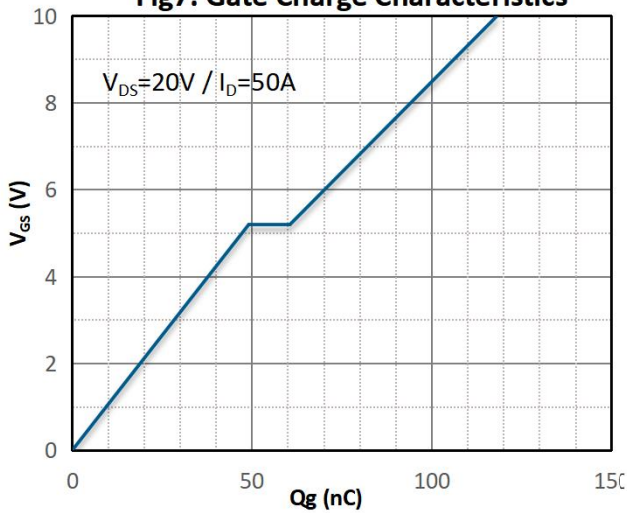


Fig8. Body-diode Forward Characteristics

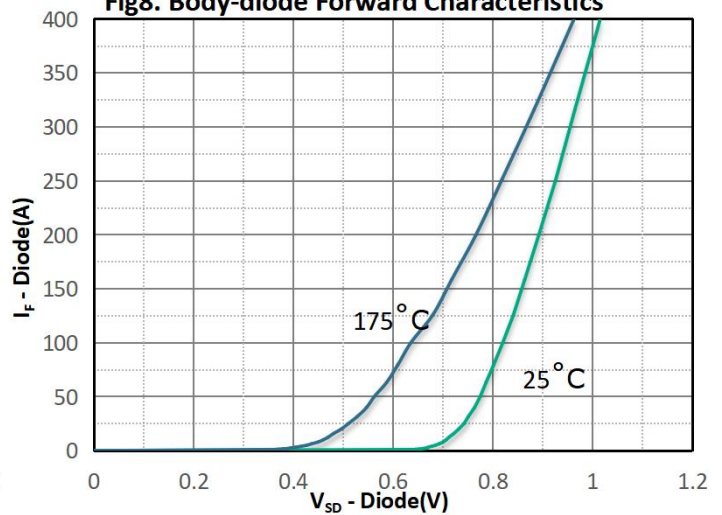


Fig9. Power De-rating

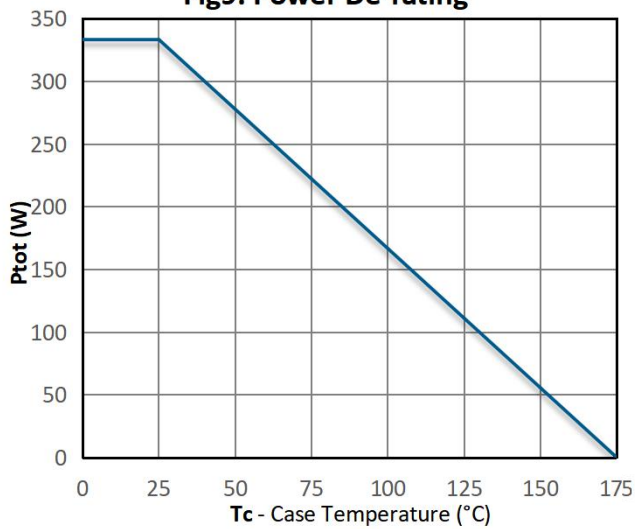


Fig10. Current De-rating

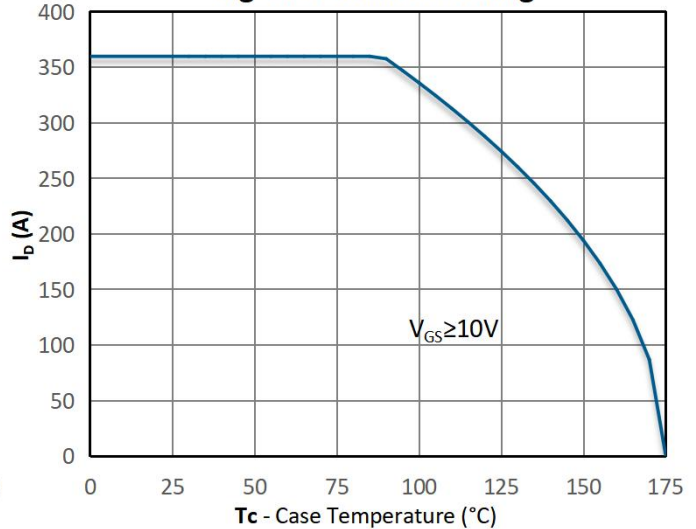


Fig11. Safe Operating Area

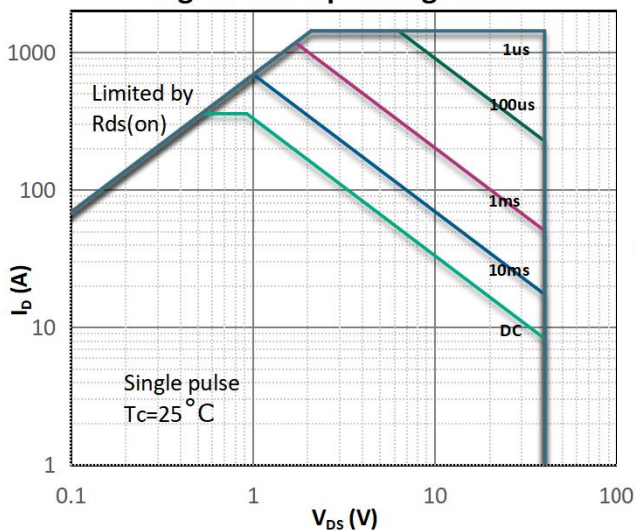
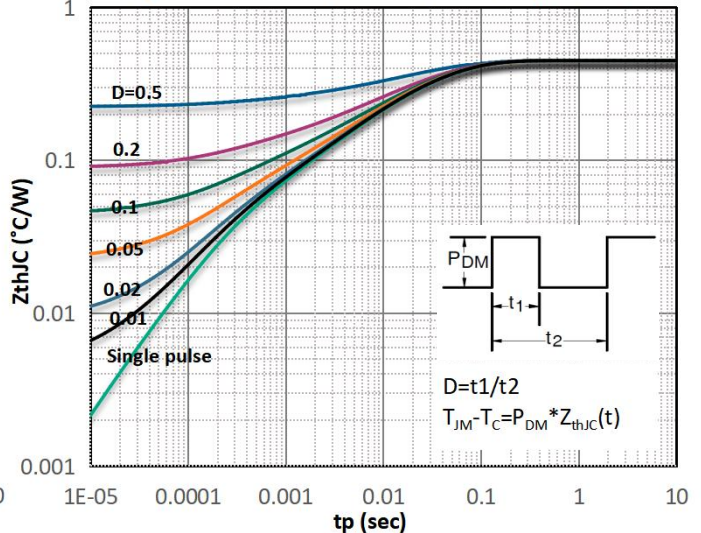
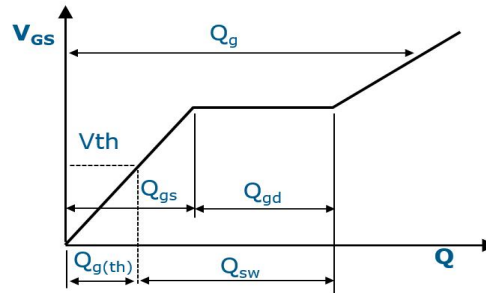
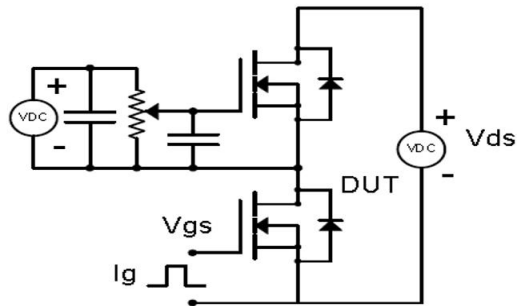


Fig12. Max. Transient Thermal Impedance

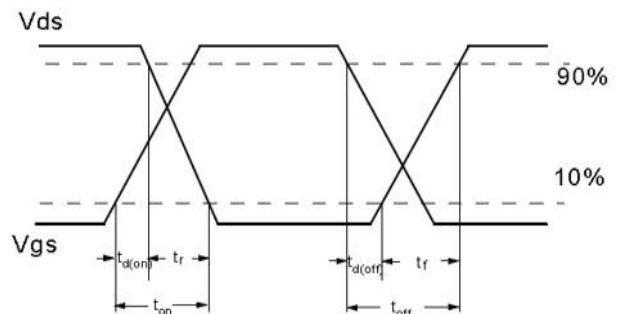
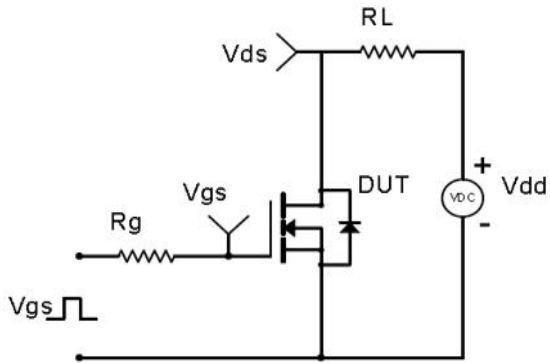


Test Circuit & Waveform

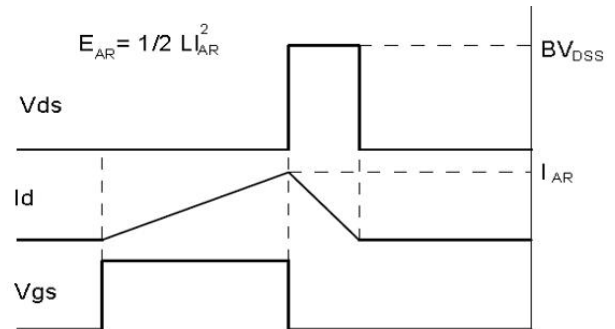
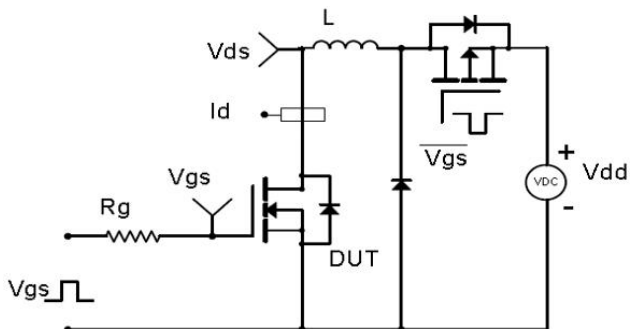
Gate Charge Test Circuit & Waveform



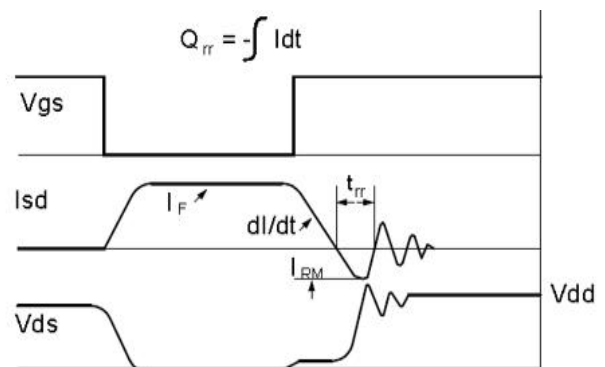
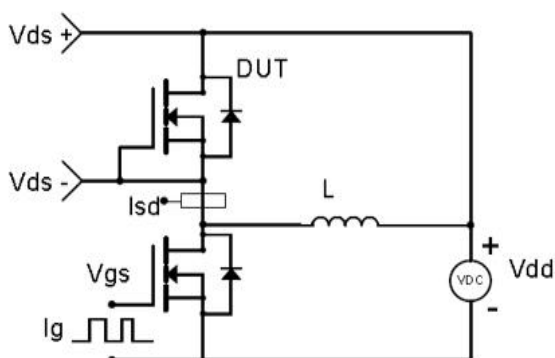
MOSFET Switching Test Circuit & Waveform



E_{AS} Test Circuit & Waveform



Diode Recovery Test Circuit & Waveform



Package Outline : TOLL

