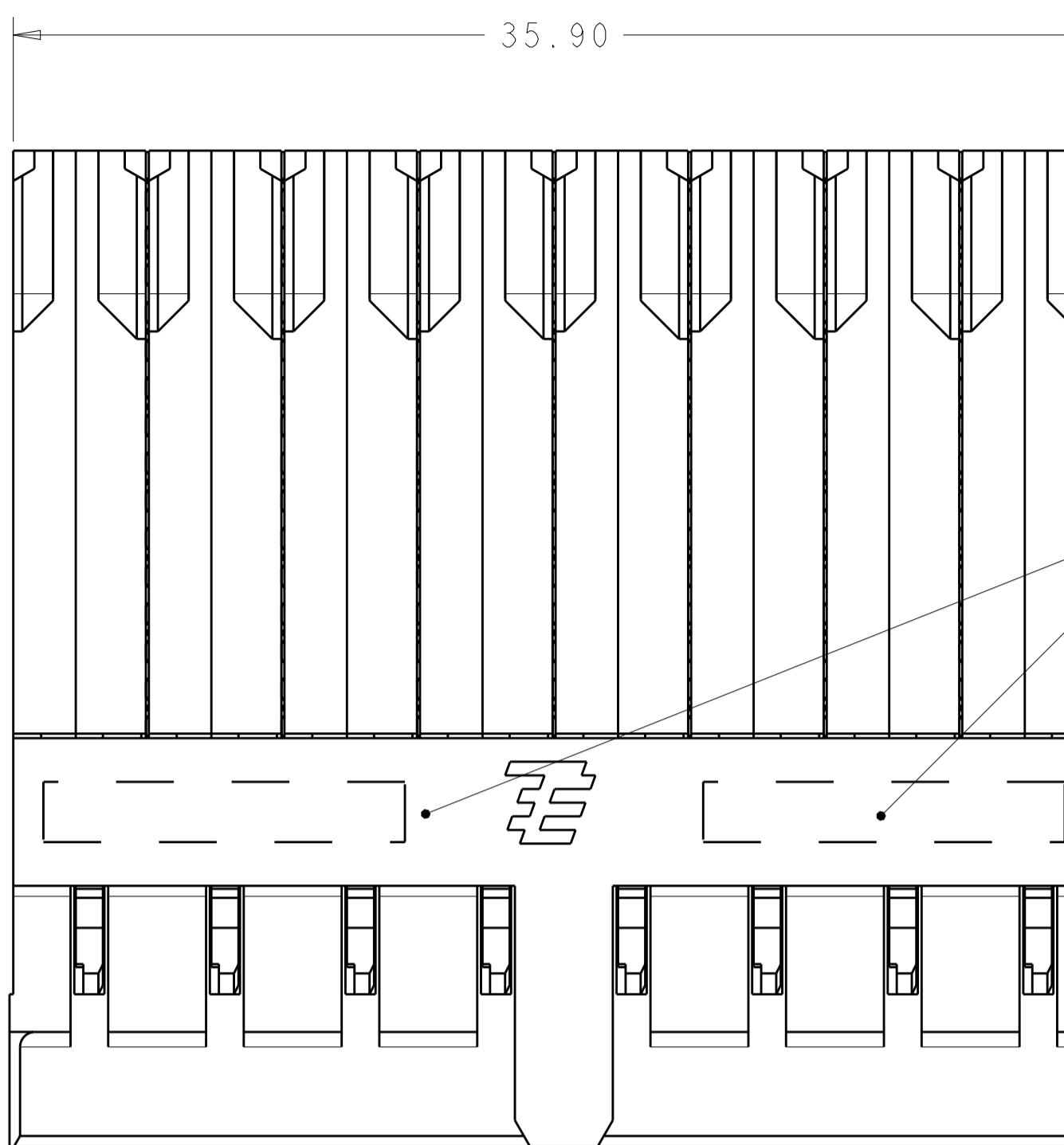
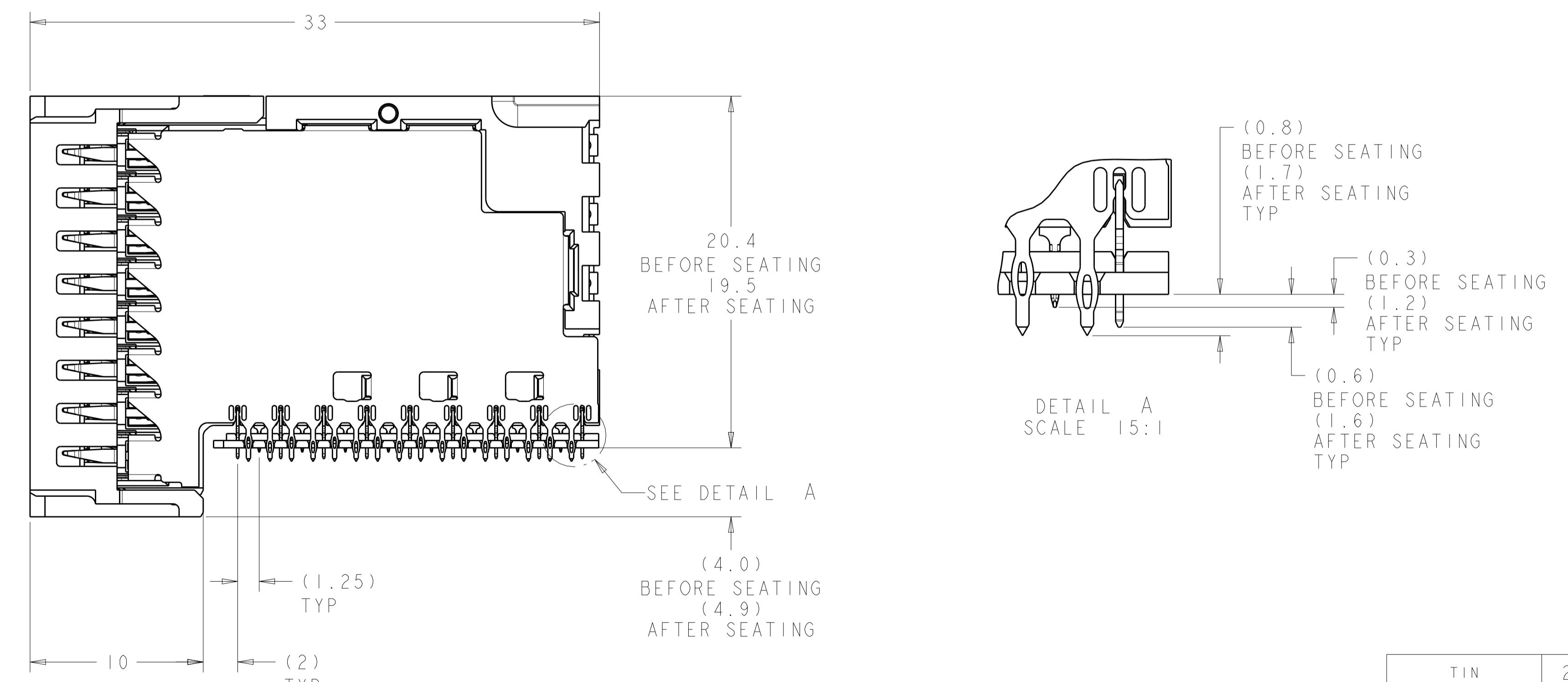
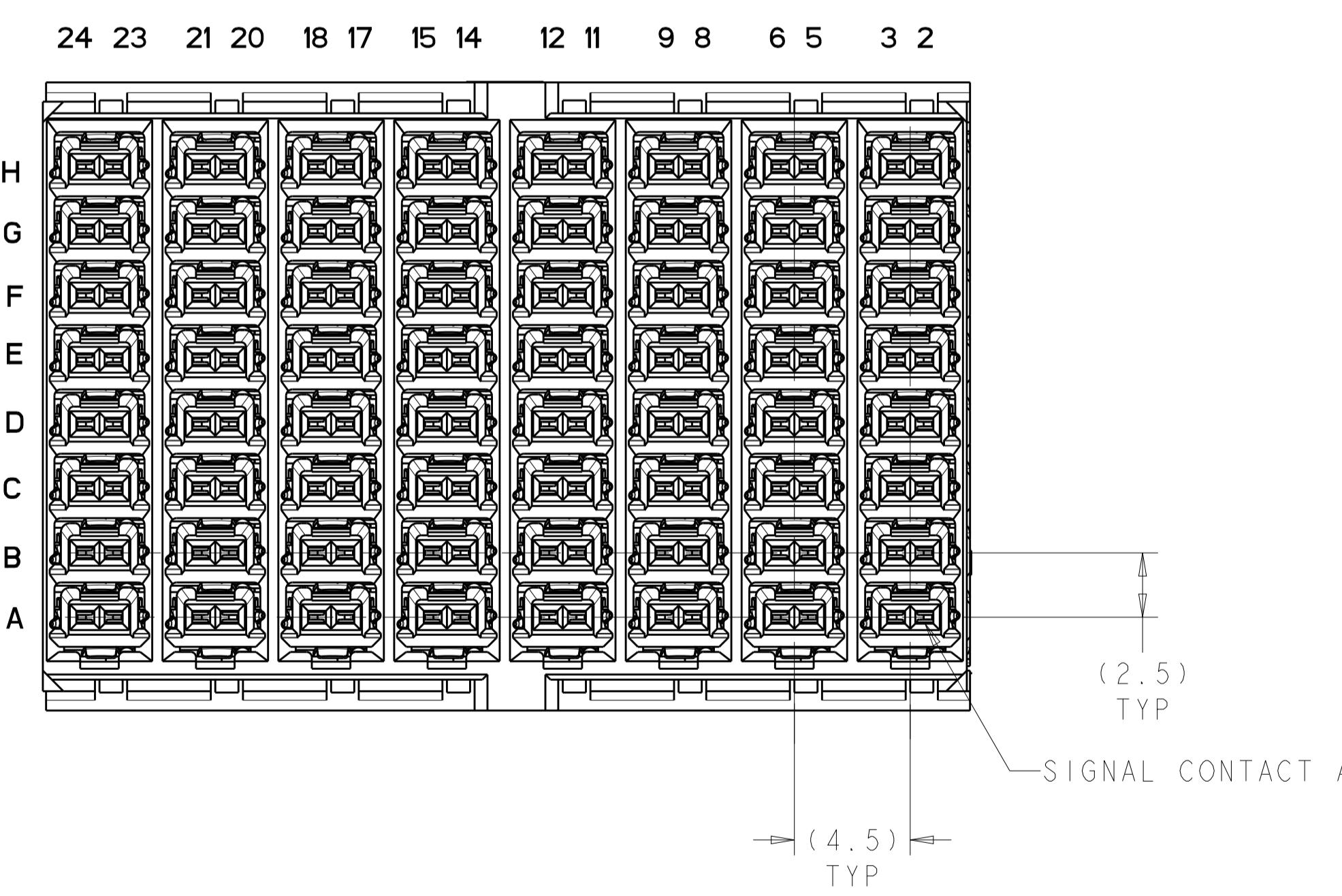


LOC	DIST	REVISIONS						
		GP	00	P LTR	DESCRIPTION	DATE	DNW	APV0
				A	RELEASED	23MAR2014	RT	NS
				B	REV PER ECO-14-010320	07JUL2014	LAW	NW
				C	REVISED PER ECO-17-006848	10MAY2017	JM	SS



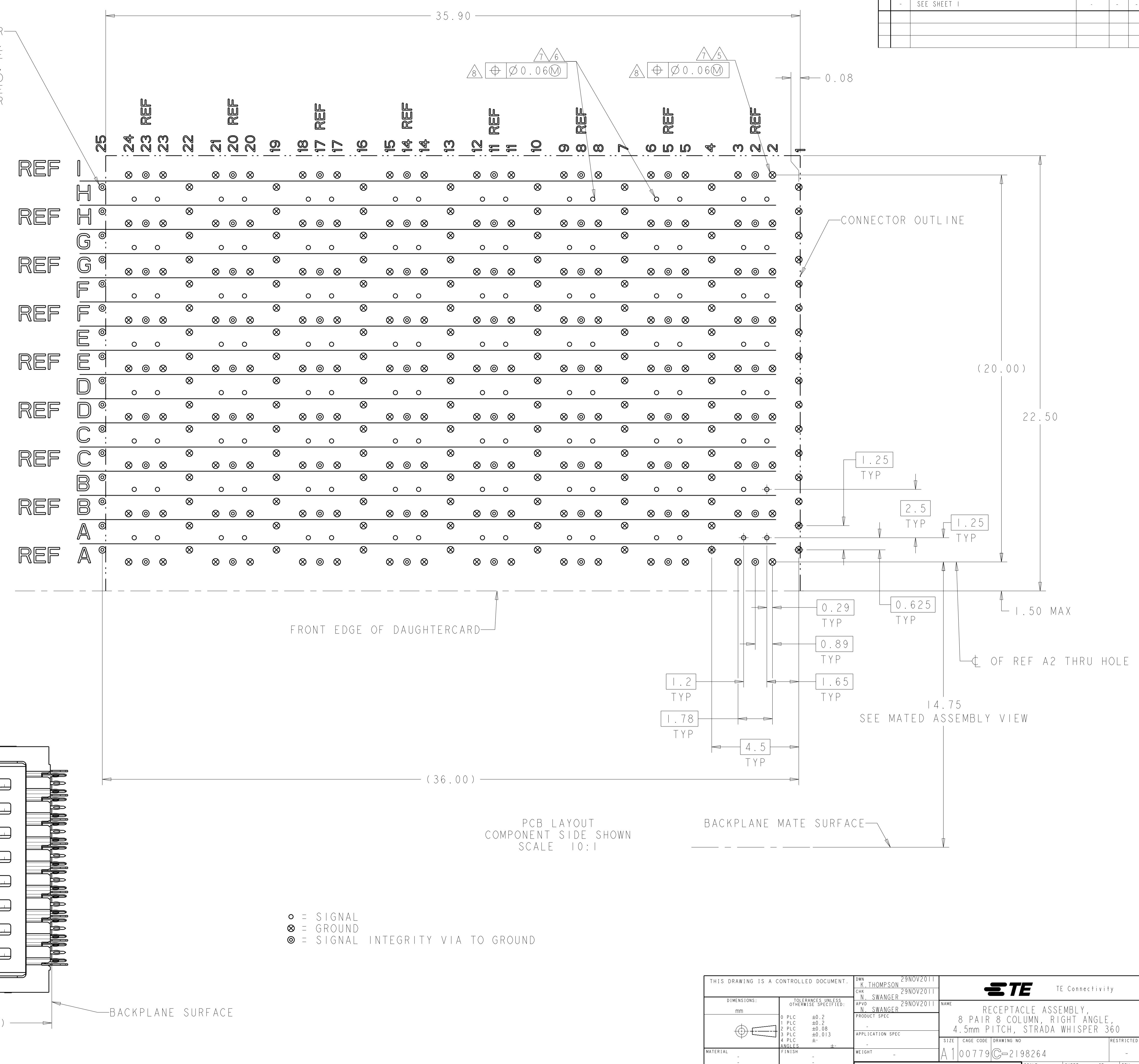
- ⚠ MATERIAL:  
CONTACT AND SHIELD - COPPER ALLOY.  
HOUSINGS, AND ORGANIZER - THERMOPLASTIC, UL 94V-0, BLACK.
- ⚠ CONFORMS TO REQUIREMENTS OF TE PRODUCT SPECIFICATION 108-2413 & 108-32037,  
BASED ON TELCORDIA GR-1217-CORE FOR SYSTEM QUALITY LEVEL III;  
APPLICATIONS IN CONTROLLED ENVIRONMENTS (CENTRAL OFFICE).
- ⚠ CONNECTOR MARKED WITH PART NUMBER, DATE CODE,  
MANUFACTURING LOCATION IDENTIFICATION CODE, AND 2D BARCODE IN APPROXIMATE AREA SHOWN.
- 4. CONTACT AREA LUBRICATED WITH BELLCORE APPROVED LUBRICANT.  
TECHNICAL REFERENCE: GR-1217-CORE, ISSUE 2, DECEMBER 2008.
- ⚠ PLATED THRU HOLE REQUIREMENTS:  
FINISHED HOLE =  $\phi 0.344 \pm 0.039$   
DRILLED HOLE =  $\phi 0.420 \pm 0.013$   
COPPER THICKNESS =  $0.038 \pm 0.013$   
IMMERSION TIN IF APPLICABLE =  $0.00051 \pm 0.00013$
- ⚠ PLATED THRU HOLE REQUIREMENTS:  
FINISHED HOLE =  $\phi 0.244 \pm 0.039$   
DRILLED HOLE =  $\phi 0.320 \pm 0.013$   
COPPER THICKNESS =  $0.038 \pm 0.013$   
IMMERSION TIN IF APPLICABLE =  $0.00051 \pm 0.00013$
- ⚠ PTH DIMENSIONS APPLY TO 1.00mm DEPTH FROM MOUNT SIDE OF PCB.
- ⚠ HOLE POSITION TOLERANCE APPLIES WITHIN PATTERN FOR CONNECTOR  
PATTERN TO CONNECTOR PATTERN, A  $0.12 \text{M}$  POSITION TOLERANCE APPLIES.



<b>OBSOLETE</b>	TIN	2198264-3
	TIN LEAD	2198264-1
	PRESS FIT TAIL PLATING	PART NO

THIS DRAWING IS A CONTROLLED DOCUMENT.	K. THOMPSON 29NOV2011	N. SWANGER 29NOV2011	TE Connectivity
DIMENSIONS: mm	0 PLC $\pm 0.2$	1 PLC $\pm 0.2$	RECEPTACLE ASSEMBLY,
	1 PLC $\pm 0.2$	2 PLC $\pm 0.08$	8 PAIR 8 COLUMN, RIGHT ANGLE,
	3 PLC $\pm 0.013$	4 PLC $\pm 0.013$	4.5mm PITCH, STRADA WHISPER 360
MATERIAL	FINISH	ANGLES $\pm 1^\circ$	SIZE CAGE CODE DRAWING NO. RESTRICTED TO
			A 100779 C=2198264
		SEE TABLE	Customer Drawing
			SCALE 5:1 SHEET 1 OF 2 REV C

FOOTPRINT SHOWN IS FOR  
SINGLE MODULE APPLICATION  
WHEN CONNECTORS ARE  
STACKED SIDE BY SIDE  
THESE SIL VIAS ARE USED  
AS GROUNDS BY THE  
NEXT CONNECTOR



## DAUGHTER CARD

○ = SIGNAL  
⊗ = GROUND  
◎ = SIGNAL INTEGRITY VIA TO GROUND

Technical drawing showing a dimension of 14.75 between two points. A note on the left indicates a thru-hole, and a note on the right indicates the backplane surface.

THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN K. THOMPSON 29NOV2011	TE Connectivity
		CHK N. SWANGER 29NOV2011	
DIMENSIONS: mm	TOLERANCES UNLESS OTHERWISE SPECIFIED:	APVD N. SWANGER 29NOV2011	NAME  RECEPTACLE ASSEMBLY, 8 PAIR 8 COLUMN, RIGHT ANGLE, 4.5mm PITCH, STRADA WHISPER 360
	0 PLC ±0.2 1 PLC ±0.2 2 PLC ±0.08 3 PLC ±0.013 4 PLC ±- ANGLES ±-	PRODUCT SPEC - APPLICATION SPEC -	
MATERIAL -	FINISH -	WEIGHT -	SIZE CAGE CODE DRAWING NO A1 00779 C-2198264 RESTRICTED TO -
Customer Drawing			SCALE 5:1 SHEET ? OF ? REV C