

General Description

The CMD482/CMU482 uses advanced trench technology and design to provide excellent RDS(ON). This device is ideal for boost converters and synchronous rectifiers for consumer, telecom, industrial power supplies and LED backlighting.

Features

- Low On-Resistance
- 100% avalanche tested
- RoHS Compliant

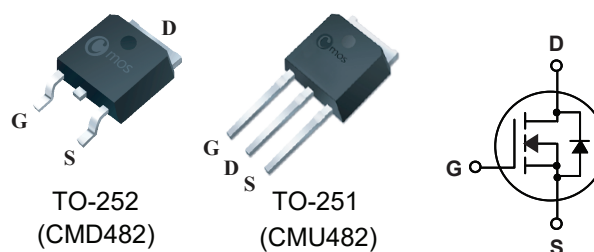
Product Summary

BVDSS	RDS(on) max.	ID
100V	42mΩ	32A

Applications

- LED controller
- Power Supplies
- DC-DC Converters

TO-252/251 Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	100	V
V _{GS}	Gate-Source Voltage	±20	V
I _D @T _C =25°C	Continuous Drain Current	32	A
I _D @T _C =100°C	Continuous Drain Current	22	A
I _{DM}	Pulsed Drain Current	70	A
EAS	Single Pulse Avalanche Energy ¹	156	mJ
P _D @T _C =25°C	Total Power Dissipation	100	W
T _{STG}	Storage Temperature Range	-55 to 150	°C
T _J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
R _{θJA}	Thermal Resistance Junction-ambient	---	50	°C/W
R _{θJC}	Thermal Resistance Junction -Case	---	1.25	°C/W

Electrical Characteristics ($T_J=25^{\circ}\text{C}$, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DS}	Drain-Source Breakdown Voltage	$V_{GS}=0V$, $I_D=250\mu A$	100	---	---	V
$R_{DS(ON)}$	Static Drain-Source On-Resistance	$V_{GS}=10V$, $I_D=12A$	---	32	42	mΩ
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$, $I_D=250\mu A$	1	---	2.5	V
I_{DSS}	Drain-Source Leakage Current	$V_{DS}=100V$, $V_{GS}=0V$, $T_J=25^{\circ}\text{C}$	---	---	1	μA
		$V_{DS}=100V$, $V_{GS}=0V$, $T_J=55^{\circ}\text{C}$	---	---	5	
I_{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 20V$, $V_{DS}=0V$	---	---	±100	nA
g_{fs}	Forward Transconductance	$V_{DS}=10V$, $I_D=30A$	---	25	---	S
R_g	Gate Resistance	$V_{DS}=0V$, $V_{GS}=0V$, $f=1\text{MHz}$	---	2	---	Ω
Q_g	Total Gate Charge	$V_{DS}=50V$, $V_{GS}=10V$, $I_D=10A$	---	35	---	nC
Q_{gs}	Gate-Source Charge		---	6	---	
Q_{gd}	Gate-Drain Charge		---	9	---	
$T_{d(on)}$	Turn-On Delay Time	$V_{DS}=50V$, $V_{GS}=10V$, $R_L=5\Omega$ $R_{GEN}=3\Omega$	---	18	---	ns
T_r	Rise Time		---	15	---	
$T_{d(off)}$	Turn-Off Delay Time		---	40	---	
T_f	Fall Time		---	16	---	
C_{iss}	Input Capacitance	$V_{DS}=25V$, $V_{GS}=0V$, $f=1\text{MHz}$	---	2600	---	pF
C_{oss}	Output Capacitance		---	150	---	
C_{rss}	Reverse Transfer Capacitance		---	100	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_S	Continuous Source Current	$V_G=V_D=0V$, Force Current	---	---	32	A
V_{SD}	Diode Forward Voltage	$V_{GS}=0V$, $I_S=30A$, $T_J=25^{\circ}\text{C}$	---	---	1.2	V

Note :

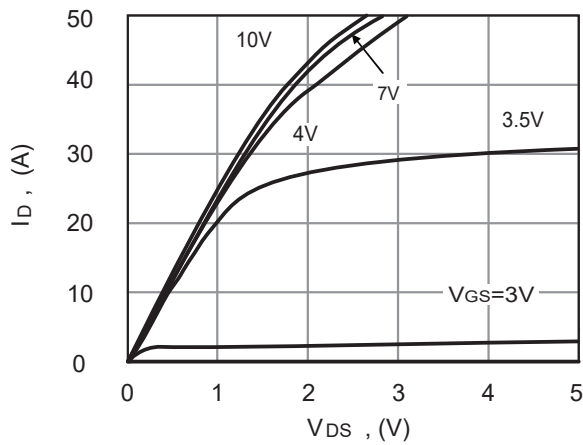
1.The test condition is $V_{DD}=50V$, $V_{GS}=10V$, $L=0.5\text{mH}$, $I_D=25A$

This product has been designed and qualified for the consumer market.

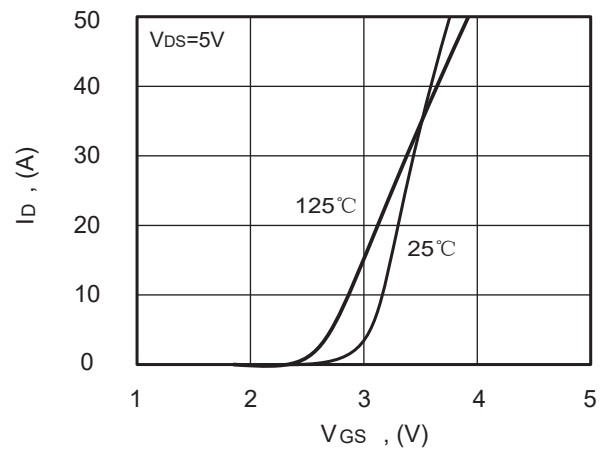
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Cmos reserves the right to improve product design ,functions and reliability without notice.Please refer to the latest version of specification.

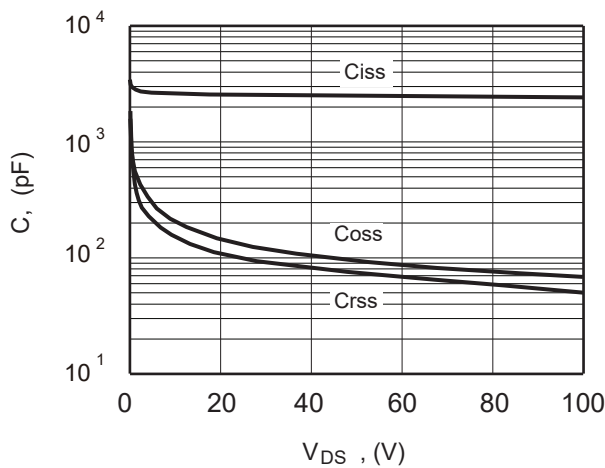
Typical Characteristics



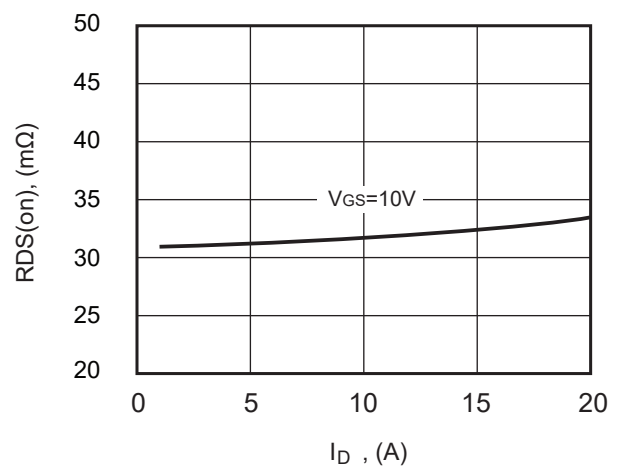
Output Characteristics



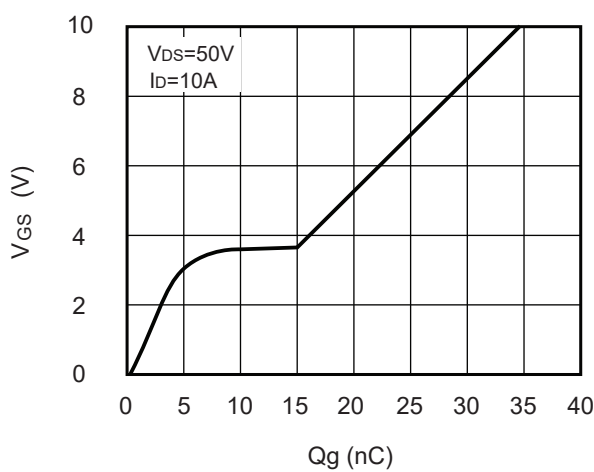
Transfer Characteristics



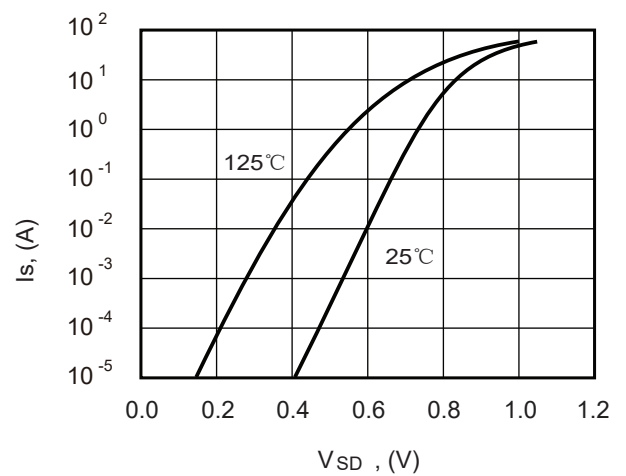
Capacitance Characteristics



On-Resistance vs. Drain Current



Gate-Charge Characteristics

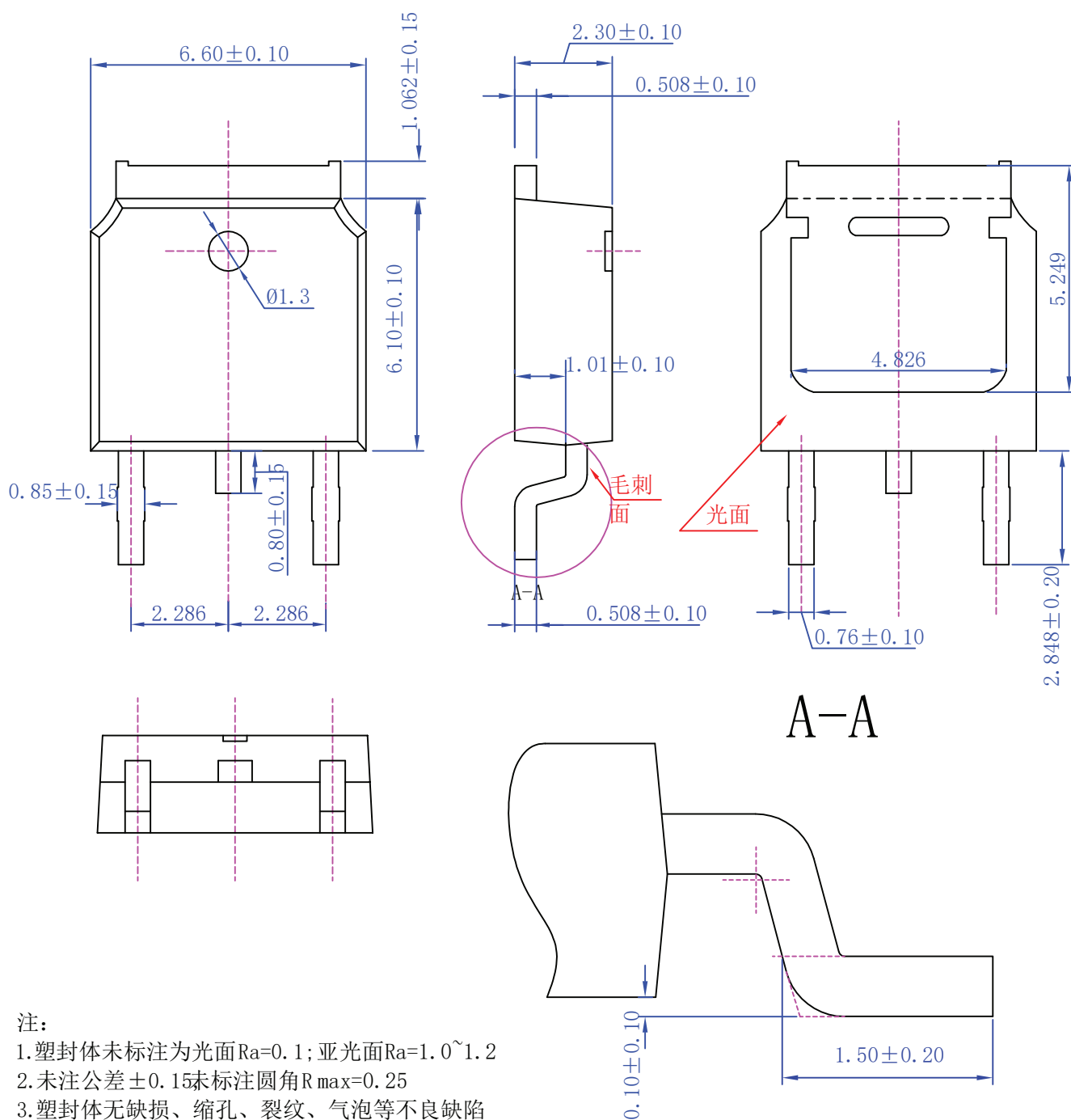


Body-Diode Characteristics

Package Dimension

TO-252

Unit :mm



Package Dimension

TO-251A

Unit :mm

