

DATA SHEET

SURFACE-MOUNT CERAMIC MULTILAYER CAPACITORS

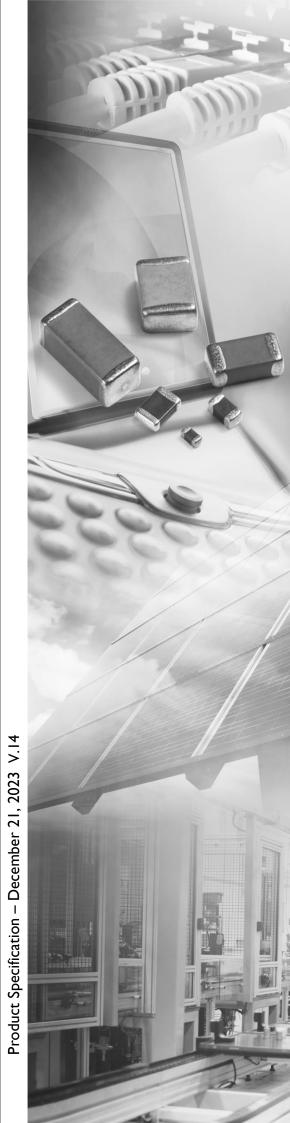
General purpose & High capacitance Class 2, Y5V

6.3 V TO 50 V

10~nF to $47~\mu F$ RoHS compliant & Halogen Free



YAGEO



SCOPE

This specification describes Y5V series chip capacitors with leadfree terminations.

<u>APPLICATIONS</u>

Consumer electronics, for example:

- Tuners
- Television receivers
- Video recorders
- All types of cameras
- Mobile telephones

FEATURES

Supplied in tape on reel Nickel-barrier end termination RoHS compliant Halogen Free compliant

ORDERING INFORMATION-GLOBAL PART NUMBER, PHYCOMP CTC & 12NC

All part numbers are identified by the series, size, tolerance, TC material, packing style, voltage, process code, termination and capacitance value.

YAGEO BRAND ordering code

GLOBAL PART NUMBER (PREFERRED)

CC	XXXX	<u>X</u>	<u>X</u>	Y5V	<u>X</u>	BB	<u>XXX</u>
	(1)	(2)	(3)		(4)		(5)

(I) SIZE - INCH BASED (METRIC) 0201 (0603)

0402 (1005) 0603 (1608)

0805 (2012)

1206 (3216)

1210 (3225)

(2) TOLERANCE

 $M = \pm 20\%$

Z = -20% to +80%

(3) PACKING STYLE

R = Paper/PE taping reel; Reel 7 inch

K = Blister taping reel; Reel 7 inch

P = Paper/PE taping reel; Reel 13 inch

F = Blister taping reel; Reel 13 inch

(4) RATED VOLTAGE

5 = 6.3 V

6 = 10 V

7 = 16 V

8 = 25 V

9 = 50 V

(5) CAPACITANCE VALUE

2 significant digits+number of zeros

The 3rd digit signifies the multiplying factor, and letter R is decimal point

Example: $103 = 10 \times 10^3 = 10,000 \text{ pF} = 10 \text{ nF}$

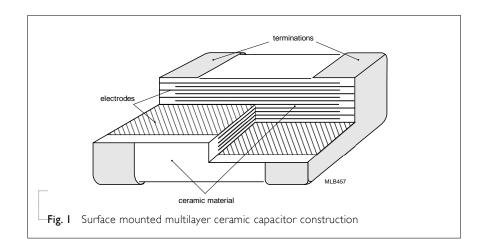


CONSTRUCTION

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The capacitor consists of a rectangular block of ceramic dielectric in which a number of interleaved metal electrodes are contained. This structure gives rise to a high capacitance per unit volume.

The inner electrodes are connected to the two end terminations and finally covered with a layer of plated tin (NiSn). The terminations are lead-free. A cross section of the structure is shown in Fig.I.

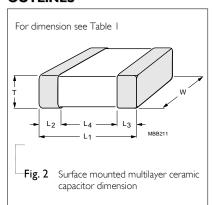


DIMENSION

Table I For outlines see fig. 2

TYPE L _I (mm)		W (mm)	T (MM)	L ₂ / L ₃ (mm)		L ₄ (mm)
IIFE	L ₁ (((((())))		T (MM)	min.	max.	min.
0201	0.6 ±0.03	0.3 ±0.03	_	0.10	0.20	0.20
0402	1.0 ±0.05	0.5 ±0.05	_	0.15	0.35	0.30
0603	1.6 ±0.10	0.8 ±0.10	_	0.20	0.60	0.40
0805	2.0 ±0.10 ⁽¹⁾	1.25 ±0.10 ⁽¹⁾		0.25	0.75	0.70
0803	2.0 ±0.20 ⁽²⁾	1.25 ±0.20 ⁽²⁾	_	0.23	0.73	0.70
1206	3.2 ±0.15 ⁽¹⁾	1.6 ±0.15 ⁽¹⁾	Refer to	0.25	0.75	1.40
1206	3.2 ±0.30 ⁽²⁾	1.6 ±0.20 ⁽²⁾	table 2 to 4	0.23	0.73	1.40
1210	3.2 ±0.20 ^(I)	2.5 ±0.20 ^(I)		0.25	0.75	1.40
1210	3.2 ±0.40 ⁽²⁾	2.5 ±0.30 ⁽²⁾		0.25	0.75	1.40
1012	4.5 ±0.20 ⁽¹⁾	3.2 ±0.20 ^(I)	-	0.25	0.75	2.20
1812	4.5 ±0.40 ⁽²⁾	3.2 ±0.40 ⁽²⁾		0.25	0.75	2.20

OUTLINES



NOTE

- 1. Dimension for size 0805 to 1812, $C \le 100 \text{ nF}$
- 2. Dimension for size 0805 to 1812, C > 100 nF

CAPACITANCE RANGE & THICKNESS FOR Y5V

Table 2 Sizes from 0201 to 0402

CAP.	0201		0402				
	6.3 V	25 V	6.3 V	10 V	16 V	25 V	50 V
10 nF		0.3±0.03		0.5±0.05	0.5±0.05	0.5±0.05	0.5±0.05
22 nF				0.5±0.05	0.5±0.05	0.5±0.05	
47 nF				0.5±0.05	0.5±0.05	0.5±0.05	
100 nF	0.3±0.03		0.5±0.05	0.5±0.05	0.5±0.05	0.5±0.05	
220 nF			0.5±0.05	0.5±0.05	0.5±0.05		
470 nF			0.5±0.05	0.5±0.05	0.5±0.05		
1.0 µF			0.5±0.05	0.5±0.05			
2.2 µF							
4.7 µF							
IOμF							
22 µF							
47 µF							

Table 3 Sizes from 0603 to 0805

CAP.	0603					0805				
	6.3 V	10 V	16 V	25 V	50 V	6.3 V	10 V	16 V	25 V	50 V
10 nF				0.8±0.1	0.8±0.1				0.6±0.1	0.6±0.1
22 nF				0.8±0.1	0.8±0.1				0.6±0.1	0.6±0.1
47 nF				0.8±0.1	0.8±0.1				0.6±0.1	0.6±0.1
100 nF			0.8±0.1	0.8±0.1	0.8±0.1				0.6±0.1	0.6±0.1
220 nF			0.8±0.1	0.8±0.1	0.8±0.1			0.6±0.1	0.85±0.1	0.85±0.1
470 nF			0.8±0.1	0.8±0.1				0.85±0.1	0.85±0.1	0.85±0.1
Ι.0 μF	0.8±0.1	0.8±0.1	0.8±0.1	0.8±0.1				0.85±0.1	0.85±0.1	1.25±0.2
2.2 μF	0.8±0.1	0.8±0.1	0.8±0.1			0.85±0.1	0.85±0.1	0.85±0.1	1.25±0.2	
4.7 µF	0.8±0.1	0.8±0.1				0.85±0.1	0.85±0.1	1.25±0.2		
Ι0 μF						1.25±0.2	1.25±0.2			
22 µF						1.25±0.2	1.25±0.2			
47 μF										

NOTE

- 1. Values in shaded cells indicate thickness class in mm
- 2. Capacitance value of non E-3 series is on request







SERIES 6.3 V to 50 V

CAPACITANCE RANGE & THICKNESS FOR Y5V

Table 4	Sizes from	1206 to	1210

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CAP.	1206					1210				
	6.3 V	10 V	16 V	25 V	50 V	6.3 V	10 V	16 V	25 V	50V
10 nF				0.6±0.1	0.6±0.1					
22 nF				0.6±0.1	0.6±0.1					
47 nF				0.6±0.1	0.6±0.1					
100 nF				0.6±0.1	0.6±0.1					
220 nF				0.6±0.1	0.6±0.1					
470 nF				0.85±0.1	0.85±0.1					
Ι.0 μF			0.85±0.1	0.85±0.1	0.85±0.1					
2.2 µF		0.85±0.1	0.85±0.1	0.85±0.1						
4.7 µF		0.85±0.1	0.85±0.1							
ΙΟ μF	0.85±0.1	0.85±0.1	1.15±0.1	1.6±0.2		1.5±0.1	1.5±0.1	1.5±0.1	1.5±0.1	1.5±0.1
22 µF	1.6±0.2	1.6±0.2	1.6±0.2			1.6±0.2	1.6±0.2	1.6±0.2		
47 µF										

NOTE

- 1. Values in shaded cells indicate thickness class in mm
- 2. Capacitance value of non E-3 series is on request



THICKNESS CLASSES AND PACKING QUANTITY

	Tah	l۵	5
1	·uo		•

Table 3		T4 DE 14 (ID T1)	Ø180 MM	/ 7 INCH	Ø330 MM	/ 13 INCH	OLIAN ITITY
SIZE CODE	THICKNESS CLASSIFICATION	TAPE WIDTH – QUANTITY PER REEL	Paper	Blister	Paper	Blister	QUANTITY PER BULK CASE
0201	0.3 ±0.03 mm	8 mm	15,000		50,000		
0402	0.5 ±0.05 mm	8 mm	10,000		50,000		50,000
0603	0.8 ±0.1 mm	8 mm	4,000		15,000		15,000
	0.6 ±0.1 mm	8 mm	4,000		20,000		10,000
0805	0.85 ±0.1 mm	8 mm	4,000		15,000		8,000
	1.25 ±0.2 mm	8 mm		3,000		10,000	5,000
	0.6 ±0.1 mm	8 mm	4,000		20,000		
	0.85 ±0.1 mm	8 mm	4,000		15,000		
1204	1.00 / 1.15 ±0.1 mm	8 mm		3,000		10,000	
1206	1.25 ±0.2 mm	8 mm		3,000		10,000	
	1.6 ±0.15 mm	8 mm		2,500		10,000	
	1.6 ±0.2 mm	8 mm		2,000		10,000	
	0.6 / 0.7 ±0.1 mm	8 mm		4,000		15,000	
	0.85 ±0.1 mm	8 mm		4,000		10,000	
	1.15 ±0.1 mm	8 mm		3,000		10,000	
	1.15 ±0.15 mm	8 mm		3,000		10,000	
	1.25 ±0.2 mm	8 mm		3,000			
1210	1.5 ±0.1 mm	8 mm		2,000			
	1.6 / 1.9 ±0.2 mm	8 mm		2,000			
	2.0 ±0.2 mm	8 mm		2,000 1,000			
	2.5 ±0.2 mm	8 mm		1,000 500			



ELECTRICAL CHARACTERISTICS

Y5V DIELECTRIC CAPACITORS; NISN TERMINATIONS

Unless otherwise specified, all test and measurements shall be made under standard atmospheric conditions for testing as given in 5.3 of IEC 60068-1:

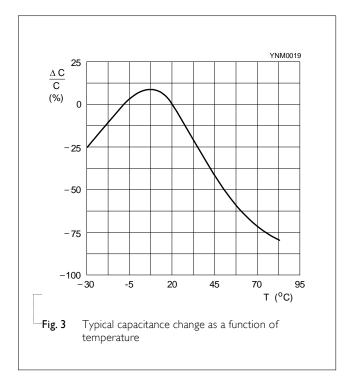
Temperature: 15 °C to 35 °C
Relative humidity: 25% to 75%
Air pressure: 86 kPa to 106 kPa

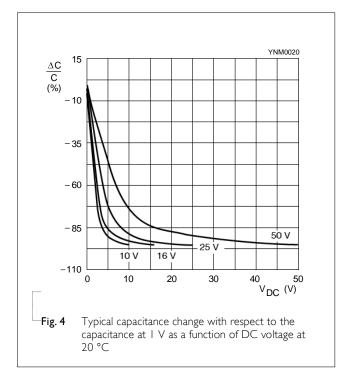
Before the measurements are made, the capacitor shall be stored at the measuring temperature for a time sufficient to allow the entire capacitor to reach this temperature.

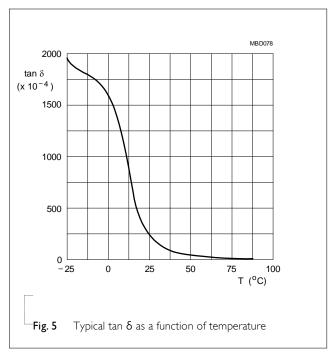
The period as prescribed for recovery at the end of a test is normally sufficient for this purpose.

Table 6			
DESCRIPTION			VALUE
Capacitance range			I0 nF to 22 μF
Capacitance tolerance			$\pm 20\%$ -20% to +80%
Dissipation factor (D.F.)	≤ 6.3 V		≤ 15%
		Exception:	0805 ≥ 22 μF ≤ 20%
	10 V		 ≤ I2.5%
		Exception:	0402 ≥ 680 nF; 0603 \geq 2.2 µF; 0805 = 10 µF; \leq 15%
			0805 ≥ 22 μF; 1206 ≥ 10 μF ≤ 20%
	16 V		≤ 12.5%
		Exception:	<u>0603</u> ≥ 4.7 μF ≤ 15%
			1206 ≥ 10 μ F ≤ 20%
	≥ 25 V		≤ 9%
		Exception:	0201 ≥ 10 nF ≤ 12.5%
Insulation resistance after	¹ I minute at l	J _r (DC)	Rins \geqslant 10 G Ω or Rins x Cr \geqslant 500 Ω .F whichever is less Rins x Cr \geqslant 100 Ω .F : 0603 , 4.7uF, 6.3V and 10V; 0805, 22uF, 10V; Rins x Cr \geqslant 50 Ω .F: 0805, 22uF, 6.3V;
Maximum capacitance ch	_	•	
(temperature characteris	tic/coefficient)	:	+22% to -82%
Operating temperature r	ange:		–30 °C to +85 °C









SOLDERING RECOMMENDATION

Table 7

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SOLDERING	SIZE					
METHOD	0201	0402	0603	0805	1206	≥ 1210
Reflow	Reflow only	> 100 nF	> 1.0 µF	> 2.2 µF	> 2.2 µF	Reflow only
Reflow/Wave		≤ 100 nF	≤ 1.0 µF	≤ 2.2 µF	≤ 2.2 µF	





Surface-Mount Ceramic Multilayer Capacitors Y5V

TESTS AND REQUIREMENTS

Table 8	Test procedures and requirements	ŝ
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TEST	TEST MET	HOD	PROCEDURE	REQUIREMENTS
Mounting	IEC 60384- 21/22	4.3	The capacitors may be mounted on printed-circuit boards or ceramic substrates	No visible damage
Visual inspection and dimension check		4.4	Any applicable method using × 10 magnification	In accordance with specification
Capacitance (1)		4.5.1	Class 2: At 20 °C, 24 hrs after annealing $f = 1 \text{ KHz for C} \leq 10 \mu\text{F, rated voltage} > 6.3 \text{ V, measuring at voltage } 1 \text{ V}_{ms} \text{ at } 20 \text{ °C}$ $f = 1 \text{ KHz, for C} \leq 10 \mu\text{F, rated voltage} \leq 6.3 \text{ V, measuring at voltage } 0.5 \text{ V}_{ms} \text{ at } 20 \text{ °C}$ $f = 120 \text{ Hz for C} > 10 \mu\text{F, measuring at voltage } 0.5 \text{ V}_{ms} \text{ at } 20 \text{ °C}$	Within specified tolerance
Dissipation factor (D.F.) (1)		4.5.2	Class 2: At 20 °C, 24 hrs after annealing $f = 1 \text{ KHz for C} \leq 10 \mu\text{F, rated voltage} > 6.3 \text{ V, measuring at voltage } 1 \text{ V}_{ms} \text{ at } 20 \text{ °C}$ $f = 1 \text{ KHz, for C} \leq 10 \mu\text{F, rated voltage} \leq 6.3 \text{ V, measuring at voltage } 0.5 \text{ V}_{ms} \text{ at } 20 \text{ °C}$ $f = 120 \text{ Hz for C} > 10 \mu\text{F, measuring at voltage } 0.5 \text{ V}_{ms} \text{ at } 20 \text{ °C}$	In accordance with specification
Insulation resistance		4.5.3	At U_r (DC) for I minute	In accordance with specification
Temperature characteristic		4.6	Class 2: Between minimum and maximum temperature Y5V: -30 °C to +85 °C Normal Temperature: 20 °C	<general purpose="" series=""> ΔC/C Class 2: Y5V: 22% to -82% <high capacitance="" series=""> ΔC/C Class 2: Y5V: 22% to -82%</high></general>
Adhesion		4.7	A force applied for 10 seconds to the line joining the terminations and in a plane parallel to the substrate	Force size ≥ 0603: 5N size = 0402: 2.5N size = 0201: IN

NOTE:

1. For individual product specification, please contact local sales.





TEST	TEST MET	<u> do</u> b	PROCEDURE	REQUIREMENTS
Bending strength	IEC 60384- 21/22	4.8	Mounting in accordance with IEC 60384-22 paragraph 4.3	No visible damage
			Conditions: bending I mm at a rate of I mm/s,	<general purpose="" series=""></general>
			radius jig 5 mm	ΔC/C
				Class2:
				Y5V: ±10%
				<high capacitance="" series=""></high>
				ΔC/C
				Class2:
				Y5V: ±10%
Resistance to soldering heat		4.9	Precondition: $150 + 0/-10$ °C for 1 hour, then keep for 24 ± 1 hours at room temperature	Dissolution of the end face plating shall not exceed 25% of the length of the edge
			Preheating: for size ≤ 1206: 120 °C to 150 °C for 1 minute	concerned
			Preheating: for size >1206: 100 °C to 120 °C for I	<general purpose="" series=""></general>
			minute and 170 °C to 200 °C for I minute	ΔC/C
			Solder bath temperature: 260 ±5 °C	Class2:
			Dipping time: 10 ±0.5 seconds	Y5V: ±20%
			Recovery time: 24 ±2 hours	<high capacitance="" series=""></high>
				ΔC/C
				Class2:
				Y5V: ±20%
			-	D.F. within initial specified value
				R _{ins} within initial specified value
Solderability		4.10	Preheated the temperature of 80 °C to 140 °C and maintained for 30 seconds to 60 seconds.	The solder should cover over 95% of the critical area of each termination
			I. Temperature: 235 \pm 5°C / Dipping time: 2 \pm 0.5 s	
			2. Temperature: $245\pm5^{\circ}\text{C}$ / Dipping time: $3\pm0.5\text{ s}$ (lead free)	
			Depth of immersion: 10mm	



TEST	TEST METHOD		PROCEDURE	REQUIREMENTS
Rapid change of temperature	IEC 60384- 21/22	4.11	Preconditioning; 150 +0/ $=$ 10 °C for 1 hour, then keep for 24 \pm 1 hours at room temperature	No visual damage
				<general purpose="" series=""></general>
			5 cycles with following detail:	ΔC/C
			30 minutes at lower category temperature 30 minutes at upper category temperature	Class2: Y5V: ±20%
				13 V. ±20/0
				<high capacitance="" series=""></high>
			Recovery time 24 ±2 hours	ΔC/C
				Class2:
				Y5V: ±20%
			-	D.F. meet initial specified value
				R _{ins} meet initial specified value
Damp heat with U _r load		4.13	Preconditioning, class 2 only: 150 +0/-10 °C /I hour, then keep for 24 ±I hour at room temp Initial measure:	No visual damage after recovery
With Or load				<general purpose="" series=""></general>
				ΔC/C
			Spec: refer initial spec C, D, IR	Class2:
			3. Damp heat test:	Y5V: ±30%
			500 \pm 12 hours at 40 \pm 2 °C;	D.F.
			90 to 95% R.H. 1.0 U _r applied 4. Recovery: Class 2: 24 ±2 hours 5. Final measure: C, D, IR	Class2:
				Y5V: ≤ 15%
				R _{ins}
				Class2:
				Y5V: \geq 500 M Ω or R _{ins} × C _r \geq 25s
		P.S. If the capacitance value is less than the minimum value permitted, then after the other	whichever is less	
			measurements have been made the capacitor shall	<high capacitance="" series=""></high>
		be precondition according to "IEC 60384 4.1" and	ΔC/C	
			then the requirement shall be met.	Class2:
			Y5V: ±30%	
				D.F.
				Class2:
				Y5V: 2 × initial value max
				R _{ins}
				Class2:
				Y5V: 500 M Ω or $R_{ins} \times C_r \ge 5s$
				whichever is less



TEST	TEST METHOD	PROCEDURE	REQUIREMENTS
Endurance	TEST METHOD IEC 60384- 4.14 21/22	 Preconditioning, class 2 only: 150 +0/-10 °C /1 hour, then keep for 24 ±1 hour at room temp Initial measure: Spec: refer initial spec C, D, IR Endurance test: Temperature: Y5V: 85 °C Specified stress voltage applied for 1,000 hours: 	REQUIREMENTS No visual damage <general purpose="" series=""> ΔC/C Class2: Y5V: ±30% D.F. Class2:</general>
		Applied $2.0 \times U_r$ for general product. Applied $1.5 \times U_r$ for high cap. product. 4. Recovery time: 24 ± 2 hours 5. Final measure: C, D, IR P.S. If the capacitance value is less than the minimum value permitted, then after the other measurements have been made the capacitor shall be precondition according to "IEC 60384 4.1" and then the requirement shall be met.	Y5V: \leq 15% R _{ins} Class2: Y5V: \geq 1,000 M Ω or R _{ins} × C _r \geq 50s whichever is less <hr/> High Capacitance series> Δ C/C Class 2: Y5V: \pm 30% D.F. Class 2: Y5V: 2 × initial value max R _{ins} Class 2: Y5V: 1,000 M Ω or R _{ins} × C _r \geq 10s whichever is less
Voltage proof	IEC 60384-I 4.6	Specified stress voltage applied for 1~5 seconds Ur ≤100 V: series applied 2.5 Ur Charge/Discharge current is less than 50 mA	No breakdown or flashover



REVISION HISTORY

REVISION	DATE	CHANGE NOTIFICATION	DESCRIPTION
Version 14	Dec. 21, 2023	-	- Update 0805, 10 μF, 10V D.F. value
Version 13	Aug. 10, 2023	-	- 1206, 1uF, 16V and 50V
Version 12	Dec. 14, 2022	-	- Add 0603, 220nF, 50V
Version 11	Oct. 05, 2021	-	- Update 0805, 1210 I.R. spec, modify 0402 L4 spec
Version 10	Apr. 29, 2021	-	- Update 1206 ≥ 10 μF, 16V Df value
Version 9	Nov. 11, 2019	-	- Add 0603, 4.7uF, 10V
Version 8	Mar. 7, 2017	-	- 0805 L4 spec updated
Version 7	Dec. 9, 2016	-	- Soldering recommendation update
Version 6	Jan. 12, 2016	-	- Update capacitance range & thickness
Version 5	Jul. 29, 2010	-	- Modify the last 2-digit of I2NC
Version 4	Jun. 24, 2010	-	- Dimension on 1206 case size updated
Version 3	Apr. 22, 2010	-	- Dimension updated
Version 2	Feb. 04, 2010	-	- The statement of "Halogen Free" on the cover added
Version I	Nov. 04, 2009	-	- Ordering code updated - Dimension updated
Version 0	Apr. 15, 2009	-	 New datasheet for general purpose and high capacitance Y5V series with RoHS compliant Replace the "6.3V to 50V" part of pdf files: Y5V_6.3V_10V_9_Preliminary, Y5V_10V-to-50V_10_Preliminary, Y5V_16V_25V_50V_11 Combine 0201 from pdf files: UP-NP0X5RX7RY5V_0201_6.3-to-50V_2 and UY-NPOX5RX7RY5V_0201_6.3-to-50V_2 Define global part number Description of "Halogen Free compliant" added Test method and procedure updated

Y5V

SERIES

6.3 V to 50 V

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