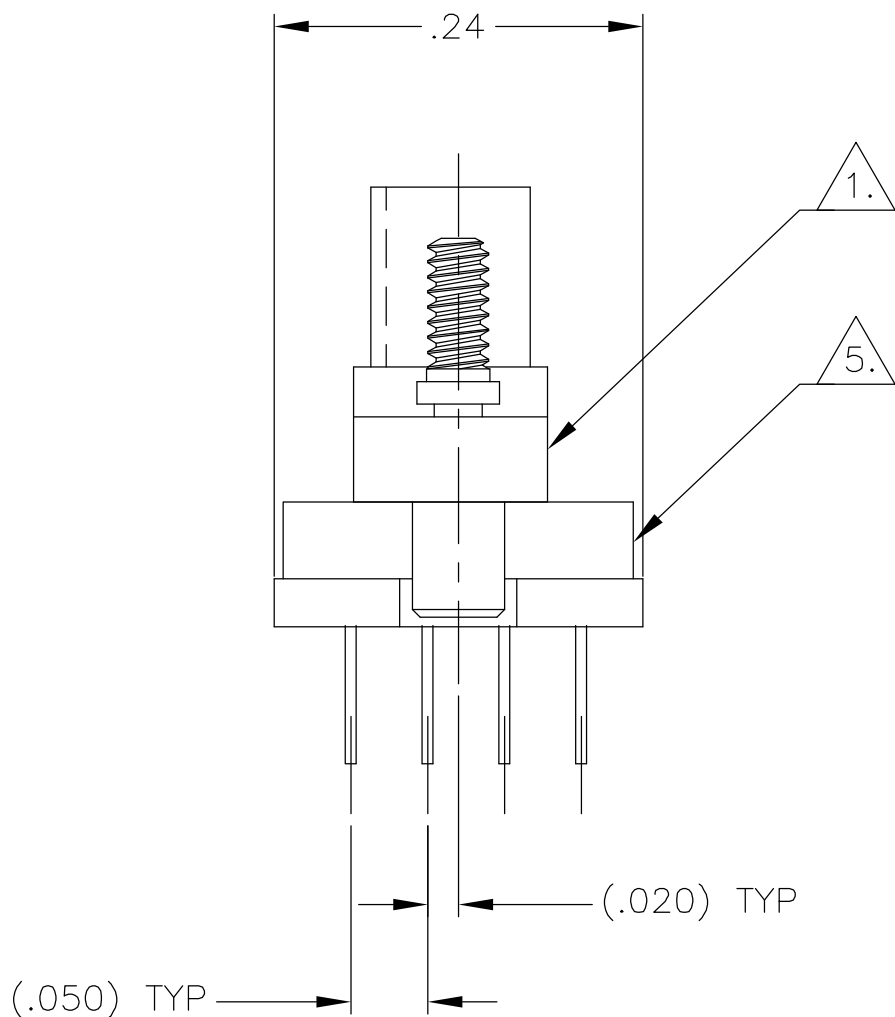
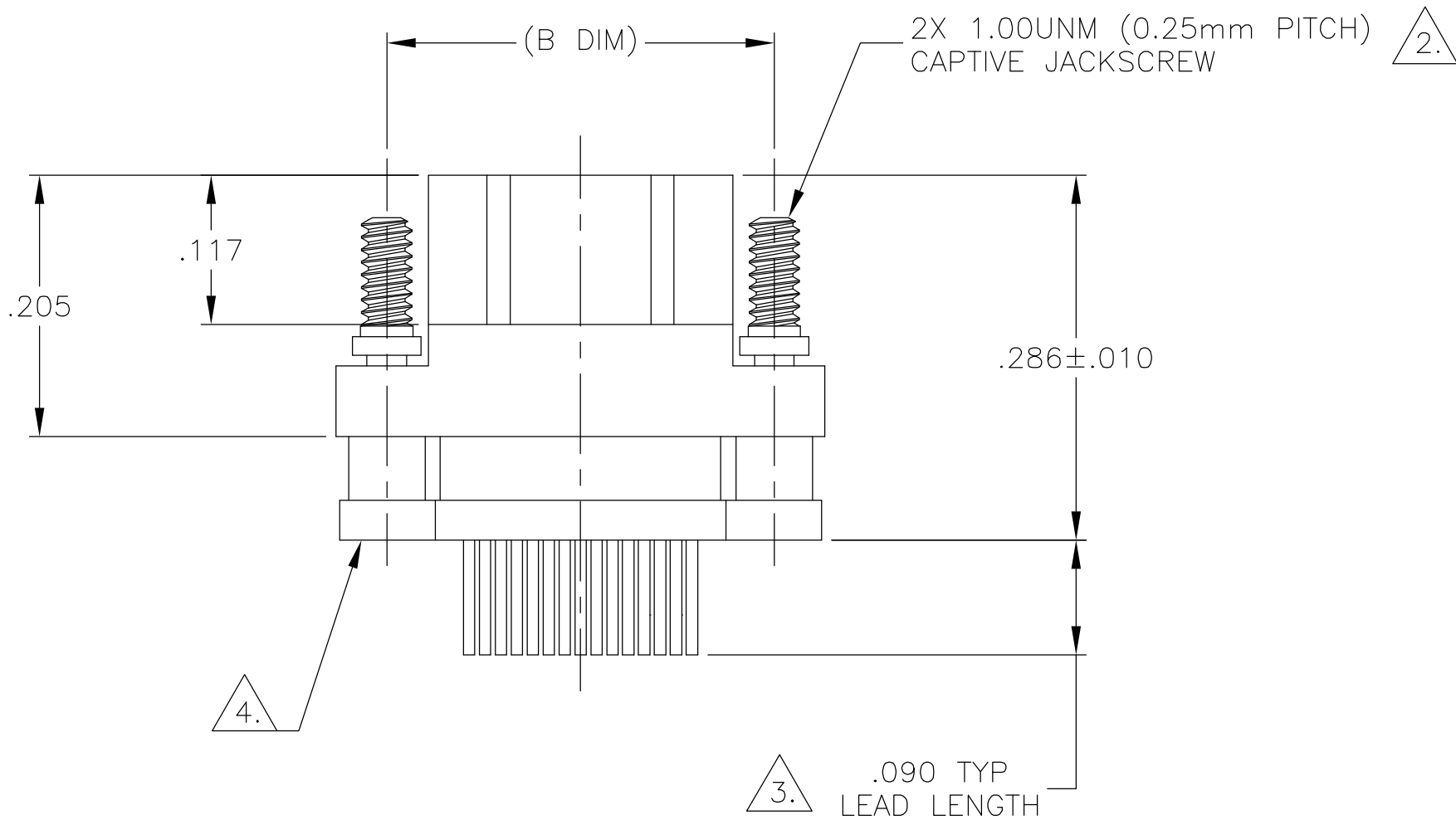
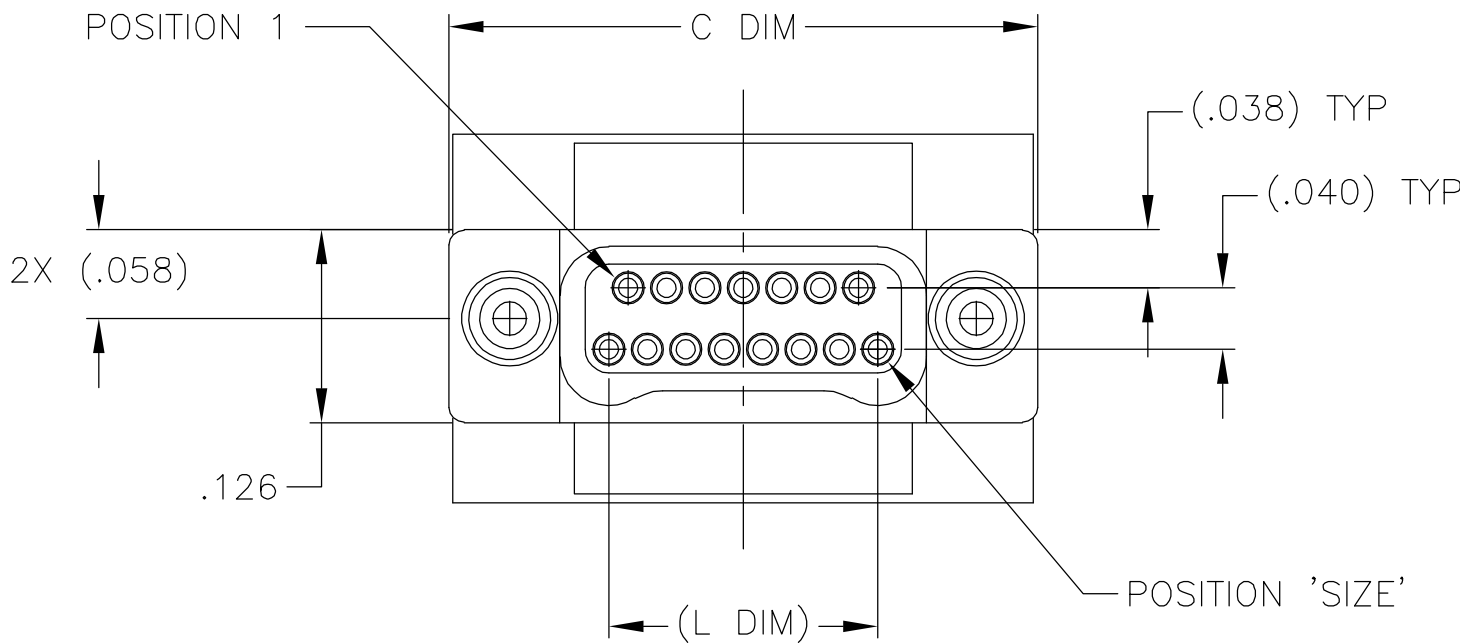


LOC	DIST	REVISIONS				
		P	LTR	DESCRIPTION	DATE	DWN
DF	D0		L	UPDATE PER 0H2Y-0164-04	12 MAY 04	CAS
						MKS

SIZE	(B DIM)	C DIM ±.0050	(L DIM)
09	.229	.3085	(.100)
15	.304	.3835	(.175)
25	.429	.5085	(.300)
37	.579	.6585	(.450)
51	.754	.8335	(.625)
65	.929	1.0085	(.800)



1. SHELL OPTIONS (TO BE SPECIFIED IN NANONICS PART NUMBER):
METAL: 6061-T6 ALUMINUM, ELECTROLESS NICKEL PLATED PER MIL-C-26074 (STANDARD) OR GOLD PLATED PER MIL-G-45204
303 STAINLESS STEEL, PASSIVATED PER SAE-AMS-QQ-P-35
INSULATOR MATERIAL FOR ALL METAL SHELLS IS LIQUID CRYSTAL POLYMER (LCP) PER MIL-M-24519 OR PER ASTM D5138
PLASTIC: LIQUID CRYSTAL POLYMER (LCP) PER MIL-M-24519 OR PER ASTM D5138
2. STANDARD 1.00UNM CAPTIVE JACKSCREWS ARE SHOWN FOR REFERENCE ONLY AND MUST BE SPECIFIED IN THE NANONICS PART NUMBER WHEN REQUIRED. JACKSCREW MATERIAL IS 303 STAINLESS STEEL, PASSIVATED PER SAE-AMS-QQ-P-35, AND DRY LUBED PER DOD-L-85645. RETAINING RING IS 17-4 STAINLESS STEEL, PASSIVATED PER SAE-AMS-QQ-P-35. JACKSCREWS HAVE A .9mm HEX SOCKET HEAD. 1.20UNM JACKSCREWS ALSO AVAILABLE.
3. LEAD MATERIAL: HH BRASS, TIN LEAD PLATED 60/40 COMPOSITION PER SAE-AMS-P-81728.
4. INSULATOR BOARD MATERIAL: G10 OR EQUIVALENT.
5. EPOXY ENCAPSULATION.
6. NANONICS TERMINATION CODE: C5
7. THIS DRAWING PREVIOUSLY IDENTIFIED AS NANONICS N10138/227

THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN M. STORRY 11 OCT 00	tyco Electronics Tyco Electronics Corporation Harrisburg, PA 17105				
		CHK S. KAIN 12 OCT 00					
DIMENSIONS: INCHES	TOLERANCES UNLESS OTHERWISE SPECIFIED:	APVD —	NAME PLUG ASSEMBLY, VERTICAL MOUNT, THROUGH HOLE, 2 TO 4 ROW, .050 SPACING, PLASTIC OR METAL				
	0 PLC ± —	PRODUCT SPEC					
	1 PLC ± —	—					
	2 PLC ± .010	APPLICATION SPEC					
	3 PLC ± .005	—					
	4 PLC ± —	—	SIZE	CAGE CODE	DRAWING NO	RESTRICTED TO	
MATERIAL	FINISH	WEIGHT —	A2	00779	C=1589482	—	
SEE NOTES	SEE NOTES	CUSTOMER DRAWING			SCALE 8:1	SHEET 1 OF 2	REV L

THIS DRAWING IS UNPUBLISHED.

RELEASED FOR PUBLICATION

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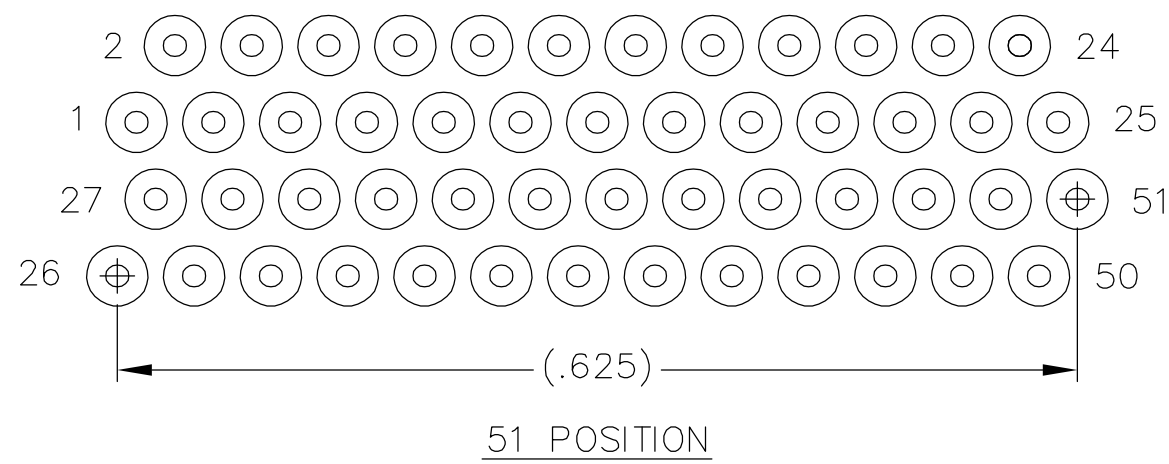
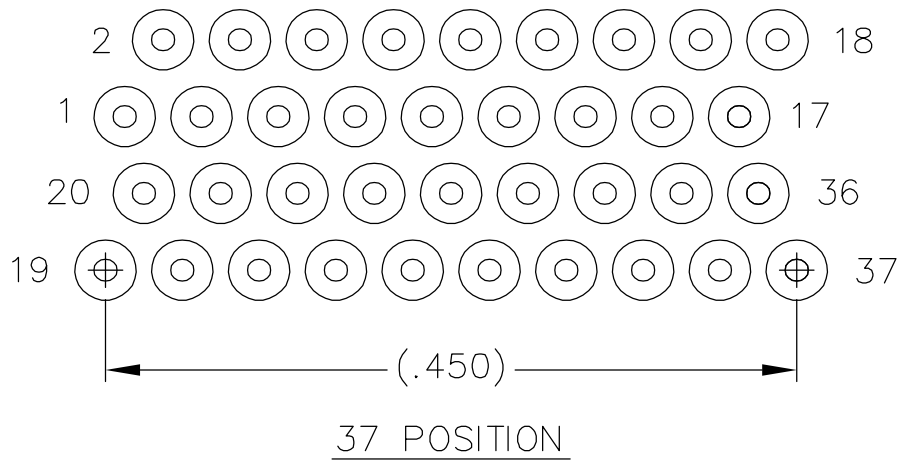
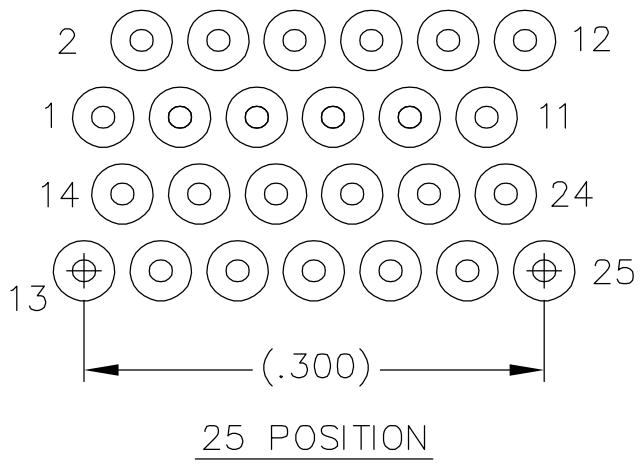
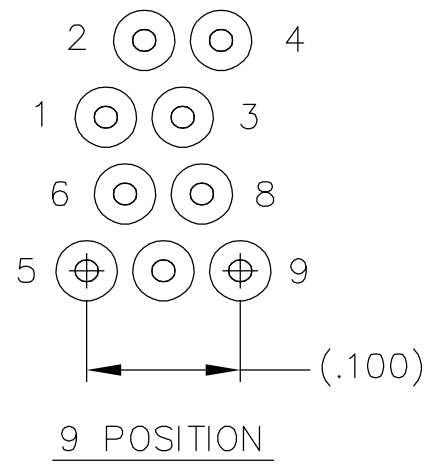
LOC		DIST		REVISIONS					
DF		D0		P	LTR	DESCRIPTION	DATE	DWN	APVD
			L	UPDATE PER 0H2Y-0164-04			12 MAY 04	CAS	MKS

D

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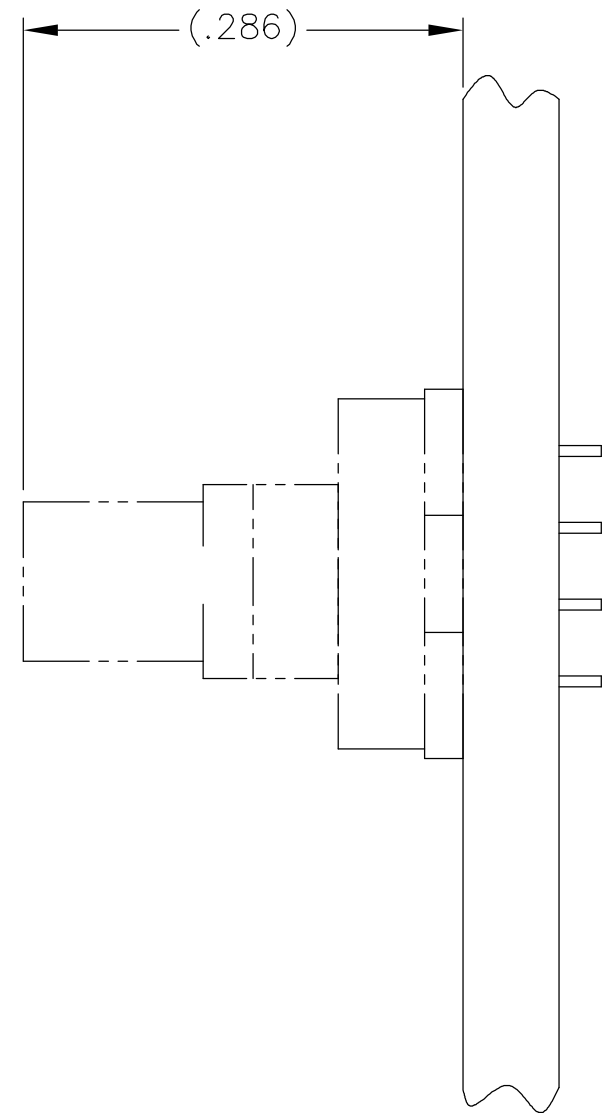
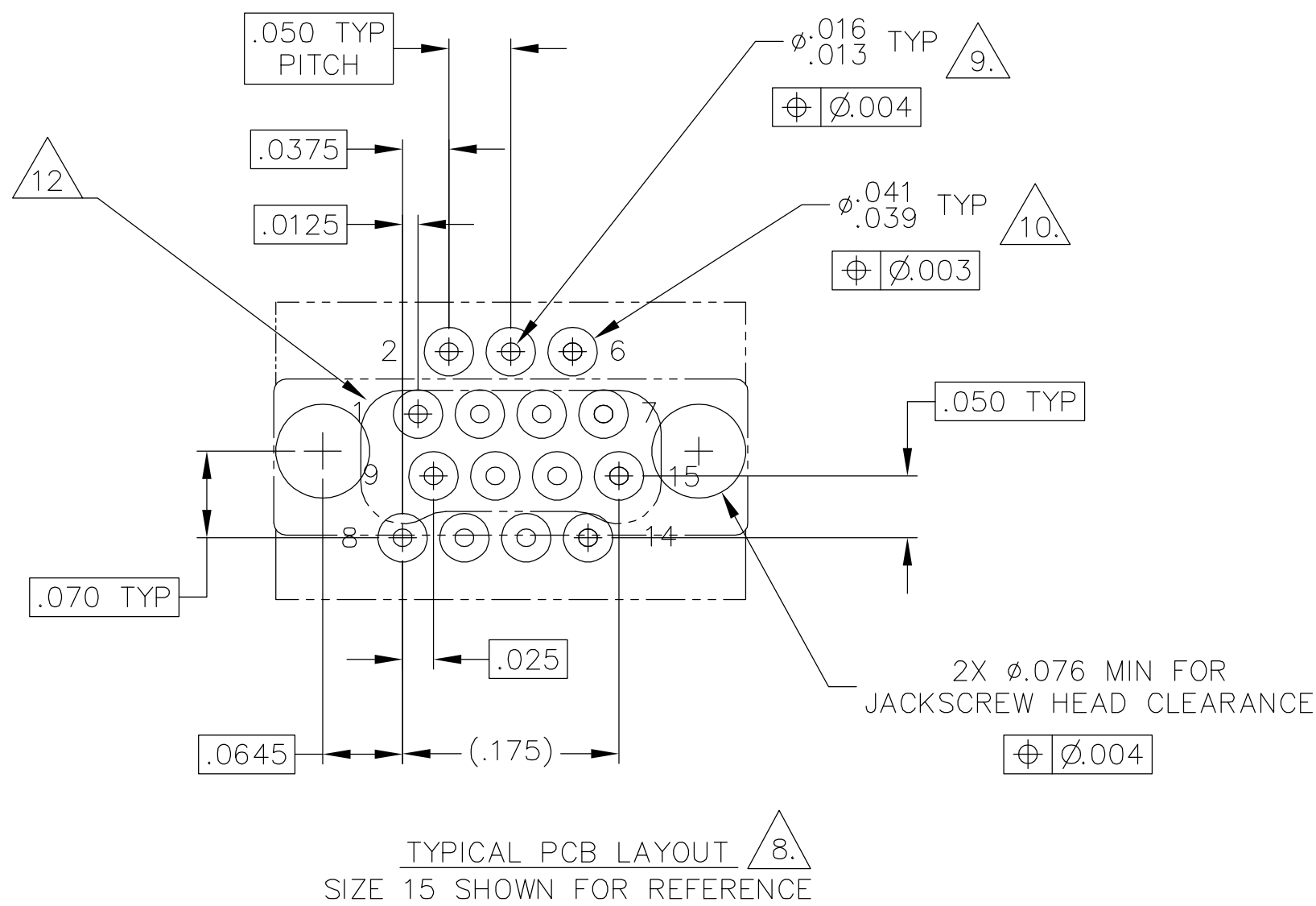
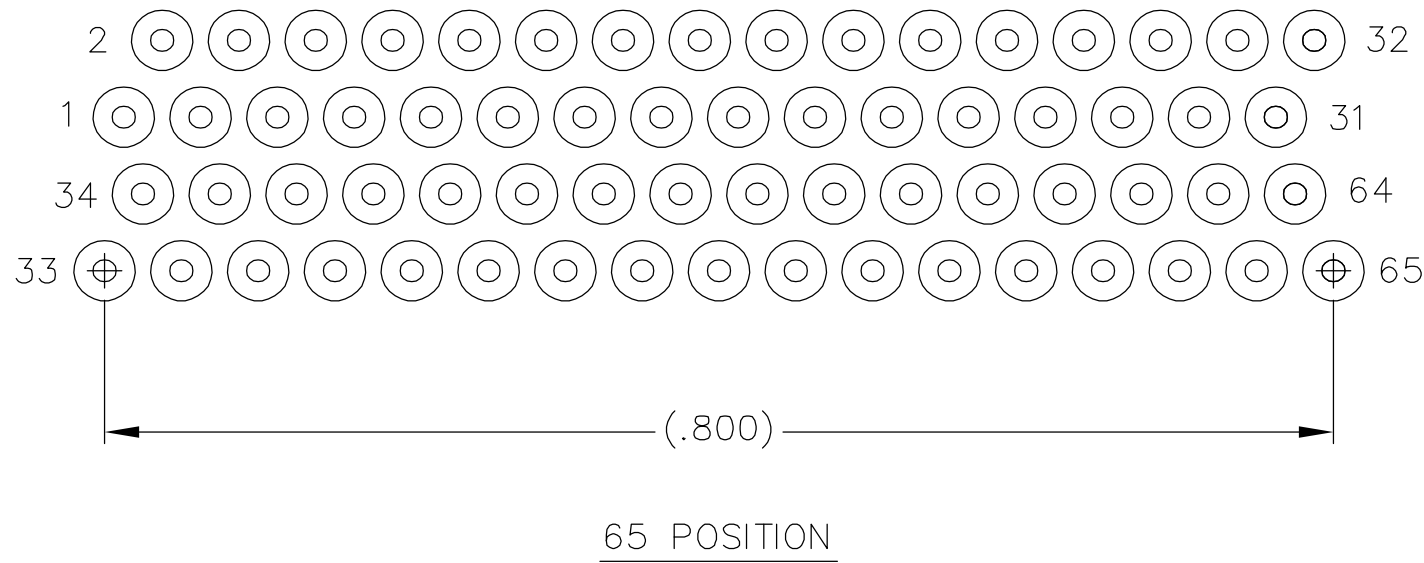


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8. POSITIONAL TOLERANCES FOR BASIC DIMENSIONED FEATURES ARE RELATIVE TO FIDUCIALS OR SOME SIMILAR DATUM REFERENCES DEFINED BY PCB DESIGNER.
9. PLATED THROUGH HOLES
10. SOLDER PADS
11. ALL THROUGH HOLE LAYOUTS ARE AS VIEWED FROM TOP OF PCB.
12. CONNECTOR ORIENTATION

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DIMENSIONS: INCHES		CHK S. KAIN 12 OCT 00				
	TOLERANCES UNLESS OTHERWISE SPECIFIED:		NAME			
	0 PLC ± -		PRODUCT SPEC			
	1 PLC ± -		APPLICATION SPEC			
MATERIAL	FINISH		WEIGHT			
	-		-			
CUSTOMER DRAWING		SCALE 8:1		SHEET 2 OF 2		REV L