

MSKSEMI 美森科

SEMICONDUCTOR



ESD



TVS



TSS



MOV



GDT



PLED

MC79LxxACDR2G(MS)/MC79LxxACHX

Product specification

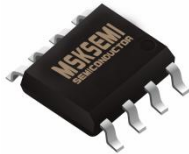
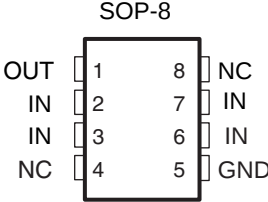
FEATURES

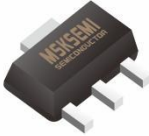
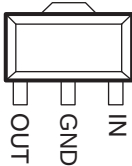
- Some kinds output voltage series
- Superior ripple rejection ratio for audio frequency
- Over current and over temperature protection

APPLICATIONS

- Three-terminal Negative Fixed Voltage Regulators

Reference News

Type No	SOP-8	PIN DESCRIPTION	MARKING
MC79LxxACDR2G(MS)		SOP-8 	

Type No	SOT-89	PIN DESCRIPTION	MARKING
MC79LxxACHX			

ORDER INFORMATION

P/N	PKG	QTY
MC79L05ACDR2G(MS)	SOP8	2500PCS
MC79L06ACDR2G(MS)	SOP8	2500PCS
MC79L08ACDR2G(MS)	SOP8	2500PCS
MC79L09ACDR2G(MS)	SOP8	2500PCS
MC79L12ACDR2G(MS)	SOP8	2500PCS
MC79L15ACDR2G(MS)	SOP8	2500PCS

P/N	PKG	QTY
MC79L05ACHX	SOT-89	1000
MC79L06ACHX	SOT-89	1000
MC79L08ACHX	SOT-89	1000
MC79L09ACHX	SOT-89	1000
MC79L12ACHX	SOT-89	1000
MC79L15ACHX	SOT-89	1000

MAXIMUM RATING operating temperature range applies unless otherwise specified

Symbol	Parameter	Value		Units
V_I	Input voltage	-35		V
P_D	Power dissipation	SOT89-3	500	mW
		SOP-8	750	
T_{opr}	Operating ambient temperature	-20 to +85		°C
T_j, T_{stg}	Junction and Storage Temperature	-65 to +150		°C

ELECTRICAL CHARACTERISTICS
 $(V_{IN}=-10V, I_O=40mA, 0^{\circ}C < T_J < 125^{\circ}C, C_{IN}=0.33\mu F, C_L=0.1\mu F, \text{unless otherwise specified})$

Parameter	Symbol	Test conditions	MC79L05AC			UNIT
			MIN	TYP	MAX	
Output voltage	V_O	$T_J=25^{\circ}C$ $V_{IN}=-10V, 1.0mA \leq I_O \leq 70mA$	-4.8 -4.75	-5.0	-5.2 -5.25	V
Load regulation	ΔV_{OLOAD}	$T_J=25^{\circ}C, 1.0mA \leq I_{OUT} \leq 100mA$		20	60	mV
		$T_J=25^{\circ}C, 1.0mA \leq I_{OUT} \leq 40mA$		10	30	
Line regulation	ΔV_{OLINE}	$T_J=25^{\circ}C, -20V \leq V_I \leq -7V$		15	150	mV
		$T_J=25^{\circ}C, -20V \leq V_I \leq -8V$		12	100	
Bias current		$T_J=25^{\circ}C$		3.8	6.0	mA
Bial current change		$T_J=25^{\circ}C, -20V \leq V_I \leq -8V$			1.5	mA
		$1.0mA \leq I_O \leq 40mA$			0.1	
Output noise voltage		$T_J=25^{\circ}C$ $f=10Hz-100Hz$		40		μV
Output short circuit current	I_{OS}	$T_J=25^{\circ}C$		300		mA

ELECTRICAL CHARACTERISTICS

($V_{IN} = -11\text{ V}$, $I_{OUT} = 40\text{ mA}$, $0^{\circ}\text{C} \leq T_j \leq 125^{\circ}\text{C}$, $C_{IN} = 0.33\mu\text{F}$, $C_L = 0.1\mu\text{F}$, unless otherwise specified)

Parameter	Symbol	Test conditions	MC79L06AC			UNIT
			MIN	TYP	MAX	
Output voltage	V_O	$T_j = 25^{\circ}\text{C}$ $V_{IN} = -10\text{V}$, $1.0\text{ mA} \leq I_O \leq 70\text{mA}$	-5.75 -5.7	-6.0	-6.25 -6.3	V
Load regulation	ΔV_{OLOAD}	$T_j = 25^{\circ}\text{C}$, $1.0\text{ mA} \leq I_{OUT} \leq 100\text{ mA}$ $T_j = 25^{\circ}\text{C}$, $1.0\text{ mA} \leq I_{OUT} \leq 40\text{ mA}$		21 11	80 40	mV
Line regulation	ΔV_{OLINE}	$T_j = 25^{\circ}\text{C}$, $-20\text{V} \leq V_i \leq -8\text{V}$ $T_j = 25^{\circ}\text{C}$, $-20\text{V} \leq V_i \leq -9\text{V}$		20 12	170 100	mV
Bias current		$T_j = 25^{\circ}\text{C}$		3.8	6.0	mA
Bial current change		$T_j = 25^{\circ}\text{C}$, $-20\text{V} \leq V_i \leq -9\text{V}$ $1.0\text{mA} \leq I_O \leq 40\text{mA}$			1.5 0.1	mA
Output noise voltage		$T_j = 25^{\circ}\text{C}$ $f = 10\text{Hz} - 100\text{Hz}$		44		μV
Output short circuit current	I_{OS}	$T_j = 25^{\circ}\text{C}$		300		mA

ELECTRICAL CHARACTERISTICS

($V_{IN} = -14\text{ V}$, $I_{OUT} = 40\text{ mA}$, $0^{\circ}\text{C} \leq T_J \leq 125^{\circ}\text{C}$, $C_{IN} = 0.33\mu\text{F}$, $C_L = 0.1\mu\text{F}$, unless otherwise specified)

Parameter	Symbol	Test conditions	MC79L08AC			UNIT
			MIN	TYP	MAX	
Output voltage	V_O	$T_J = 25^{\circ}\text{C}$ $V_{IN} = -10\text{V}$, $1.0\text{ mA} \leq I_O \leq 70\text{mA}$	-7.7 -7.6	-8.0	-8.3 -6.4	V
Load regulation	ΔV_{OLOAD}	$T_J = 25^{\circ}\text{C}$, $1.0\text{ mA} \leq I_{OUT} \leq 100\text{ mA}$		30	100	mV
		$T_J = 25^{\circ}\text{C}$, $1.0\text{ mA} \leq I_{OUT} \leq 40\text{ mA}$		15	50	
Line regulation	ΔV_{OLINE}	$T_J = 25^{\circ}\text{C}$, $-23\text{V} \leq V_i \leq -10.5\text{V}$		42	200	mV
		$T_J = 25^{\circ}\text{C}$, $-23\text{V} \leq V_i \leq -11\text{V}$		36	150	
Bias current		$T_J = 25^{\circ}\text{C}$			6.0	mA
Bial current change		$T_J = 25^{\circ}\text{C}$, $-20\text{V} \leq V_i \leq -11\text{V}$			1.5	mA
		$1.0\text{mA} \leq I_O \leq 40\text{mA}$			0.1	
Output noise voltage		$T_J = 25^{\circ}\text{C}$ $f = 10\text{Hz} - 100\text{Hz}$		54		μV
Output short circuit current	I_{OS}	$T_J = 25^{\circ}\text{C}$		300		mA

ELECTRICAL CHARACTERISTICS

($V_{IN} = -15\text{ V}$, $I_{OUT} = 40\text{ mA}$, $0^\circ\text{C} \leq T_j \leq 125^\circ\text{C}$, $C_{IN} = 0.33\mu\text{F}$, $C_L = 0.1\mu\text{F}$, unless otherwise specified)

Parameter	Symbol	Test conditions	MC79L09AC			UNIT
			MIN	TYP	MAX	
Output voltage	V_O	$T_j = 25^\circ\text{C}$ $V_{IN} = -15\text{ V}$, $1.0\text{ mA} \leq I_O \leq 70\text{ mA}$	-8.64 -8.55	-9.0	-9.36 -9.45	V
Load regulation	ΔV_{OLOAD}	$T_j = 25^\circ\text{C}$, $1.0\text{ mA} \leq I_{OUT} \leq 100\text{ mA}$ $T_j = 25^\circ\text{C}$, $1.0\text{ mA} \leq I_{OUT} \leq 40\text{ mA}$		30 15	100 50	mV
Line regulation	ΔV_{OLINE}	$T_j = 25^\circ\text{C}$, $-24\text{ V} \leq V_i \leq -12\text{ V}$ $T_j = 25^\circ\text{C}$, $-24\text{ V} \leq V_i \leq -13\text{ V}$		45 40	170 125	mV
Bias current		$T_j = 25^\circ\text{C}$			6.2	mA
Bial current change		$T_j = 25^\circ\text{C}$, $-20\text{ V} \leq V_i \leq -13\text{ V}$ $1.0\text{ mA} \leq I_O \leq 40\text{ mA}$			1.5 0.1	mA
Voltage drop	V_{DROP}	$T_j = 25^\circ\text{C}$		1.3		V
Output short circuit current	I_{OS}	$T_j = 25^\circ\text{C}$		270		mA

ELECTRICAL CHARACTERISTICS

($V_{IN} = -19\text{ V}$, $I_{OUT} = 40\text{ mA}$, $0^\circ\text{C} \leq T_j \leq 125^\circ\text{C}$, $C_{IN} = 0.33\mu\text{F}$, $C_L = 0.1\mu\text{F}$, unless otherwise specified)

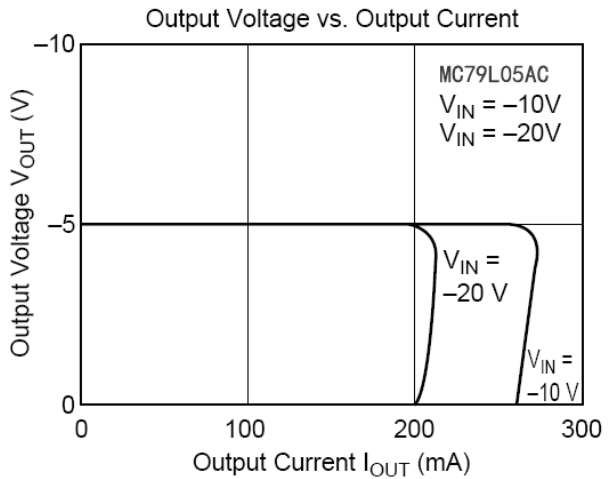
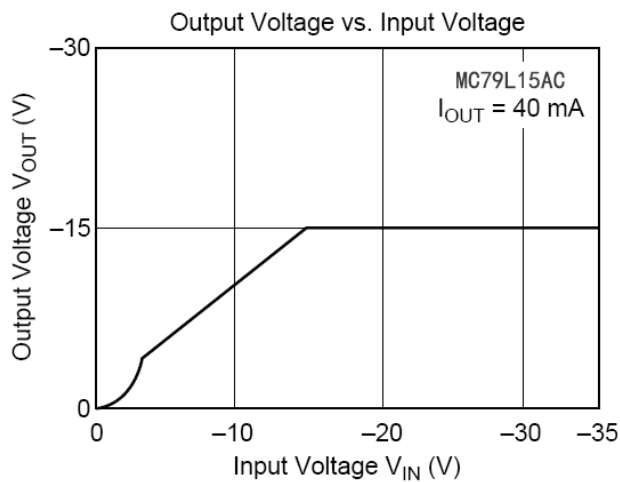
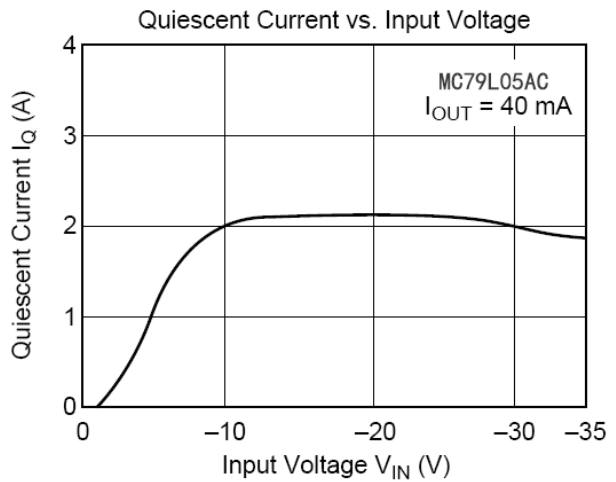
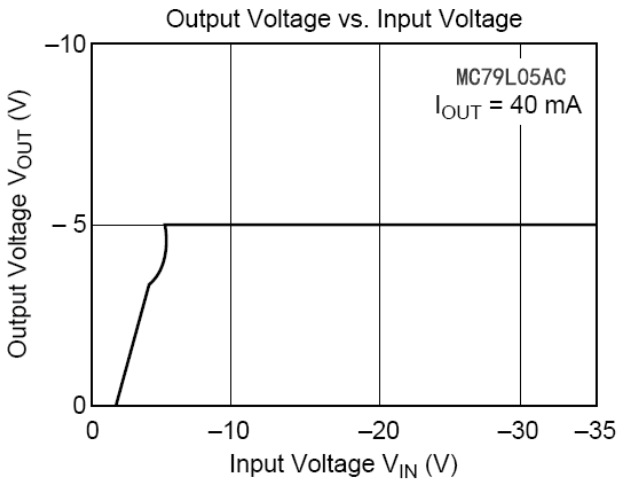
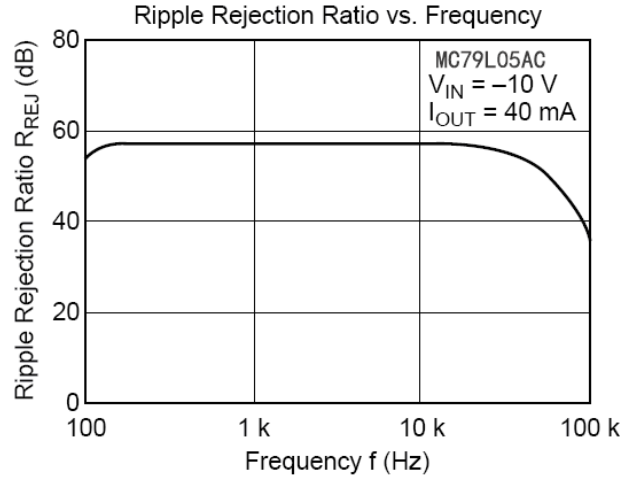
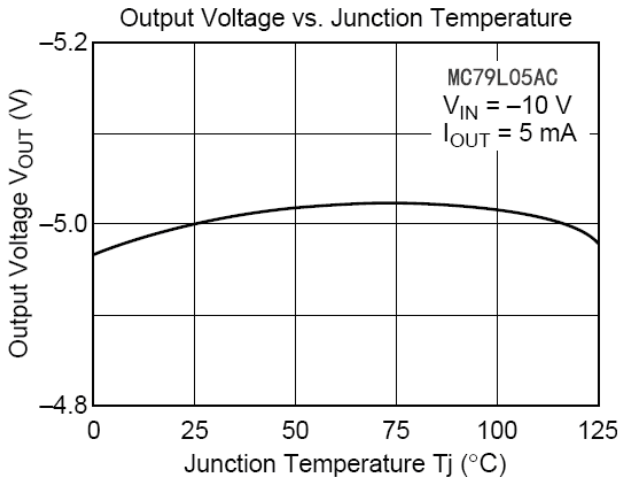
Parameter	Symbol	Test conditions	MC79L12AC			UNIT
			MIN	TYP	MAX	
Output voltage	V_O	$T_j = 25^\circ\text{C}$ $V_{IN} = -19\text{ V}$, $1.0\text{ mA} \leq I_O \leq 70\text{ mA}$	-11.52 -11.40	-12	-12.48 -12.60	V
Load regulation	ΔV_{OLOAD}	$T_j = 25^\circ\text{C}$, $1.0\text{ mA} \leq I_{OUT} \leq 100\text{ mA}$ $T_j = 25^\circ\text{C}$, $1.0\text{ mA} \leq I_{OUT} \leq 40\text{ mA}$		24 15	100 50	mV
Line regulation	ΔV_{OLINE}	$T_j = 25^\circ\text{C}$, $-27\text{ V} \leq V_i \leq -14\text{ V}$ $T_j = 25^\circ\text{C}$, $-27\text{ V} \leq V_i \leq -16\text{ V}$		50 40	250 200	mV
Bias current		$T_j = 25^\circ\text{C}$			-6.5	mA
Bial current change		$T_j = 25^\circ\text{C}$, $-27\text{ V} \leq V_i \leq -16\text{ V}$ $1.0\text{ mA} \leq I_O \leq 40\text{ mA}$			1.5 0.1	mA
Voltage drop	V_{DROP}	$T_j = 25^\circ\text{C}$		1.3		V
Output short circuit current	I_{OS}	$T_j = 25^\circ\text{C}$		250		mA

ELECTRICAL CHARACTERISTICS

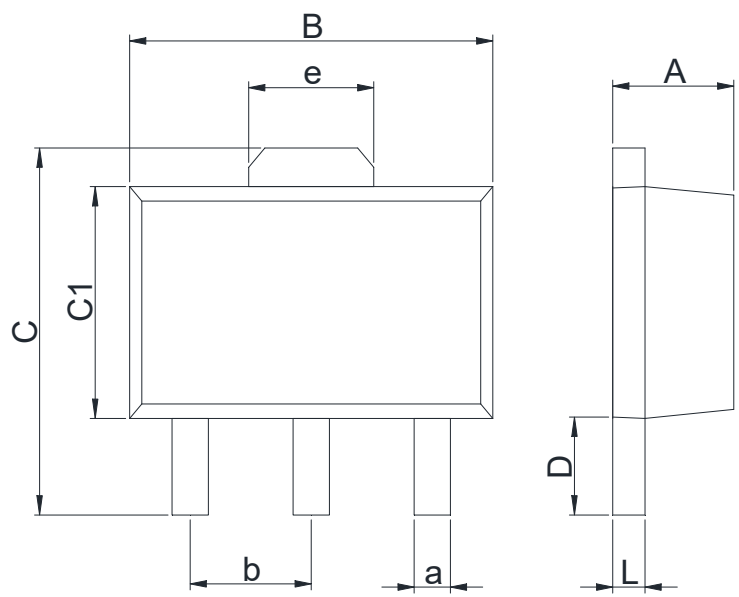
($V_{IN} = -23\text{ V}$, $I_{OUT} = 40\text{ mA}$, $0^{\circ}\text{C} \leq T_j \leq 125^{\circ}\text{C}$, $C_{IN} = 0.33\mu\text{F}$, $C_L = 0.1\mu\text{F}$, unless otherwise specified)

Parameter	Symbol	Test conditions	MC79L15AC			UNIT
			MIN	TYP	MAX	
Output voltage	V_O	$T_j = 25^{\circ}\text{C}$ $V_{IN} = -23\text{ V}$, $1.0\text{ mA} \leq I_O \leq 70\text{ mA}$	-14.4 -14.25	-15	-15.6 -15.75	V
Load regulation	ΔV_{OLOAD}	$T_j = 25^{\circ}\text{C}$, $1.0\text{ mA} \leq I_{OUT} \leq 100\text{ mA}$ $T_j = 25^{\circ}\text{C}$, $1.0\text{ mA} \leq I_{OUT} \leq 40\text{ mA}$		25 15	150 75	mV
Line regulation	ΔV_{OLINE}	$T_j = 25^{\circ}\text{C}$, $-30\text{ V} \leq V_i \leq -17.5\text{ V}$ $T_j = 25^{\circ}\text{C}$, $-30\text{ V} \leq V_i \leq -19\text{ V}$		65 50	300 250	mV
Bias current		$T_j = 25^{\circ}\text{C}$			-6.5	mA
Bial current change		$T_j = 25^{\circ}\text{C}$, $-30\text{ V} \leq V_i \leq -19\text{ V}$ $1.0\text{ mA} \leq I_O \leq 40\text{ mA}$			1.5 0.1	mA
Voltage drop	V_{DROP}	$T_j = 25^{\circ}\text{C}$		1.3		V
Output short circuit current	I_{OS}	$T_j = 25^{\circ}\text{C}$		240		mA

TYPICAL CHARACTERISTICS @ $T_a=25^\circ\text{C}$ unless otherwise specified



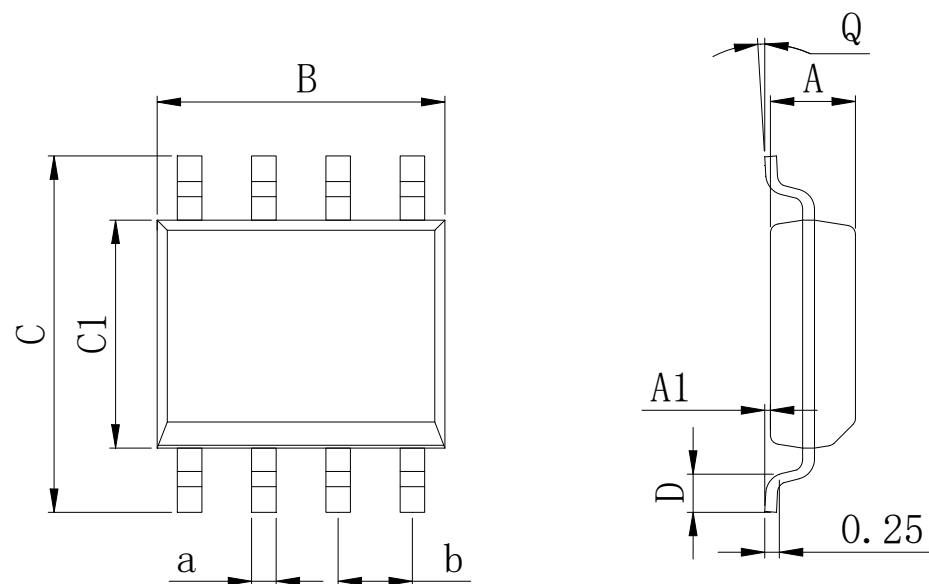
SOT89-3



Dimensions In Millimeters(SOT-89-3)									
Symbol:	A	B	C	C1	D	L	a	b	e
Min:	1.40	4.40	3.94	2.30	0.90	0.35	0.40	1.50	1.55
Max:	1.60	4.60	4.25	2.60	1.20	0.44	0.50	BSC	BSC

Physical Dimensions

SOP-8



Dimensions In Millimeters(SOP-8)									
Symbol:	A	A1	B	C	C1	D	Q	a	b
Min:	1.35	0.05	4.90	5.80	3.80	0.40	0°	0.35	1.27 BSC
Max:	1.55	0.20	5.10	6.20	4.00	0.80	8°	0.45	

Attention

■ Any and all MSKSEMI Semiconductor products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your MSKSEMI Semiconductor representative nearest you before using any MSKSEMI Semiconductor products described or contained herein in such applications.

■ MSKSEMI Semiconductor assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specification of any and all MSKSEMI Semiconductor products described or contained herein.

■ Specifications of any and all MSKSEMI Semiconductor products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.

■ MSKSEMI Semiconductor strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with some probability. It is possible that these probabilistic failures could give rise to accidents or events that could endanger human lives, that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.

■ In the event that any or all MSKSEMI Semiconductor products (including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from the authorities concerned in accordance with the above law.

■ No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of MSKSEMI Semiconductor.

■ Information (including circuit diagrams and circuit parameters) herein is for example only ; it is not guaranteed for volume production. MSKSEMI Semiconductor believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringement of intellectual property rights or other rights of third parties.

■ Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. When designing equipment, refer to the "Delivery Specification" for the MSKSEMI Semiconductor product that you intend to use.