



SCH4304 ☆

Super I/O Controller

Status: In Production

 View CAD Symbols

Features:

- X-Bus interface
- 128-pin QFP package
- LPC interface with serial IRQ pin
- Floppy disk controller
- Multi-mode parallel port
- 8024 Keyboard controller

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Device Overview

Summary

The SCH5017 is a 3.3V Super I/O controller with an LPC interface. It also includes auto fan support, a real time clock, LED support, two full UARTs, and a multi-mode parallel port.

The product provides support for monitoring two external temperatures via thermal diode inputs and an internal sensor for measuring ambient temperature and allows for a two or three-piece linear fan function. This incorporates SMBus Backdoor Access to all registers, and supports up to three T03 sensor devices, and up to nine sensors, each of which can be assigned to any one or more of the three auto fan controls. The SMBus Slave Interface has a default I2C address 5ch for HWM registers and 6ch to RTC registers.

Family parts

SCH4304-NS

Additional Features

- X-Bus interface
- 128-pin QFP package
- LPC interface with serial IRQ pin
- Floppy disk controller
- Multi-mode parallel port
- 8024 Keyboard controller
- Watchdog timer
- Security register
- Low battery warning
- ACPI support register for monitoring system sleep state
- XNOR chain
- PC2001 compliant
- ACPI 2.0 compliant

Parametrics

Name	Value
Fan Tachometer Inputs	8
8024 Keyboard Controller	Y
PECI Support	N
GPIO	51
PWM	3
Package	128 QFP
SMBus Interface	Y
Security Key Register	Y
AMD SB-TSI	N



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 Overview

 Documents

 Development Environment

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 RoHS Information

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RoHS Information

Part Number	Device Weight (g)	Shipping Weight (Kg)	Lead Count	Package Type	Package Dimension	Solder Composition	JEDEC Indicator	RoHS	China EFUP
SCH4304-NS	1.806400	6.803030	128	QFP	14x20x2.7	Matte Tin	e3		

To see a complete listing of RoHS data for this device, please [Click here](#)

Shipping Weight = Device Weight + Packing Material weight. Please [contact sales](#) office if device weight is not available.

