

PJQ1902

30V N-Channel Enhancement Mode MOSFET – ESD Protected

Voltage

30 V

Current

500mA

Features

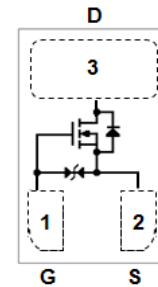
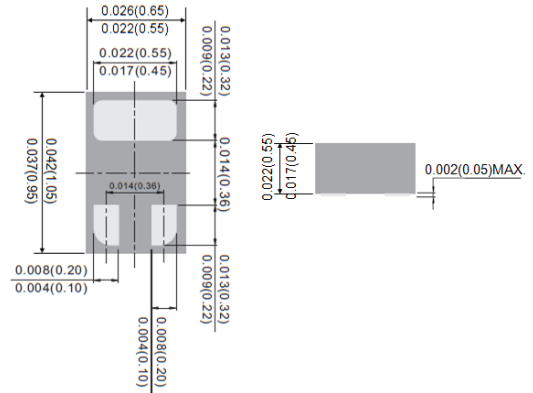
- $R_{DS(ON)}$, $V_{GS}@4.5V$, $I_D@350mA < 1.2\Omega$
- $R_{DS(ON)}$, $V_{GS}@2.5V$, $I_D@200mA < 1.6\Omega$
- $R_{DS(ON)}$, $V_{GS}@1.8V$, $I_D@80mA < 2.3\Omega$
- $R_{DS(ON)}$, $V_{GS}@1.5V$, $I_D@10mA < 2.5\Omega$ (typ.)
- Specially Designed for Switch Load, PWM Application, etc.
- ESD Protected 2KV HBM
- Lead free in compliance with EU RoHS 2011/65/EU directive
- Green molding compound as per IEC61249 Std. (Halogen Free)

Mechanical Data

- Case: DFN3L Package
- Terminals: Solderable per MIL-STD-750, Method 2026
- Approx. Weight: 0.00004 ounces, 0.0011 grams
- Marking: 2

DFN3L

Unit: inch(mm)



Maximum Ratings and Thermal Characteristics ($T_A=25^{\circ}C$ unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V _{DS}	30	V
Gate-Source Voltage		V _{GS}	+10	V
Continuous Drain Current		I _D	500	mA
Pulsed Drain Current		I _{DM}	1500	mA
Power Dissipation	T _a =25°C	P _D	700	mW
	Derate above 25°C		5.6	mW/°C
Operating Junction and Storage Temperature Range		T _J , T _{STG}	-55~150	°C
Typical Thermal resistance		R _{θJA}	175	°C/W
- Junction to Ambient ^(Note 3)				

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Electrical Characteristics ($T_A=25^{\circ}\text{C}$ unless otherwise noted)

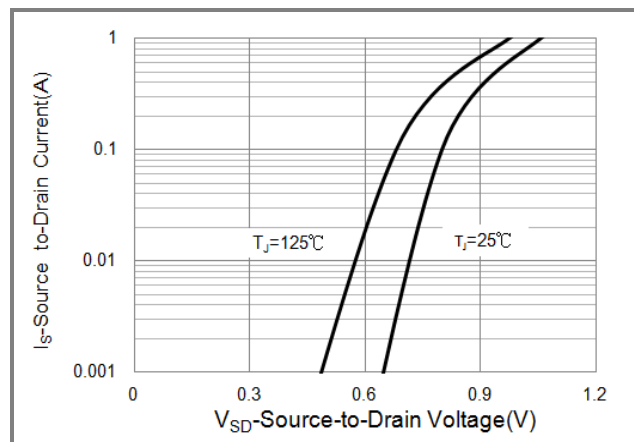
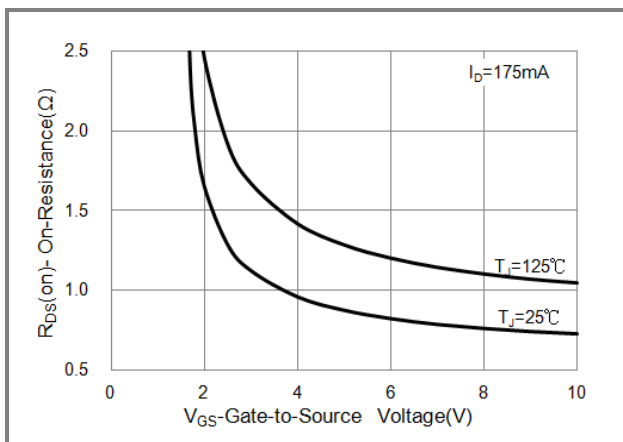
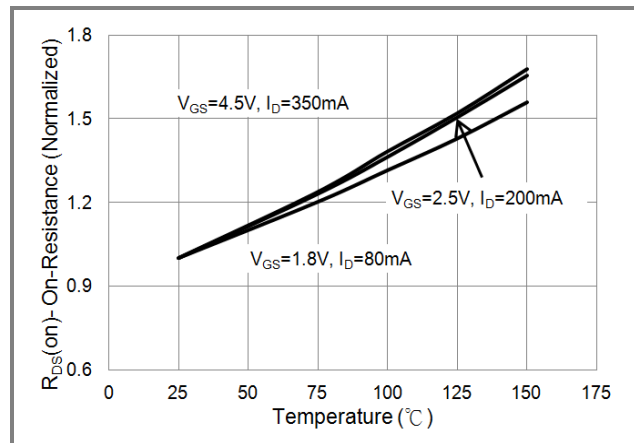
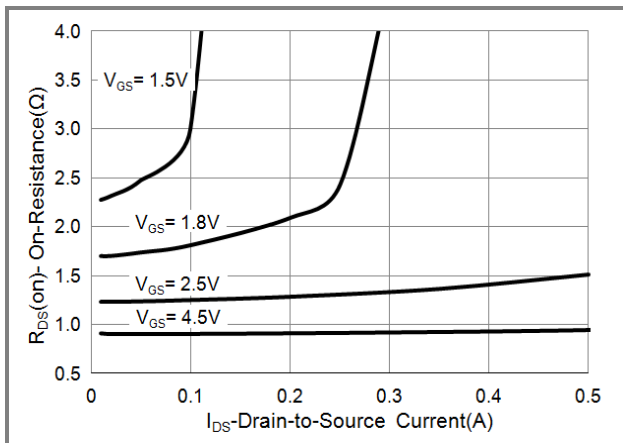
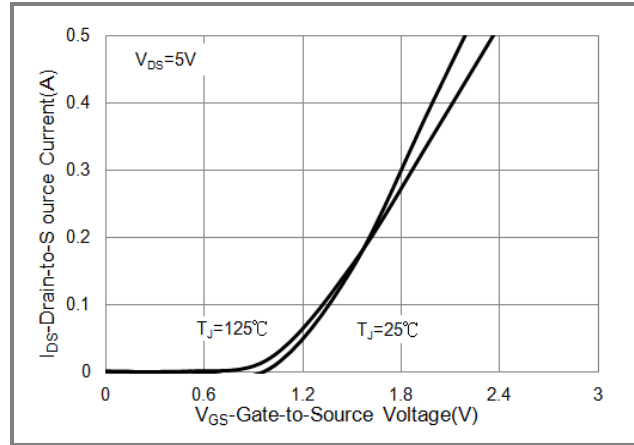
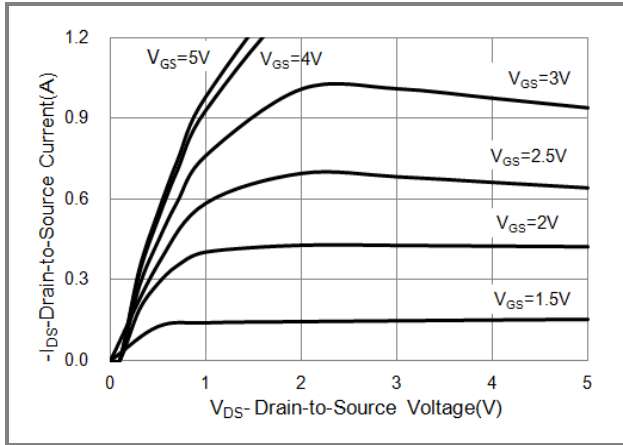
PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =250uA	30	-	-	V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250uA	0.6	0.85	1.1	V
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =4.5V, I _D =350mA	-	0.94	1.2	Ω
		V _{GS} =2.5V, I _D =200mA	-	1.32	1.6	
		V _{GS} =1.8V, I _D =80mA	-	1.82	2.3	
		V _{GS} =1.5V, I _D =10mA	-	2.5	-	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =30V, V _{GS} =0V	-	0.01	1	uA
Gate-Source Leakage Current	I _{GSS}	V _{GS} =±8V, V _{DS} =0V	-	-	±10	
		V _{GS} =±5V, V _{DS} =0V	-	-	±1	
Dynamic (Note 5)						
Total Gate Charge	Q _g	V _{DS} =15V, I _D =350mA, V _{GS} =4.5V (Note 1,2)	-	0.87	-	nC
Gate-Source Charge	Q _{gs}		-	0.26	-	
Gate-Drain Charge	Q _{gd}		-	0.16	-	
Input Capacitance	C _{iss}	V _{DS} =15V, V _{GS} =0V, f=1.0MHZ	-	34	-	pF
Output Capacitance	C _{oss}		-	8.9	-	
Reverse Transfer Capacitance	C _{rss}		-	2.5	-	
Turn-On Delay Time	td _(on)	V _{DD} =15V, I _D =80mA, V _{GS} =4.0V, R _G =6Ω (Note 1,2)	-	7.1	-	ns
Turn-On Rise Time	tr		-	20	-	
Turn-Off Delay Time	td _(off)		-	41	-	
Turn-Off Fall Time	tf		-	31	-	
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	I _S	---	-	-	350	mA
Diode Forward Voltage	V _{SD}	I _S =350mA, V _{GS} =0V	-	0.88	1.3	V

NOTES :

1. Pulse width $\leq 300\mu s$, Duty cycle $\leq 2\%$
2. Essentially independent of operating temperature typical characteristics.
3. $R_{\theta JA}$ is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper
4. The maximum current rating is package limited
5. Guaranteed by design, not subject to production testing.

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TYPICAL CHARACTERISTIC CURVES



PJQ1902

TYPICAL CHARACTERISTIC CURVES

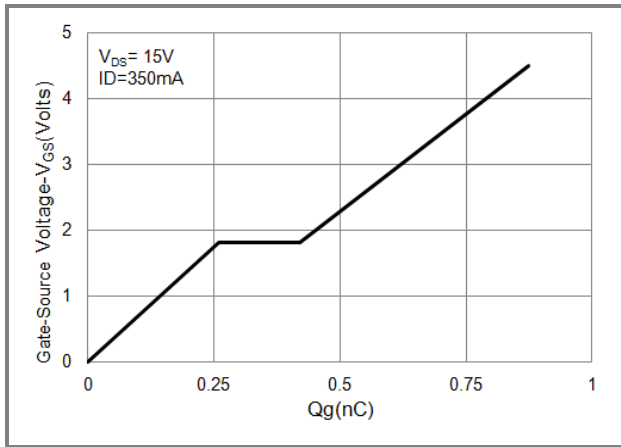


Fig.7 Gate-Charge Characteristics

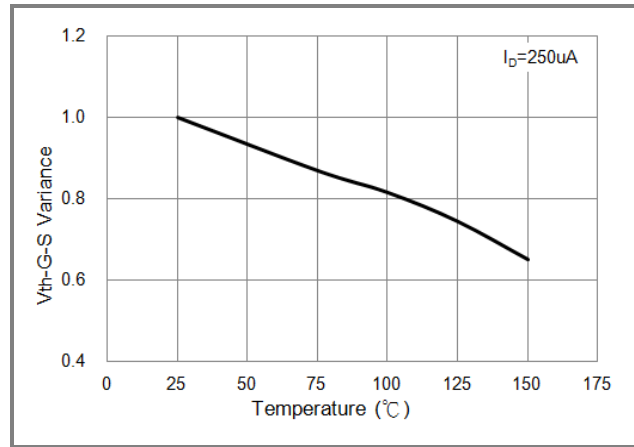


Fig.8 Threshold Voltage Variation with Temperature.

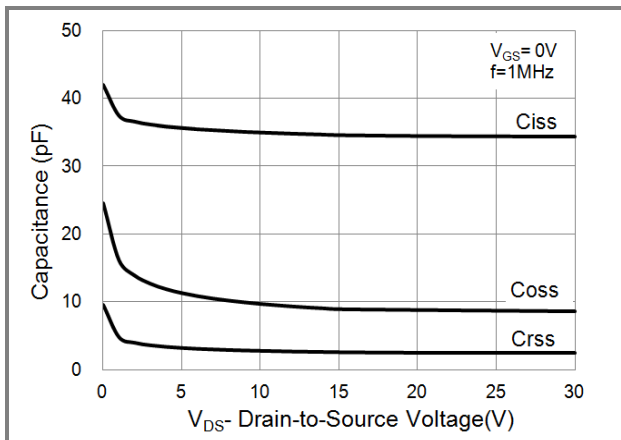


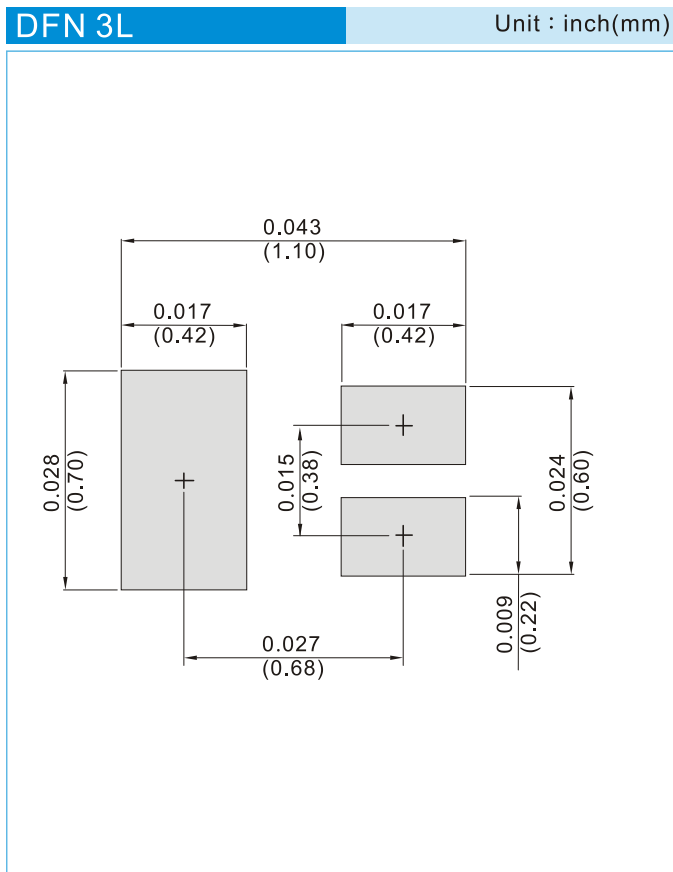
Fig.9 Capacitance vs. Drain-Source Voltage.

PJQ1902

PART NO PACKING CODE VERSION

Part No Packing Code	Package Type	Packing type	Marking	Version
PJQ1902_R1_00001	DFN3L	8K pcs / 7" reel	2	Halogen free

MOUNTING PAD LAYOUT





PJQ1902

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