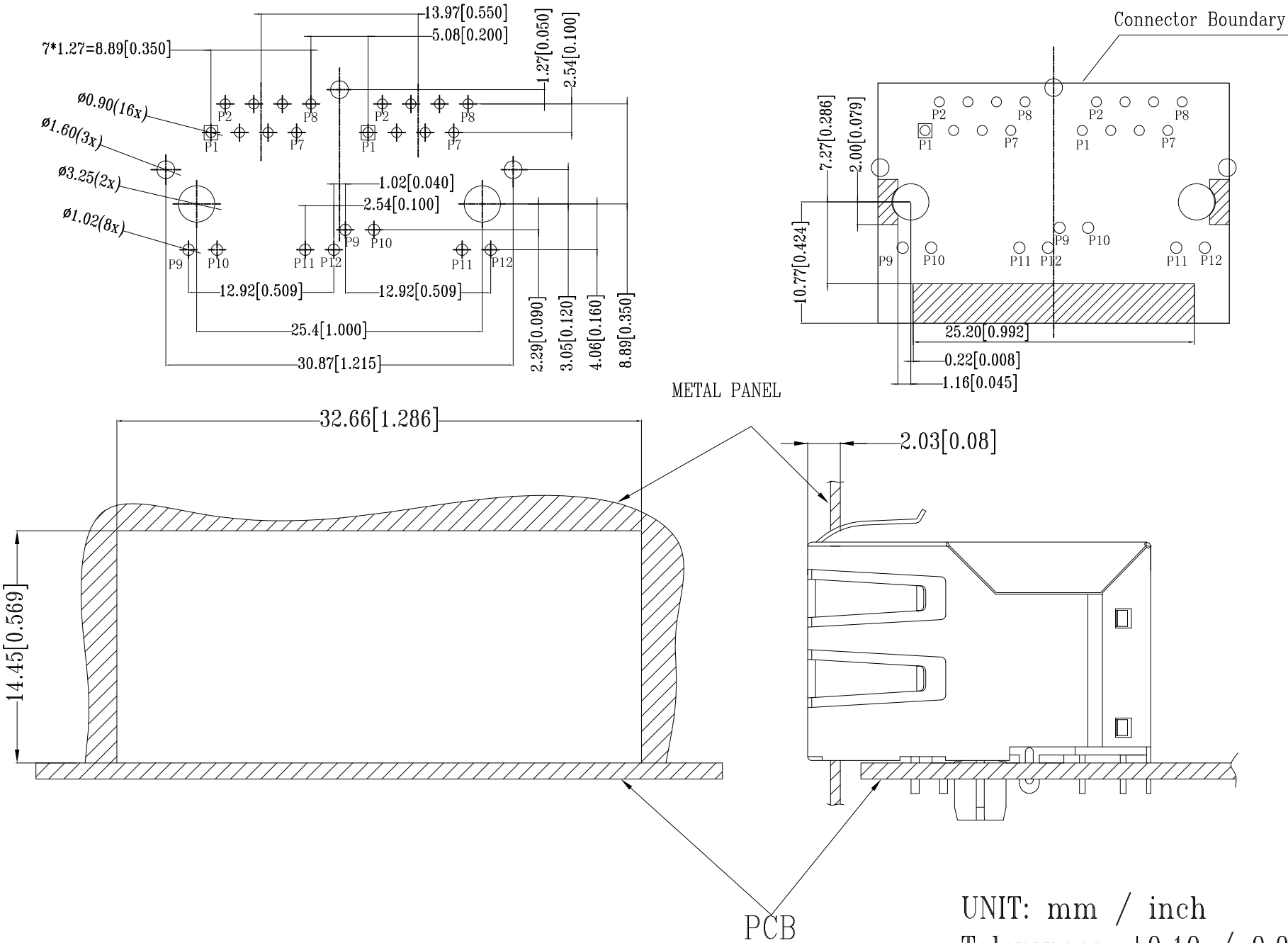


REVISIONS					
P	LTR	DESCRIPTION	DATE	DWN	APVD
	—	SEE SHEET 1	—	—	—

SUGGESTED PCB LAYOUT



UNIT: mm / inch
Tolerances: ±0.10 / 0.004

THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN	GANESH C M	27AUG2016	 TE Connectivity		
		CHK	FRANZ MUELLER	27AUG2016			
		APVD	MARTIN SZELAG	27AUG2016	NAME		
		PRODUCT SPEC			RJ45 JACK INT.MAG. 10/100 LED 1x2		
		APPLICATION SPEC					
		114-94447			SIZE	CAGE CODE	DRAWING NO
		WEIGHT			A3	00779	C-2301996-6
		CUSTOMER DRAWING			SCALE	SHEET	REV
					3:1	2 OF 3	A1

4

3

2

1

THIS DRAWING IS UNPUBLISHED

RELEASED FOR PUBLICATION

— ,2017.

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REVISIONS

P	LTR	DESCRIPTION	DATE	DWN	APVD
	—	SEE SHEET 1	—	—	—

ELECTRICAL CIRCUIT DIAGRAM

CAP 1000pF/2kV

P8

75 Ω

75 Ω

75 Ω

75 Ω

1CT:1CT

1CT:1CT

J8

J7

J5

J4

J6

J3

J2

J1

P9

P10

P11

P12

Y

G

Y

G

Pin	Yellow	Green	Pin	Yellow	Green
P9	—	+	P11	—	+
P10	+	—	P12	+	—

1.0 Turn Ratio @100kHz: (P1~P3):(J1~J2) = 1:1±2%
(P4~P6):(J3~J6) = 1:1±2%

2.0 Primary Inductance: 350μH MIN. @100kHz, 0.1V 8mA DC BIAS

3.0 DC Resistance: 1.2 OHMS MAX.

4.0 Insertion Loss: 1–100MHz –1.2dB MAX.

5.0 Return Loss: 1–30MHz –16dB MIN.
30–60MHz –12dB MIN.
60–80MHz –10dB MIN.

6.0 CROSS TALK: 1–100MHz –30dB MIN.

7.0 COMMON TO COMMON MODE ATTENUATION: 1–100MHz –30dB MIN.

8.0 Isolation: PHY Side to Line Side: 2250VDC

2301996-6

WWYY-xxxx

TE

FCC

CONTROL CODE

DATE CODE

"0213"

YY-YEAR

WW-WEEK

THIS DRAWING IS A CONTROLLED DOCUMENT.

DIMENSIONS:
mm [INCHES]

1

7

MATERIAL

1

TOLERANCES UNLESS
OTHERWISE SPECIFIED:

0 PLC ± —
1 PLC ± —
2 PLC ± —
3 PLC ± —
4 PLC ± —
ANGLES ± —
FINISH 1

DWN 27AUG2016
GANESH C M

CHK 27AUG2016
FRANZ MUELLER

APVD 27AUG2016
MARTIN SZELAG

PRODUCT SPEC
108-94552

APPLICATION SPEC
114-94447

WEIGHT

CUSTOMER DRAWING

TE

TE Connectivity

NAME
RJ45 JACK INT.MAG. 10/100 LED 1x2

SIZE
A3

CAGE CODE
00779

DRAWING NO
C-2301996-6

RESTRICTED TO
—

SCALE
3:1

SHEET
3 OF 3

REV
A1

1470-19 (1/15)