

VOLTAGE RANGE: 20 --- 100 V

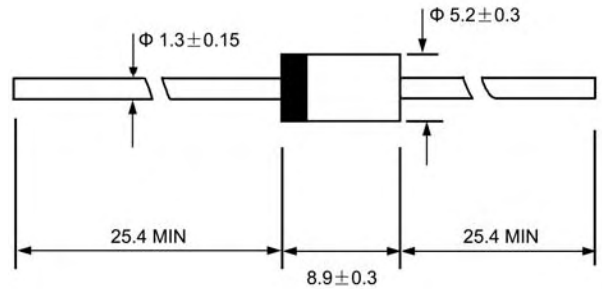
CURRENT: 3.0 A

Features

- ◇ Metal-Semiconductor junction with guard ring
- ◇ Epitaxial construction
- ◇ Low forward voltage drop, low switching losses
- ◇ High surge capability
- ◇ For use in low voltage, high frequency inverters free wheeling, and polarity protection applications
- ◇ The plastic material carries U/L recognition 94V-0

Mechanical Data

- ◇ Case: JEDEC DO-27, molded plastic
- ◇ Polarity: Color band denotes cathode
- ◇ Weight: 0.041 ounces, 1.15 grams
- ◇ Mounting position: Any



Dimensions in millimeters

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Ratings at 25°C ambient temperature unless otherwise specified.

Single phase, half wave, 60 Hz, resistive or inductive load. For capacitive load, derate by 20%.

		SB320	SB330	SB340	SB350	SB360	SB380	SB3100	UNITS
Maximum recurrent peak reverse voltage	V _{RRM}	20	30	40	50	60	80	100	V
Maximum RMS voltage	V _{RMS}	14	21	28	35	42	56	70	V
Maximum DC blocking voltage	V _{DC}	20	30	40	50	60	80	100	V
Maximum average forw ard rectified current 9.5mm lead length	I _{F(AV)}	3.0							A
Peak forw ard surge current 8.3ms single half-sine-w ave superimposed on rated load @T _J =125℃	I _{FSM}	80.0							A
Maximum instantaneous forw ard voltage @ 3.0A (Note 1)	V _F	0.55			0.7		0.85		V
Maximum reverse current @T _A =25℃ at rated DC blocking voltage @T _A =100℃	I _R	1.5							mA
		20.0			10.0				
Typical junction capacitance (Note2)	C _J	250			160				pF
Typical thermal resistance (Note3)	R _{θJA}	40							℃/W
Operating junction temperature range	T _J	- 55 ---- + 125			- 55 ---- + 150				℃
Storage temperature range	T _{STG}	- 55 ---- + 150							℃

NOTE: 1. Pulse test: 300us pulse width, 1% duty cycle.

2. Measured at 1.0MHz and applied reverse voltage of 4.0V DC.

3. Thermal resistance junction to ambient

Ratings AND Characteristic Curves

FIG.1 -- FORWARD CURRENT DERATING CURVE

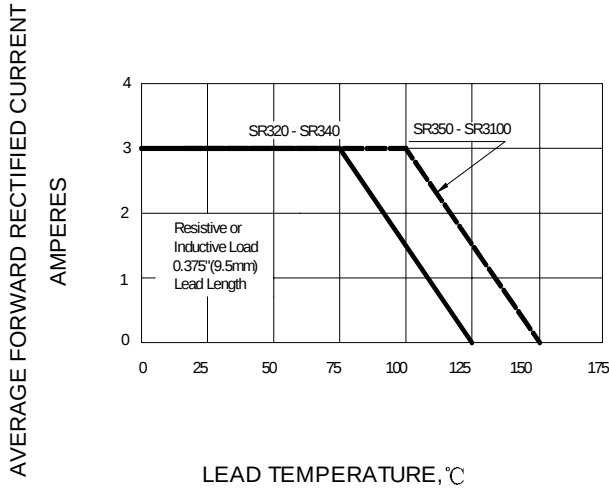


FIG.2 -- PEAK FORWARD SURGE CURRENT

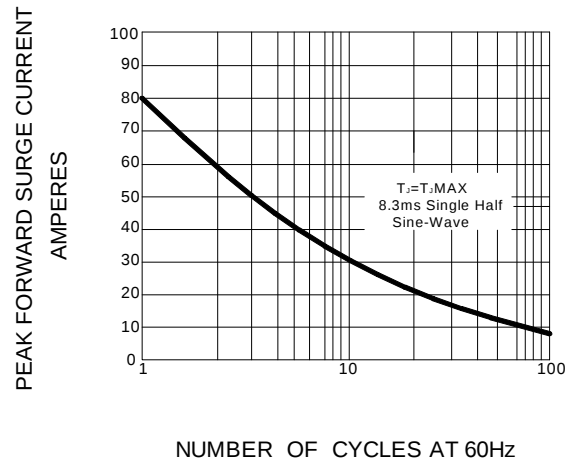


FIG.3 -- TYPICAL INSTANTANEOUS FORWARD CHARACTERISTICS

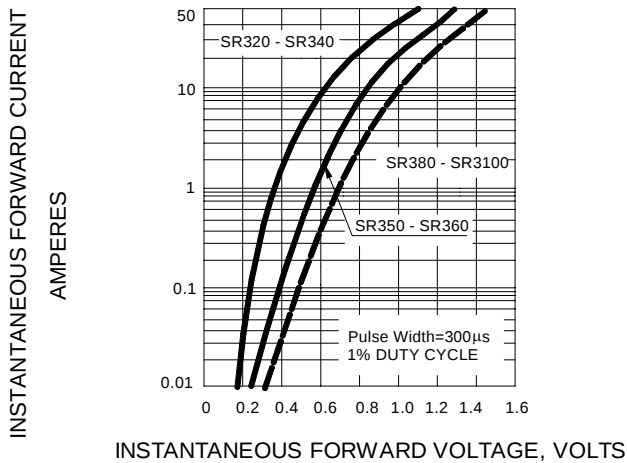


FIG.4 -- TYPICAL JUNCTION CAPACITANCE

