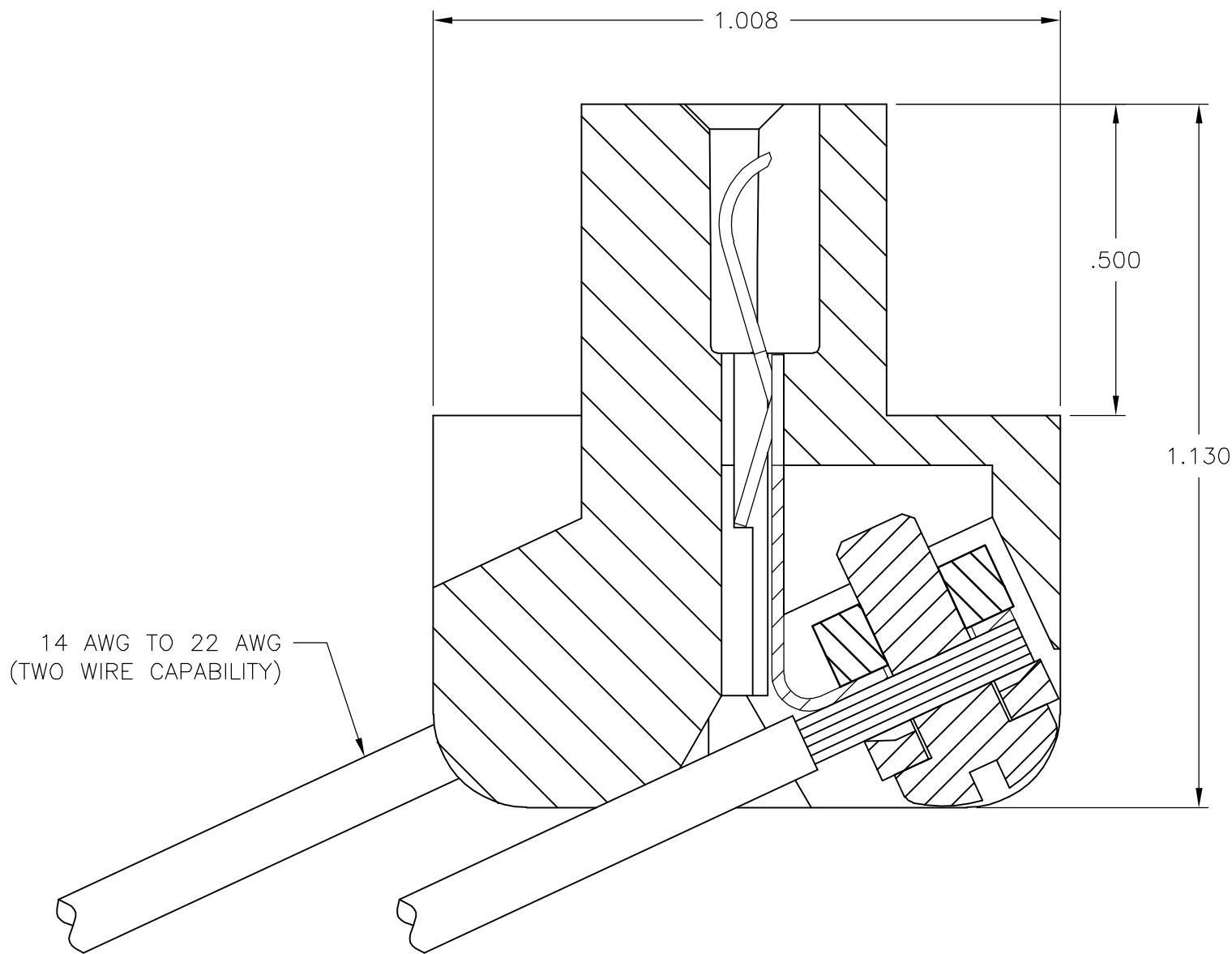
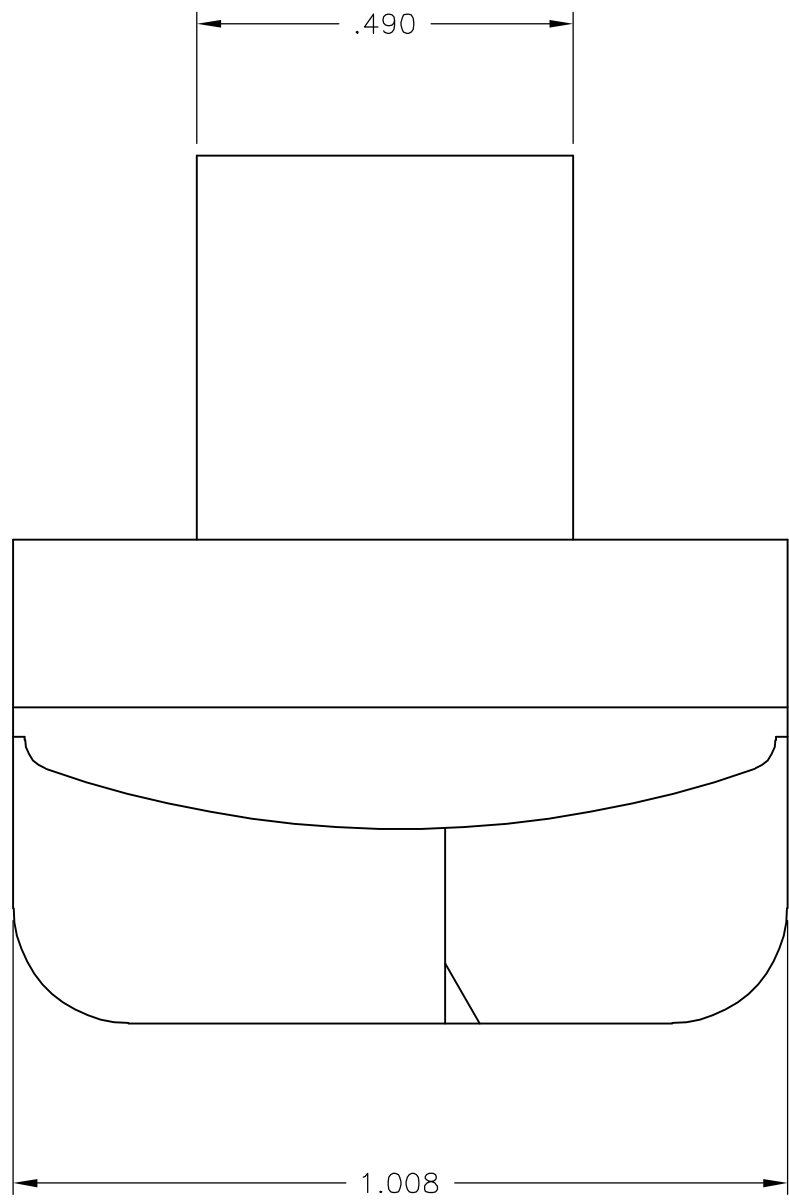
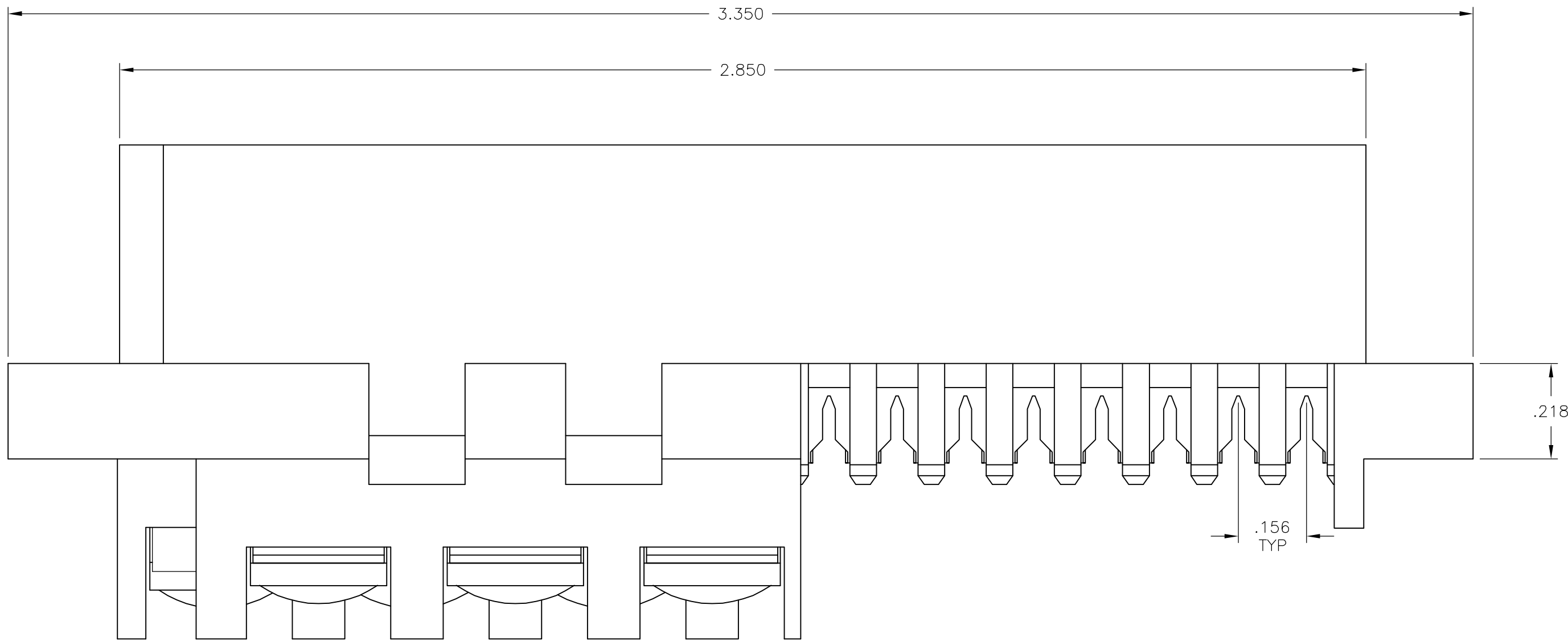
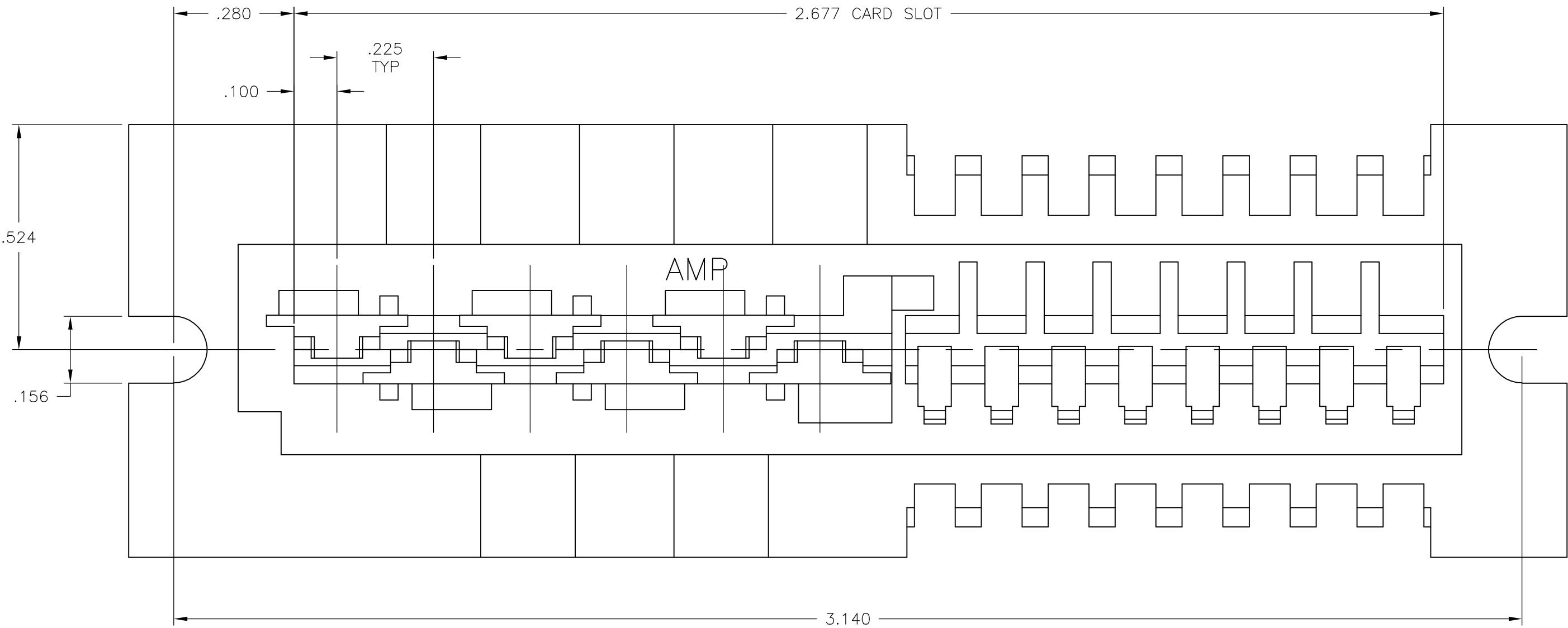


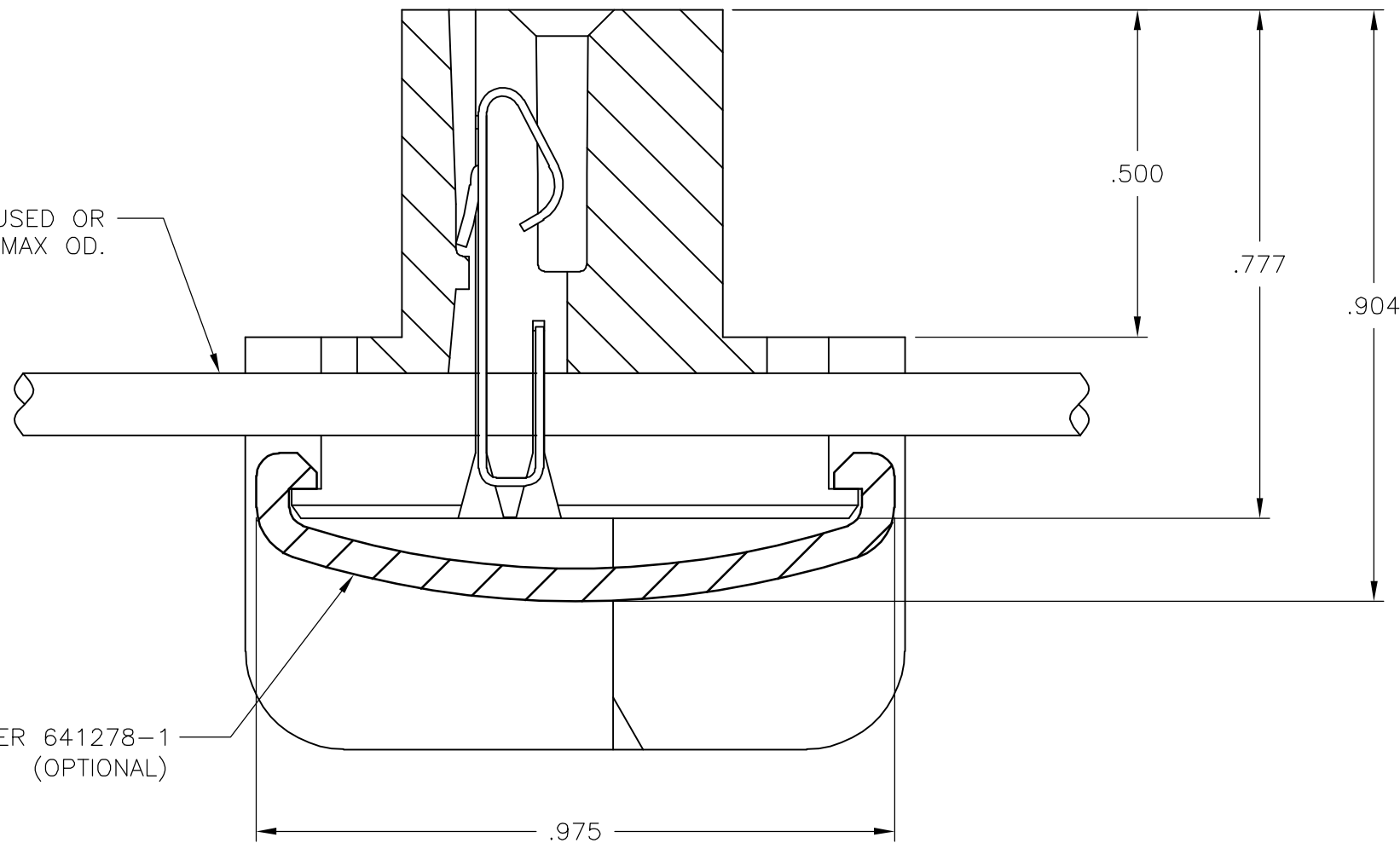
LOC	DIST	REVISIONS				
		P	LTR	DESCRIPTION	DATE	DWN APVD
FT	0	J		REVISED PER ECO-07-004106	2/21/07	SS DC

1. DESIGNED FOR .062±.0075 THICK PRINTED CIRCUIT BOARD.
2. DESIGNED TO CONFORM TO UL 250V COMMERCIAL SPACINGS.
3. PRINTED CIRCUIT BOARD EDGE CONFIGURATION PER CP78-36020.
- ⚠ MUST BE PURCHASED SEPERATELY.
- ⚠ TIN-LEAD PLATE .000100 MIN THK OVER NICKEL UNDERPLATE .000050 MIN THK.
- ⚠ GOLD PLATE .000030 MIN THK OVER NICKEL UNDERPLATE .000050 MIN THICK.
- ⚠ TIN PLATE .000100 MIN THK OVER NICKEL UNDERPLATE .000050 MIN THK.



MULTIPLE TAP SECTION

18 AWG SOLID, FUSED OR
7 STRANDED, .095 MAX OD.



MTA SECTION

OBSOLETE	⚠	1-641277-1
	⚠	641277-2
	⚠	641277-1
	FINISH	PART NUMBER

THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN S SCHLEGEL	03APR2006	tyco Electronics		Tyco Electronics Corporation Harrisburg, PA 17105-3608	
DIMENSIONS: INCHES		CHK D COURS	4/4/06	APVD D COURS		NAME	
		TOLERANCES UNLESS OTHERWISE SPECIFIED:		PRODUCT SPEC		MULTIPLE-TAP/MTA ASSY	
		0 PLC ± -		APPLICATION SPEC		-	
		1 PLC ± -		WEIGHT		A1 00779	
		2 PLC ± -		CUSTOMER DRAWING		SCALE 4:1	
MATERIAL		FINISH		SHEET 1 OF 1		REV J	