

Silicon Carbide Schottky Diode

Features

- 5kV Schottky Rectifier
- Zero Reverse Recovery Current
- High-Frequency Operation
- Temperature-Independent Switching Behavior
- Extremely Fast Switching

$$V_{RRM} = 5000 \text{ V}$$

$$I_F (T_C=135^\circ\text{C}) = 66\text{A (TO247-2L)}$$

$$I_F (T_C=135^\circ\text{C}) = 45\text{A (ceramic)}$$

$$Q_C = 365 \text{ nC}$$

Benefits

- Replace Bipolar with Unipolar Rectifiers
- Essentially No Switching Losses
- Higher Efficiency
- Reduction of Heat Sink Requirements
- Parallel Devices Without Thermal Runaway

Package



TO-247-2L



ceramic

Applications

- Switch Mode Power Supplies (SMPS)
- Power Factor Correction
- Motor Drives



Maximum Rated Values ($T_C=25^{\circ}\text{C}$ unless otherwise specified)

Symbol	Parameter	Value	Unit	Test Conditions	Note
V_{RRM}	Repetitive Peak Reverse Voltage	5000	V		
V_R	DC Peak Reverse Voltage	5000	V		
I_F	Continuous Forward Current (TO-247-2L/ceramic)	66/45	A	$T_C=25^{\circ}\text{C}$	Fig. 3 Fig. 9
		33/22		$T_C=135^{\circ}\text{C}$	
		25/16		$T_C=152^{\circ}\text{C}$	
I_{FRM}	Repetitive Peak Forward Surge Current	63	A	$T_C=25^{\circ}\text{C}$, $t_P=10\text{ ms}$, Half Sine Pulse	
		53		$T_C=110^{\circ}\text{C}$, $t_P=10\text{ ms}$, Half Sine Pulse	
I_{FSM}	Non-Repetitive Forward Surge Current	81	A	$T_C=25^{\circ}\text{C}$, $t_P=10\text{ ms}$, Half Sine Pulse	
		70		$T_C=110^{\circ}\text{C}$, $t_P=10\text{ ms}$, Half Sine Pulse	
$I_{F,MAX}$	Non-Repetitive Forward Surge Current	1150	A	$T_C=25^{\circ}\text{C}$, $t_P=10\mu\text{s}$, Square Wave Pulse	
		950		$T_C=110^{\circ}\text{C}$, $t_P=10\mu\text{s}$, Square Wave Pulse	
P_{tot}	Power Dissipation (TO-247-2L/ceramic)	833/405	W	$T_C=25^{\circ}\text{C}$	Fig. 4 Fig. 10
		361/176		$T_C=110^{\circ}\text{C}$	
T_J	Operating Temperature	-55 to +175	$^{\circ}\text{C}$		
T_{stg}	Storage Temperature	-55 to +175	$^{\circ}\text{C}$		
	TO-247 Mounting Torque	1 8.8	Nm lbf-in	M3 Screw 6-32 Screw	

Electrical Characteristics ($T_J=25^{\circ}\text{C}$)

Symbol	Parameter	Value			Unit	Test Conditions	Note
		Min.	Typ.	Max.			
V_F	Forward Voltage		1.4	1.9	V	$I_F=10\text{A}$, $T_J=25^{\circ}\text{C}$	Fig. 1
			2.4	3.2		$I_F=10\text{A}$, $T_J=175^{\circ}\text{C}$	
I_R	Reverse Current		0.6		mA	$V_R=5000\text{V}$, $T_J=25^{\circ}\text{C}$	Fig. 2
			1.4			$V_R=5000\text{V}$, $T_J=175^{\circ}\text{C}$	
Q_C	Total Capacitive Charge		365		nC	$V_R=2000\text{V}$, $I_F=10\text{A}$ $di/dt=200\text{A}/\mu\text{s}$, $T_J=25^{\circ}\text{C}$	Fig. 5
C	Total Capacitance		3880		pF	$V_R=0\text{V}$, $T_J=25^{\circ}\text{C}$, $f=1\text{MHz}$	Fig. 6
			131			$V_R=1000\text{V}$, $T_J=25^{\circ}\text{C}$, $f=1\text{MHz}$	
			98			$V_R=2000\text{V}$, $T_J=25^{\circ}\text{C}$, $f=1\text{MHz}$	
E_C	Capacitance Stored Energy		241		μJ	$V_R=0\sim 2000\text{ V}$	Fig. 7

Thermal Characteristics

Symbol	Parameter	Value	Unit	Note
$R_{\theta JC}$	Thermal Resistance(Junction to Case)	0.18	$^{\circ}\text{C}/\text{W}$	TO-247-2L Fig. 8
		0.37		ceramic Fig. 11

Typical Performance

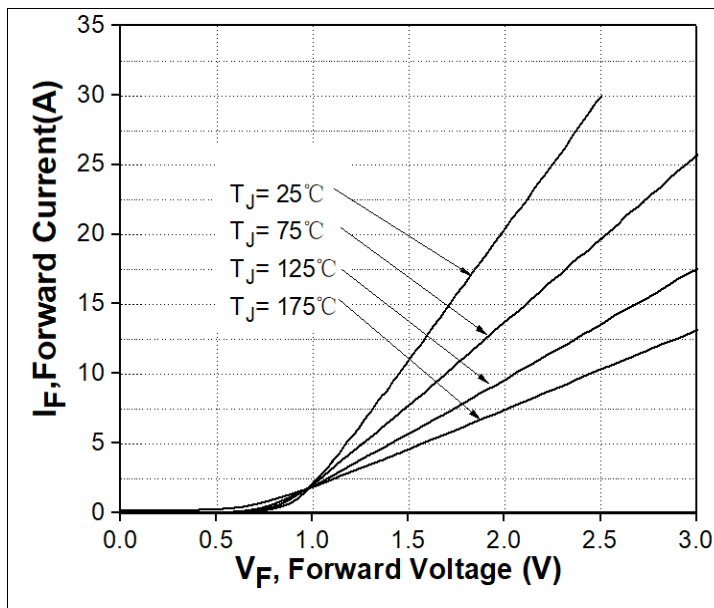


Figure 1. Forward Characteristics

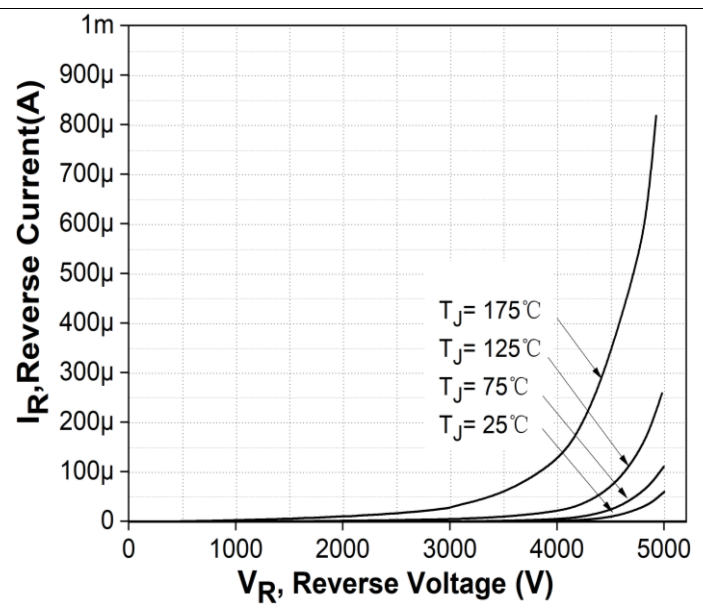


Figure 2. Reverse Characteristics

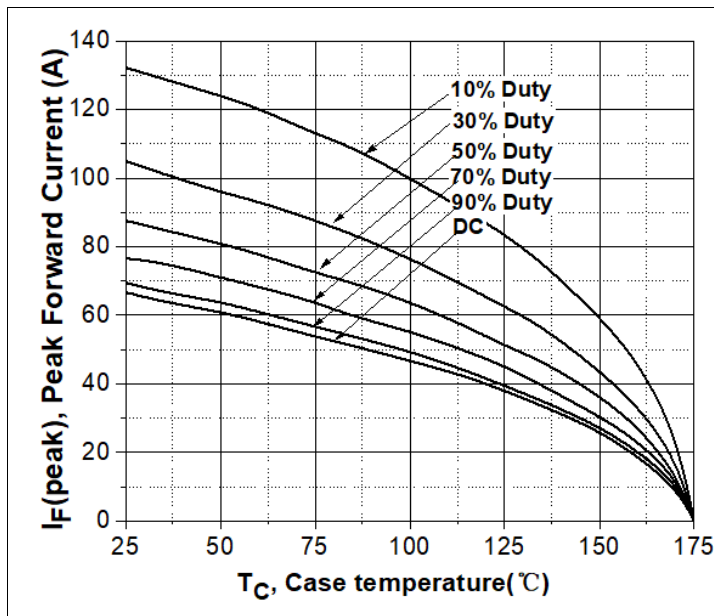


Figure 3. Current Derating (TO-247-2L)

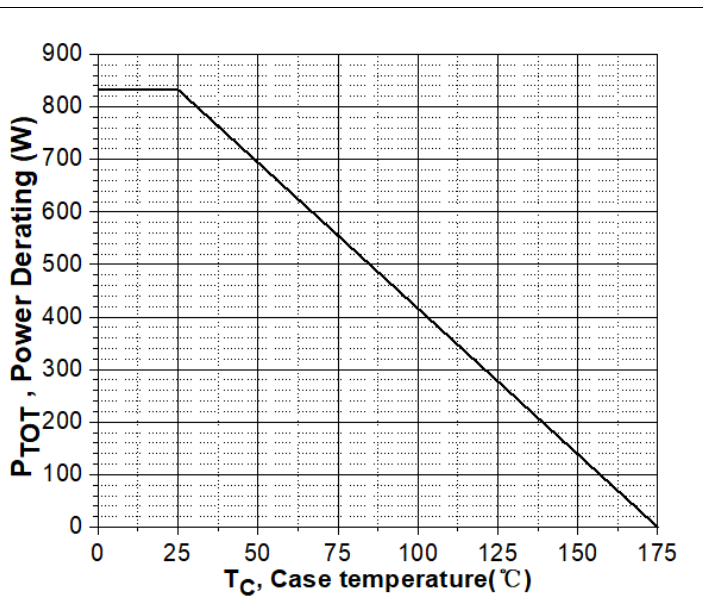


Figure 4. Power Derating (TO-247-2L)

Typical Performance

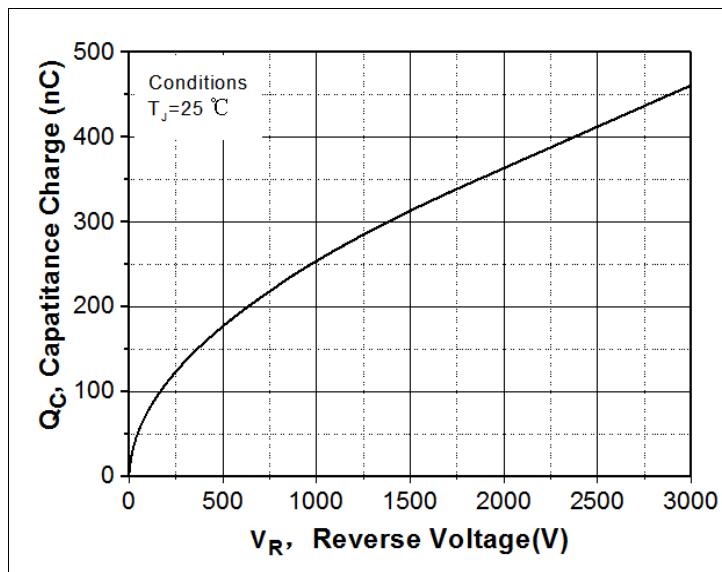


Figure 5. Capacitance Charge Vs. Reverse Voltage

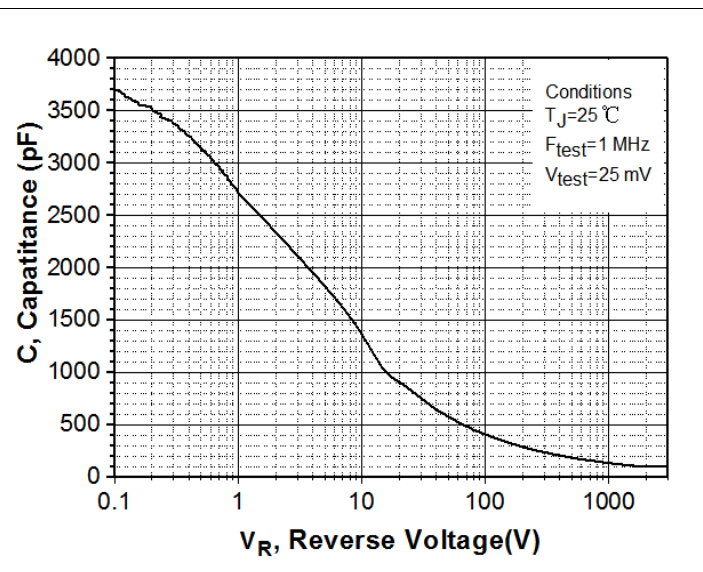


Figure 6. Capacitance Vs. Reverse Voltage

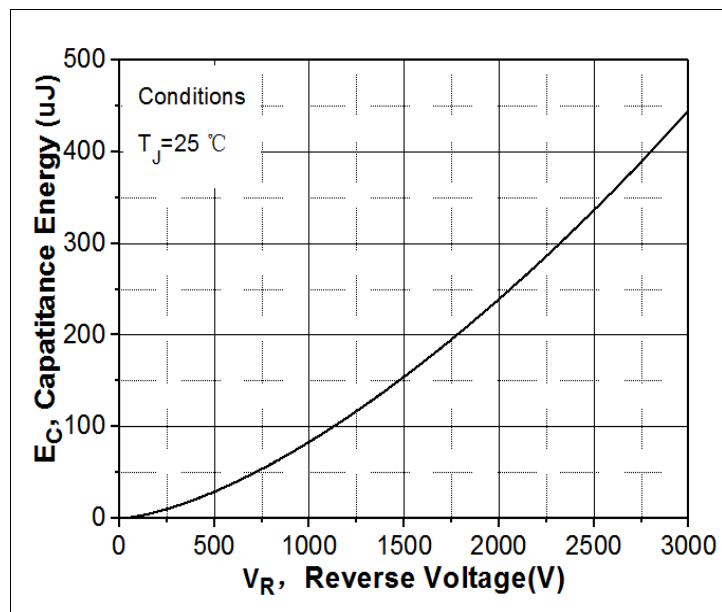


Figure 7. Capacitance Stored Energy

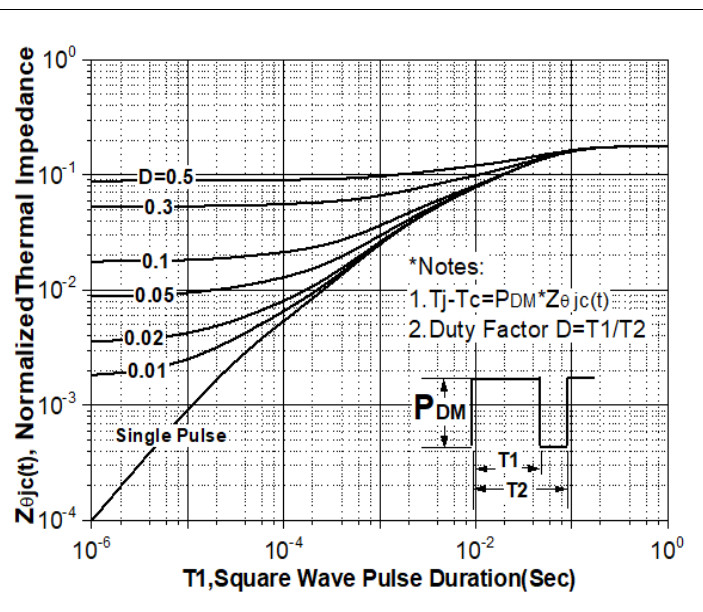


Figure 8. Transient Thermal Response Curve(Junction-to-Case)(TO-247-2L)

Typical Performance

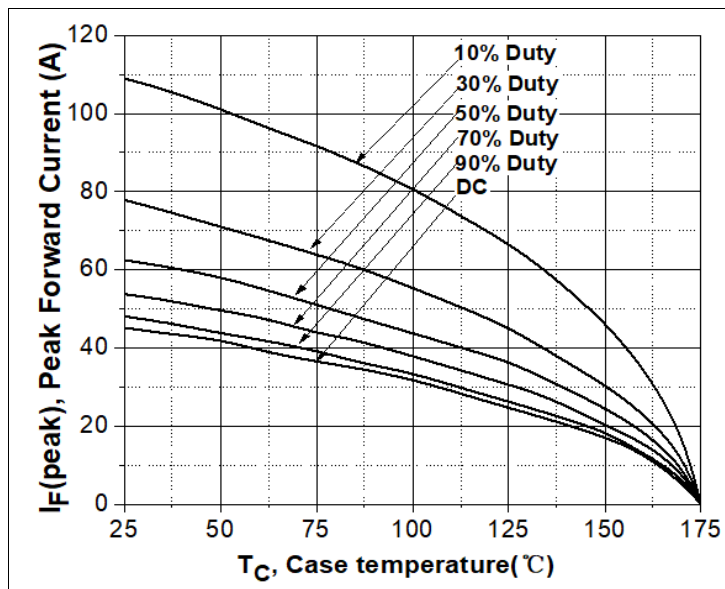


Figure 9. Current Derating (ceramic)

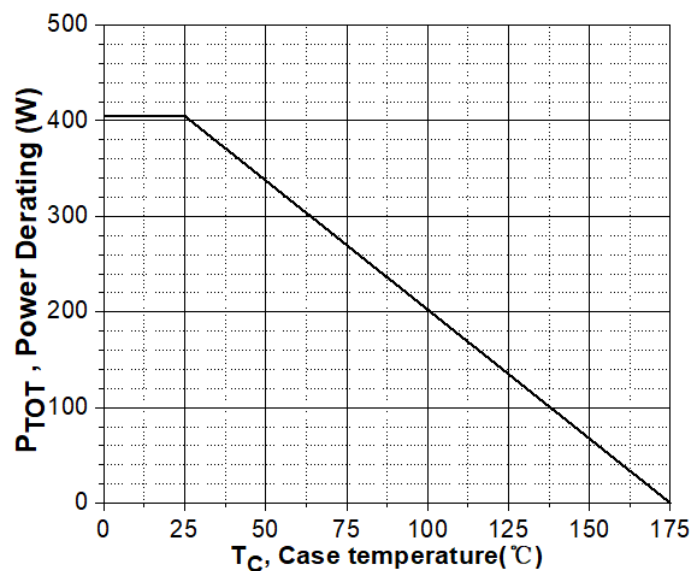
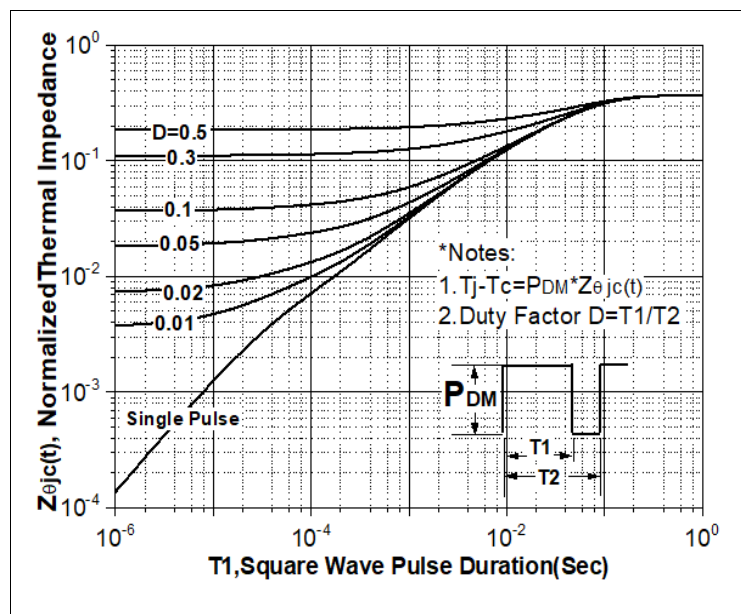


Figure 10. Power Derating (ceramic)



Package TO-247-2L

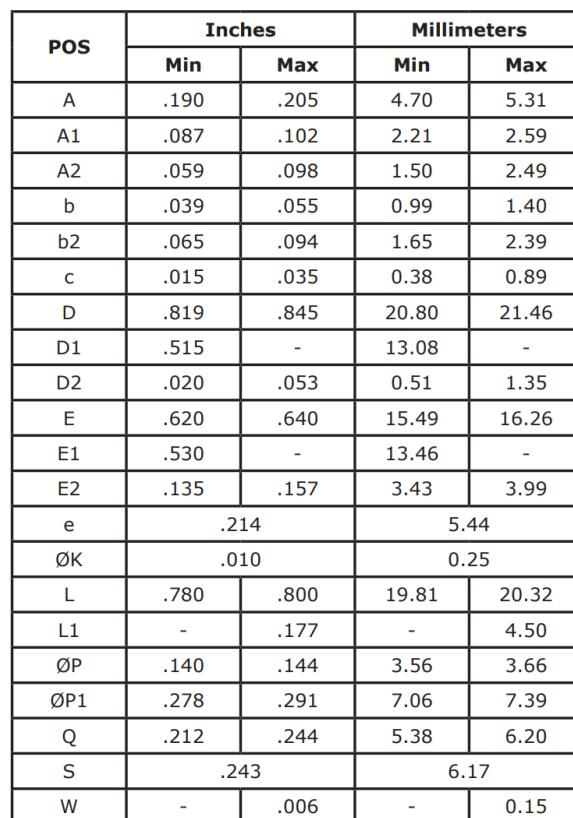


Diagram illustrating a two-layer PCB layout for a through-hole component. The dimensions are as follows:

- Top layer hole diameter: .12
- Bottom layer hole diameter: .08
- Distance between hole centers: .4
- Note: all units are in inches

TO-247-2L