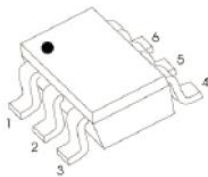
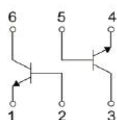


SOT-363



Marking: K6N



特征 Features

- Epitaxial planar die construction
- Ideal for low power amplification and switching
- High Stability and High Reliability

机械数据 Mechanical Data

- SOT-363 Small Outline Plastic Package
- Epoxy UL: 94V-0
- Mounting Position: Any

极限值和温度特性(TA = 25°C 除非另有规定)

Maximum Ratings & Thermal Characteristics (Ratings at 25°C ambient temperature unless otherwise specified.)

参数 Parameters	符号 Symbol	数值 Value	单位 Unit
Collector-Base Voltage	V _{CBO}	60	V
Collector-Emitter Voltage	V _{CEO}	40	V
Emitter -Base Voltage	V _{EBO}	5	V
Collector Current-Continuous	I _C	200	mA
Collector Power Dissipation	P _C	200	mW
Junction Temperature	T _j	150	°C
Storage Temperature	T _{stg}	-55-+150	°C
Thermal resistance From junction to ambient	R _{θJA}	625	°C/W

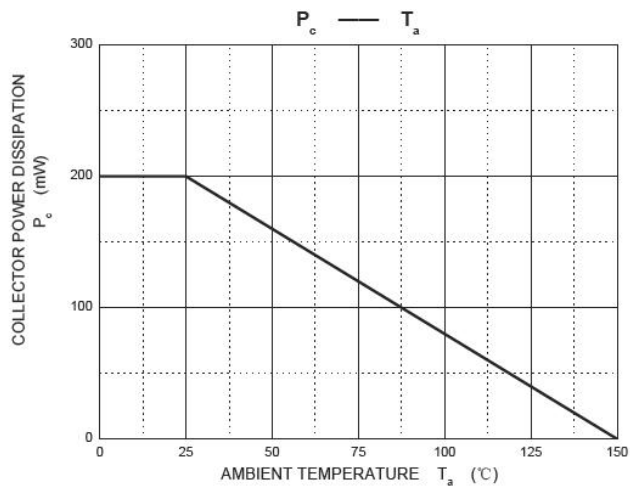
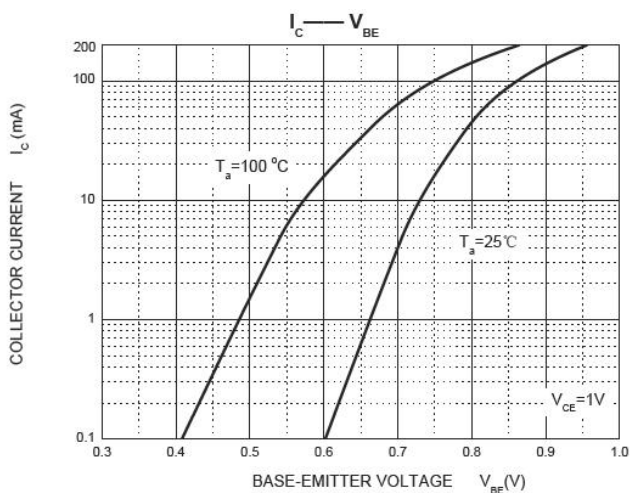
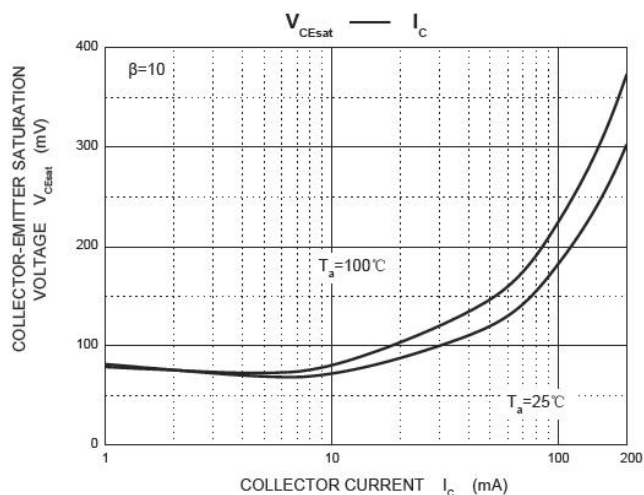
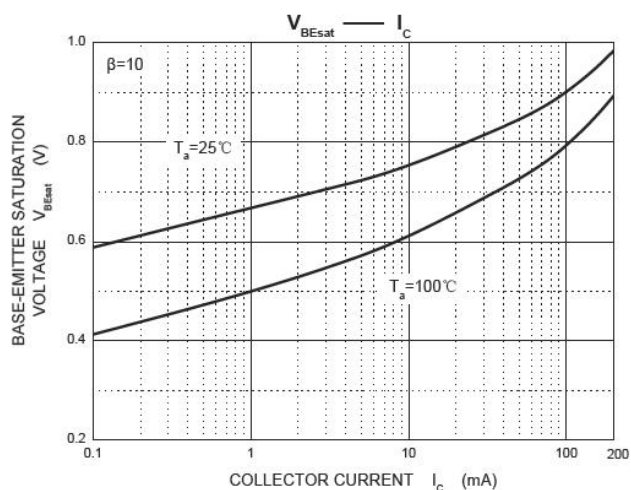
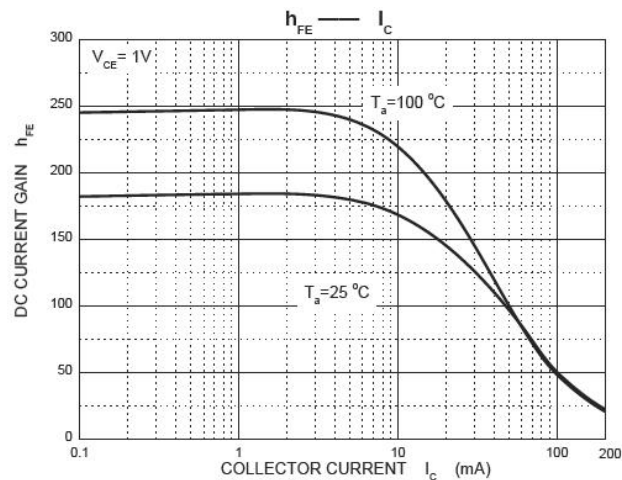
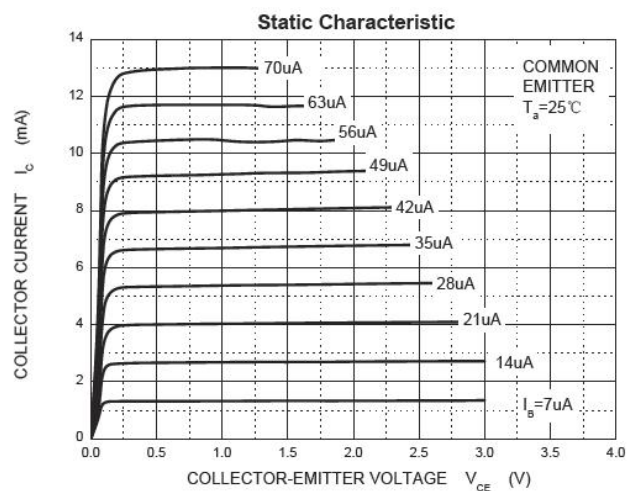
电特性 (TA = 25°C 除非另有规定)

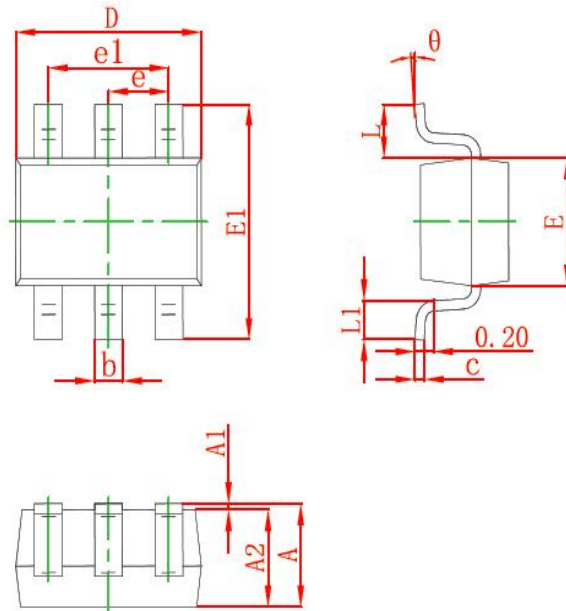
Electrical Characteristics (Ratings at 25°C ambient temperature unless otherwise specified).

参数 Parameter	符号 Symbols	测试条件 Test Condition	界限 Limits		单位 Unit
			Min	Max	
Collector-base breakdown voltage	V _{(BR)CBO}	I _C =10uA, I _E =0	60		V
Collector-emitter breakdown voltage	V _{(BR)CEO}	I _C =1mA, I _B =0	40		V
Emitter-base breakdown voltage	V _{(BR)EBO}	I _E =10uA, I _C =0	5		V
Collector cut-off current	I _{CEX}	V _{CE} =30V, V _{EB(off)} =3V		50	nA
Collector cut-off current	I _{CBO}	V _{CB} =30V, I _E =0		50	nA
Emitter cut-off current	I _{EBO}	V _{EB} =5V, I _C =0		50	nA
DC current gain	h _{FE} (1)	V _{CE} =1V, I _C =0.1mA	40		
	h _{FE} (2)	V _{CE} =1V, I _C =1mA	70		
	h _{FE} (3)	V _{CE} =1V, I _C =10mA	100	300	
	h _{FE} (4)	V _{CE} =1V, I _C =50mA	60		
	h _{FE} (5)	V _{CE} =1V, I _C =100mA	30		
Collector-emitter saturation voltage	V _{CE(sat)}	I _C =10mA, I _B =1mA		0.20	V
		I _C =50mA, I _B =5mA		0.30	V
Base -emitter saturation voltage	V _{BE(sat)}	I _C =10mA, I _B =1mA	0.65	0.85	V
		I _C =50mA, I _B =5mA		0.95	V
Transition frequency	f _T	V _{CE} =20V, I _C =10mA, f=100MHz	300		MHz
Collector output capacitance	C _{ob}	V _{CB} =5V, I _E =0, f=1MHz		4	pF
Noise figure	NF	V _{CE} =5V, I _C =0.1mA, f=1KHz, R _S =1KΩ		5	dB
Delay time	t _d	V _{CC} =3V, V _{BE(off)} =-0.5V, I _C =10mA, I _{B1} =1mA		35	nS
Rise time	t _r			35	nS
Storage time	t _s			200	nS
Fall time	t _f	V _{CC} =3V, I _C =10mA, I _{B1} =I _{B2} =1mA		50	nS

*Pulse test: pulse width≤300us,duty cycle≤2.0%

Typical characteristics

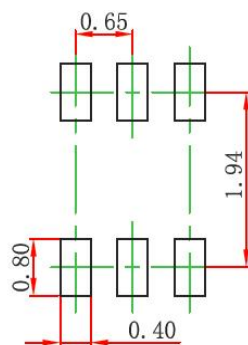


SOT-363 PACKAGE OUTLINE Plastic surface mounted package


Symbol	Dimensions in Millimeters	
	Min	Max
A	0.900	1.100
A1	0.000	0.100
A2	0.900	1.000
b	0.150	0.350
c	0.100	0.150
D	2.000	2.200
E	1.150	1.350
E1	2.150	2.400
e	0.650 TYP	
e1	1.200	1.400
L	0.525 REF	
L1	0.260	0.460
θ	0°	8°

焊盘设计参考Precautions: PCB Design

Recommended land dimensions for SOT-363 diode. Electrode patterns for PCBs


Note:

1. Controlling dimension: in millimeters.
2. General tolerance: $\pm 0.05\text{mm}$.
3. The pad layout is for reference purposes only.

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