

CSD19534Q5A(ES)

Rev-1.6

SuperMOS –PDFN5*6-8L 100V BV_{DSS}, 6.5mΩ R_{DS(ON)}, N-channel MOSFET

1. Description

The CSD19534Q5A(ES) is N-Channel enhancement MOS Field Effect Transistor. Uses advanced shielded gate trench technology and design to provide excellent R_{DS(ON)} with low gate charge. Device is suitable for use in DC-DC conversion, power switch and charging circuit. Standard Product CSD19534Q5A(ES) is Pb-free.

2. Features

- 100V, R_{DS(ON)}=6.5mΩ(Typ.) @V_{GS}=10V
 - High density cell design for low RDS(on)
 - Material: Halogen free
- Reliable and rugged
 - Avalanche Rated
 - Low leakage current

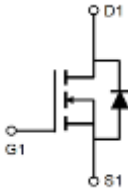
3. Applications

- PWM applications
 - Load switch
 - Power management in portable/desktop PCs
 - DC/DC conversion
- 100% UIS TESTED

4. Ordering Information

Part Number	Package	Marking	Material	Packing	Quantity per Reel	Flammability Rating	Reel Size
CSD19534Q5A(ES)	PDFN5*6-8L	GNJ10R085/LOT	Halogen free	Tape & Reel	5,000 PCS	UL 94V-0	13 inches

5. Pin Configuration and Functions

Pin	Function	Outline	Circuit Diagram
4	Gate	Note 2 	
1/2/3	Source		
5/6/7/8	Drain		

6. Specification

Absolute Maximum Rating & Thermal Characteristics

Ratings at 25 °C ambient temperature unless otherwise specified.

Parameter		Symbol	Limit	Unit
Drain-Source Voltage		BV_{DSS}	100	V
Gate-Source Voltage		V_{GS}	± 20	V
Continuous Drain Current	$T_C=25^{\circ}\text{C}$	I_D	92	A
	$T_C=100^{\circ}\text{C}$		55.2	
Maximum Power Dissipation		P_D	125	W
Pulsed Drain Current		I_{DM}	368	A
Single Pulse Avalanche Current ¹		I_{AS}	24	A
Single Pulse Avalanche Energy ¹		E_{AS}	144	mJ
Operating Junction Temperature		T_J	150	$^{\circ}\text{C}$
Lead Temperature		T_L	260	$^{\circ}\text{C}$
Storage Temperature Range		T_{stg}	-55 to 150	$^{\circ}\text{C}$

Thermal resistance ratings

Single Operation				
Parameter	Symbol	Typical	Maximum	Unit
Junction-to-Case Thermal Resistance	$R_{\theta JC}$		1.0	$^{\circ}\text{C/W}$

Notes:

1: The EAS test condition is $T_J=25^{\circ}\text{C}$, $V_{DD}=50\text{V}$, $V_{GS}=10\text{V}$, $L=0.5\text{mH}$, $R_g=25\Omega$

2: This diagram is only an electrical schematic, and the actual pin size is based on POD.

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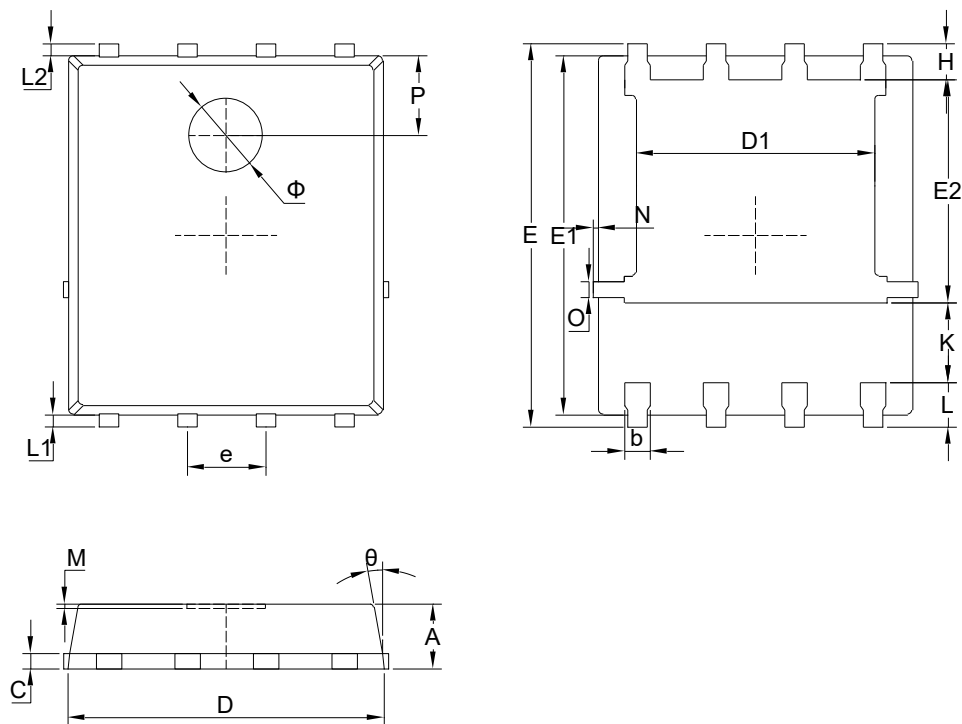
Electrical Characteristics

At TA = 25°C unless otherwise specified

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
OFF CHARACTERISTICS						
Drain-to-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =250uA	100			V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =100V, V _{GS} =0V			1.0	uA
Gate-to-source Leakage Current	I _{GSS}	V _{DS} =0V, V _{GS} =±20V			±100	nA
ON CHARACTERISTICS						
Gate Threshold Voltage	V _{GS(TH)}	V _{GS} =V _{DS} , I _D =250uA	2.4	3.0	3.6	V
Drain-to-source On-resistance	R _{DS(on)}	V _{GS} =10V, I _D =30A		6.5	8.4	mΩ
CHARGES, CAPACITANCES AND GATE RESISTANCE						
Input Capacitance	C _{ISS}	V _{GS} =0V, f=1MHz, V _{DS} =25V		1800		pF
Output Capacitance	C _{OSS}			1357		
Reverse Transfer Capacitance	C _{RSS}			53		
Total Gate Charge	Q _{G(TOT)}	V _{GS} =10V, V _{DS} =50V, I _D =20A		29		nC
Gate-to-Source Charge	Q _{GS}			6.8		
Gate-to-Drain Charge	Q _{GD}			8.4		
SWITCHING CHARACTERISTICS						
Turn-On Delay Time	t _{d(ON)}	V _{GS} =10V, V _{DD} =50V, I _D =20A, R _G =6Ω		8.4		ns
Rise Time	t _r			9.4		
Turn-Off Delay Time	t _{d(OFF)}			27		
Fall Time	t _f			18		
BODY DIODE CHARACTERISTICS						
Forward Voltage	V _{SD}	V _{GS} =0V, I _S =30A			1.2	V

7. Dimension (PDFN5*6-8L)

POD A(J)

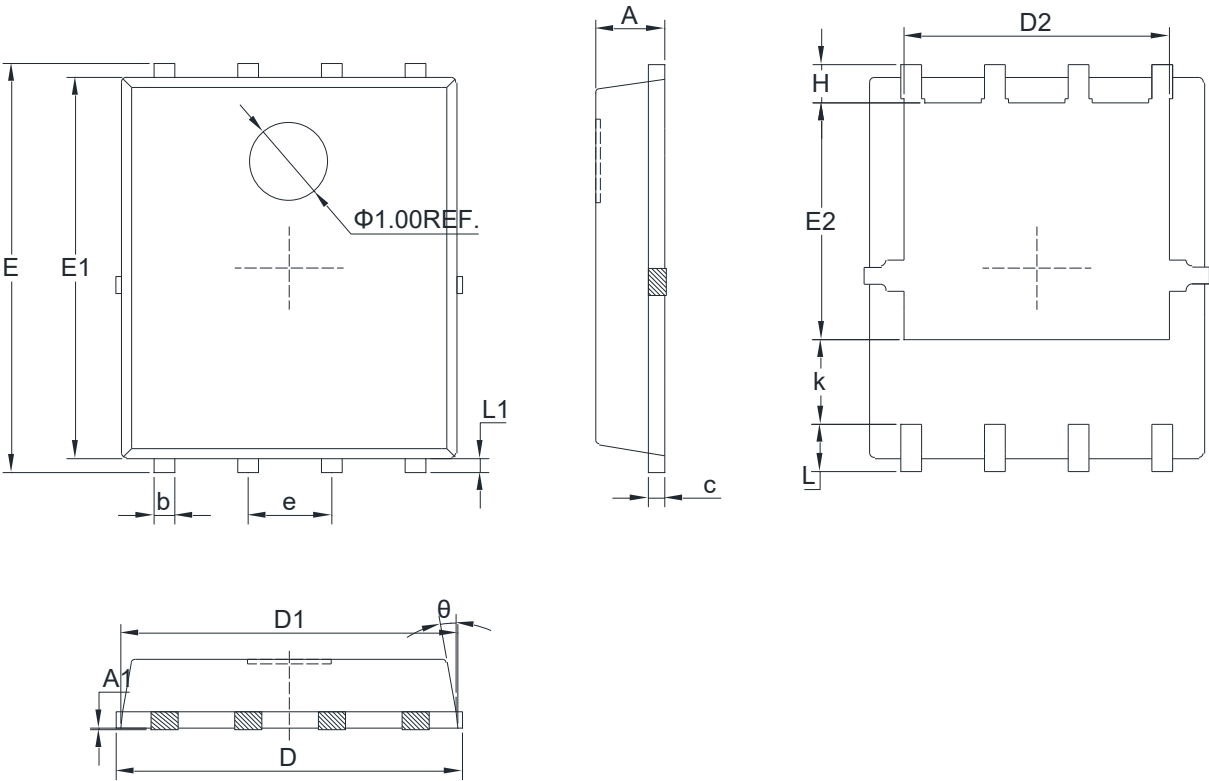


Symbol	Dimensions in Millimeters			Symbol	Dimensions in Millimeters		
	MIN	NOM	MAX		MIN	NOM	MAX
A	0.90	1.05	1.20	H	0.48	0.58	0.68
b	0.35	0.40	0.50	K	1.17	1.27	1.37
C	0.20	0.25	0.35	L	0.64	0.74	0.84
D	4.90	5.05	5.20	L1/L2	0.20 REF.		
D1	3.72	3.82	3.92	θ	8°	10°	12°
E	6.00	6.15	6.30	M	0.08 REF.		
E1	5.60	5.75	5.90	N	0	-	0.15
E2	3.47	3.57	3.67	O	0.25 REF.		
e	1.27 BSC.			P	1.28 REF.		
Φ	1.20 REF						

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POD B(Q)

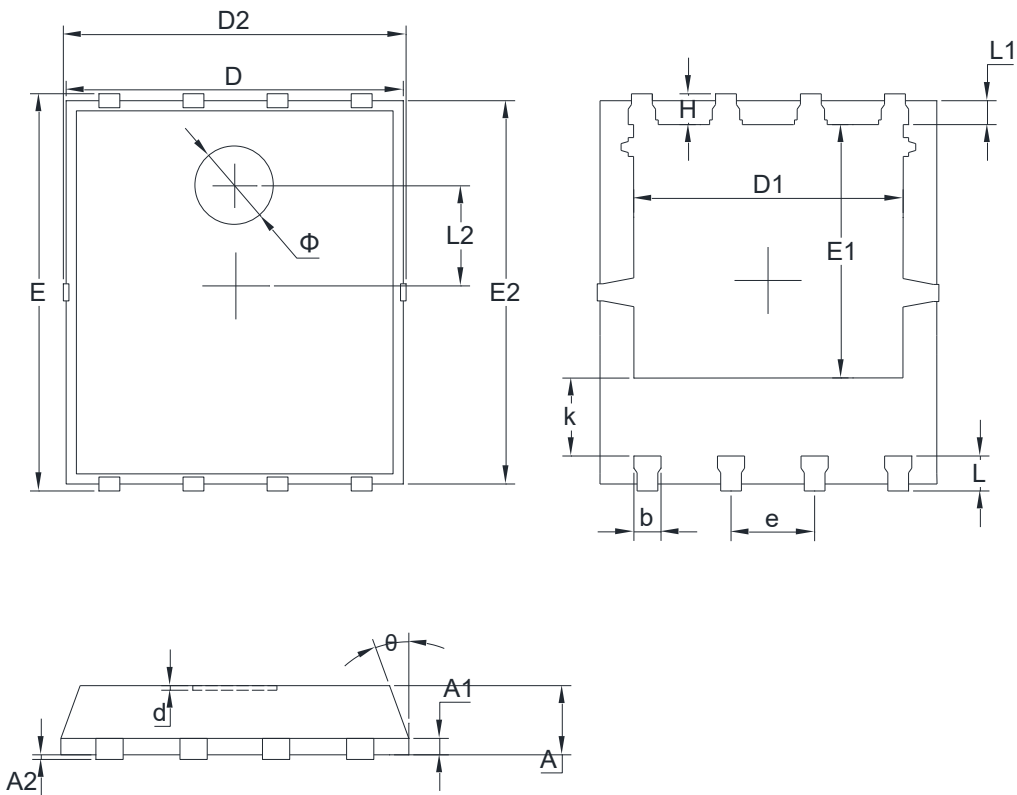


Symbol	Dimensions in Millimeters			Symbol	Dimensions in Millimeters		
	MIN	NOM	MAX		MIN	NOM	MAX
A	0.90	1.00	1.10	H	0.46	0.56	0.66
A1			0.05	k	1.10		
b	0.30	0.40	0.50	L	0.51	0.61	0.71
c	0.20	0.25	0.30	L1	0.05	0.15	0.25
D	4.80	5.10	5.40	θ	8°	10°	12°
D2	3.75	3.90	4.05	D1	4.80	4.90	5.00
E	5.95	6.05	6.15				
E1	5.65	5.75	5.85				
e	1.27BCS						
E2	3.34	3.49	3.64				

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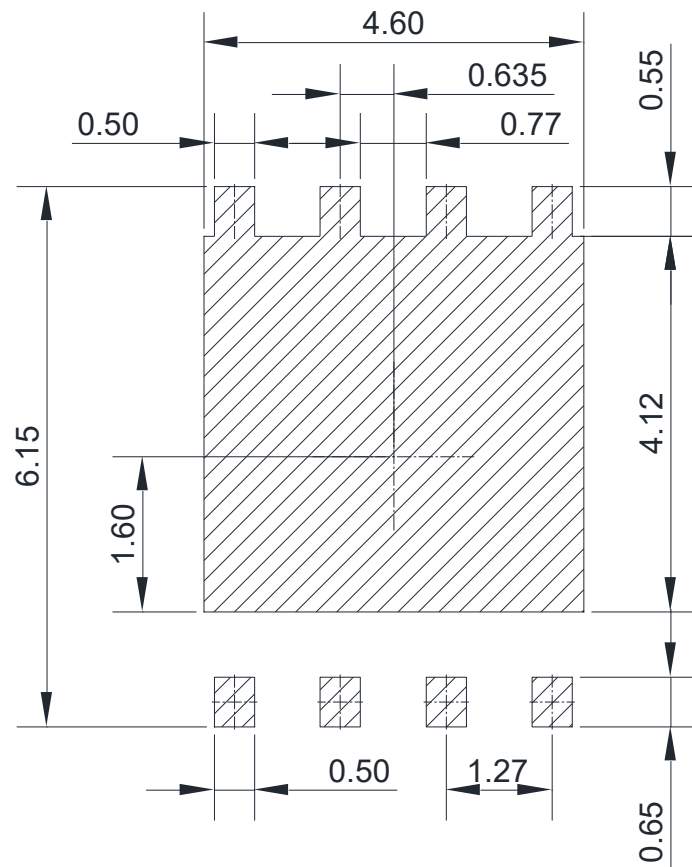
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POD C(X)



Symbol	Dimensions in Millimeters			Symbol	Dimensions in Millimeters		
	MIN	NOM	MAX		MIN	NOM	MAX
A	0.900	1.000	1.100	H	0.549	0.625	0.701
A1	0.254 REF			k	1.190	1.290	1.390
A2	0.000		0.050	L	0.534	0.610	0.686
D	4.824	4.900	4.976	L1	0.424	0.500	0.576
D1	3.910	4.010	4.110	θ	8°	10°	12°
D2	4.924	5.000	5.076	L2	1.800 REF		
E	5.924	6.000	6.076	ϕ	1.100	1.200	1.300
E1	3.375	3.475	3.575	d			0.100
e	1.270 TYP			b	0.350	0.400	0.450
E2	5.674	5.750	5.826				

8. Recommended Soldering Footprint



DIMENSIONS: MILLIMETERS

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