# MSKSEMI 美森科













**ESD** 

TVS

TSS

MOV

GDT

PIFD

## MB14F-MS THRU MB120F-MS

**Product specification** 





#### **FEATURES**

- Reverse Voltage 40 to 200 V
- Forward Current 1 A
- High Surge Current Capability
- Designed for Surface Mount Application

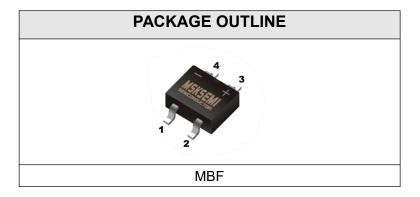
### **MECHANICAL DATA**

Case: MBF

• Terminals: Solderable per MIL-STD-750, Method 2026

• Approx. Weight: 75mg 0.0026oz

## **Reference News**



PIN	DESCRIPTION			
1	Input Pin (~)			
2	Input Pin ( ~ )			
3	Output Anode (+)			
4	Output Cathode ( - )			

MB14F-MS	MB16F-MS	MB18F-MS	MB110F-MS	MB115F-MS	MB120F-MS
- +	- +	- +	- +	- +	- +
MB14F	MB16F	MB18F	MB110F	MB115F	MB120F



## **Maximum Ratings and Electrical characteristics**

Ratings at 25 ambient temperature unless otherwise specified.

Single phase half-wave 60 Hz, resistive or inductive load, for capacitive load current derate by 20 %.

Parameter	Symbols	MB14F -MS	MB16F -MS	MB18F -MS	MB110F -MS	MB115F -MS	MB120F -MS	Units
Maximum Repetitive Peak Reverse Voltage	$V_{RRM}$	40	60	80	100	150	200	V
Maximum RMS voltage	V <sub>RMS</sub>	28	42	56	70	105	140	V
Maximum DC Blocking Voltage	V <sub>DC</sub>	40	60	80	100	150	200	٧
Maximum Average Forward Rectified Current	I <sub>F(AV)</sub>	1.0				А		
Peak Forward Surge Current,8.3ms Single Half Sine-wave Superimposed on Rated Load (JEDEC method)	I <sub>FSM</sub>	40			30		А	
Max Instantaneous Forward Voltage at 1 A	V <sub>F</sub>	0.55	0.55 0.70		0.85	0.90		V
Maximum DC Reverse Current $T_a = 25^{\circ}$ C at Rated DC Reverse Voltage $T_a = 100^{\circ}$ C	I <sub>R</sub>	0.3 10		0.2 0.1 5 2		mA		
Typical Junction Capacitance 1)	C <sub>j</sub>	110 80			pF			
Typical Thermal Resistance 2)	$R_{\theta JA}$	R <sub>0JA</sub> 100			°C/W			
Operating Junction Temperature Range	Tj	-55 ~ +125					°C	
Storage Temperature Range				-55 ~	+150			°C

Note: 1. Measured at 1MHz and applied reverse voltage of 4 V D.C.

<sup>2.</sup> Mounted on glass epoxy PC board with  $4 \times 1.5" \times 1.5"$  (  $3.81 \times 3.81$  cm ) copper pad.



Fig.1 Forward Current Derating Curve

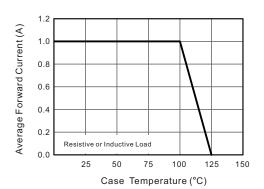
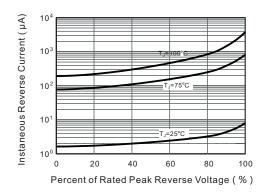


Fig.2 Typical Reverse Characteristics



20 10 10 10 MB14F-MS MB16F-MS/MB18F-MS MB110F-MS MB110F-

Fig.4 Typical Junction Capacitance

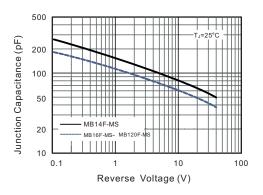


Fig.5 Maximum Non-Repetitive Peak Forward Surage Current

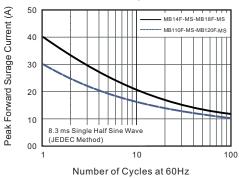
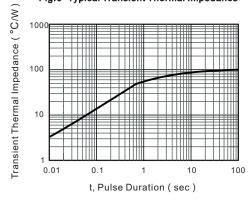
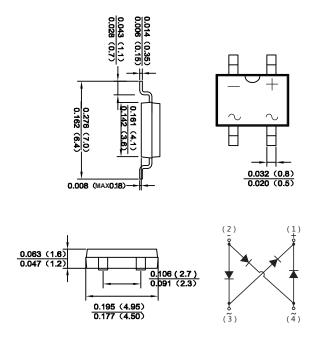


Fig.6- Typical Transient Thermal Impedance



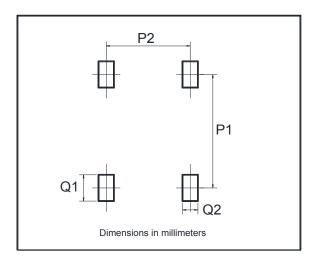


#### **MBF**



Dimensions in inches and (millimeters)

## **Suggested Pad Layout**



Dim	Min
P1	6.00
P2	2.40
Q1	1.84
Q2	1.20

## **REEL SPECIFICATION**

P/N	PKG	QTY
MB14F-MS THRU MB120F-MS	MBF	5000



#### **Attention**

- Any and all MSKSEMI Semiconductor products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your MSKSEMI Semiconductor representative nearest you before using any MSKSEMI Semiconductor products described or contained herein in such applications.
- MSKSEMI Semiconductor assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all MSKSEMI Semiconductor products described or contained herein.
- Specifications of any and all MSKSEMI Semiconductor products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer'sproducts or equipment.
- MSKSEMI Semiconductor. strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with someprobability. It is possiblethat these probabilistic failures could give rise to accidents or events that could endanger human lives, that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits anderror prevention circuitsfor safedesign, redundant design, and structural design.
- In the event that any or all MSKSEMI Semiconductor products (including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from theauthorities concerned in accordance with the above law.
- No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of MSKSEMI Semiconductor.
- Information (including circuit diagrams and circuit parameters) herein is for example only; it is not guaranteed for volume production. MSKSEMI Semiconductor believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.
- Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. Whendesigning equipment, referto the "Delivery Specification" for the MSKSEMI Semiconductor productthat you intend to use.