

6001

DATASHEET

Specification Revision History:

Version	Date	Description
V1.0	2019/08	New
V1.1	2021/05	Modify Ordering Information
V1.2	2025/02	Modify Ordering Information
V1.3	2025/03	Add application precautions and overall typesetting.

General Description

The 600X family have a high gain-bandwidth product of 1MHz, a slew rate of 0.8V/us, and a quiescent current of 75µA amplifier at 5V. The 600X family is designed to provide optimal performance in low voltage and low noise systems. They provide rail-to-rail output swing into heavy loads. The input common mode voltage range includes ground, and the maximum input offset voltage is 3.5mV for 600X family. They are specified over the extended industrial temperature range (-40°C to +125°C). The operating range is from 1.8V to 6V. The 6001 single is available in Green SC70-5 and SOT23-5 packages. The 6002 and MSOP-8 packages. The 6004 Quad is available in Green SOP-14 and TSSOP-14 packages.

Features

- Single-Supply Operation from +1.8V ~ +6V
- Rail-to-Rail Input / Output
- Gain-Bandwidth Product: 1MHz (Typ)
- Low Input Bias Current: 1pA (Typ)
- Low Offset Voltage: 3.5mV (Max)
- Quiescent Current: 75µA per Amplifier (Typ)
- Embedded RF Anti-EMI Filter
- Operating Temperature: -40°C ~ +125°C
- Small Package:

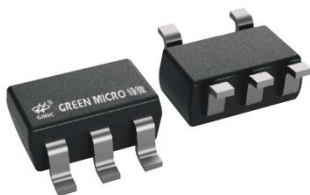
6001 Available in SOT23-5 and SC70-5 Packages

6002 Available in SOP-8 and MSOP-8 Packages

6004 Available in SOP-14 and TSSOP-14 Packages

Applications

- ASIC Input or Output Amplifier
- Sensor Interface
- Medical Communication
- Smoke Detectors
- Audio Output
- Piezoelectric Transducer Amplifier
- Medical Instrumentation
- Portable Systems



SOT-23-5

Ordering Information

Product Model	Package Type	Marking	Packing	Packing Qty
GM6001DR	SOT-23-5	AAGD	REEL	3000PCS/REEL

Pin Configuration

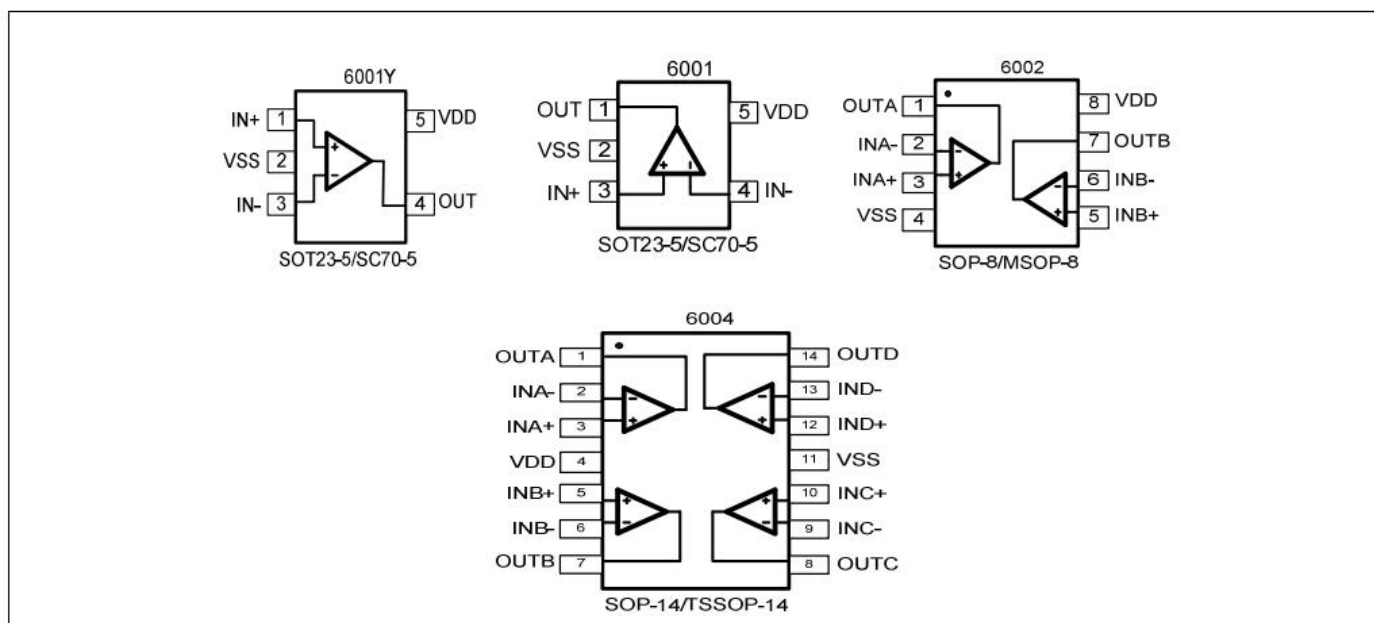


Figure 1. Pin Assignment Diagram

Absolute Maximum Ratings

Condition	Min	Max
Power Supply Voltage (V _{DD} to V _{SS})	-0.5V	+7.5V
Analog Input Voltage (IN+ or IN-)	V _{SS} -0.5V	V _{DD} +0.5V
PDB Input Voltage	V _{SS} -0.5V	+7V
Operating Temperature Range	-40°C	+125°C
Junction Temperature	+160°C	
Storage Temperature Range	-55°C	+150°C
Lead Temperature (soldering,10sec)	+260°C	
Package Thermal Resistance(TA=+25°C)		
SOP-8,θ _{JA}	125°C/W	
MSOP-8,θ _{JA}	216°C/W	
SOT23-5,θ _{JA}	190°C/W	
SC70-5,θ _{JA}	333°C/W	
ESD Susceptibility		
HBM	6KV	
MM	400V	

Note: Stress greater than those listed under Absolute Maximum Ratings may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions outside those indicated in the operational sections of this specification are not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

Electrical Characteristics

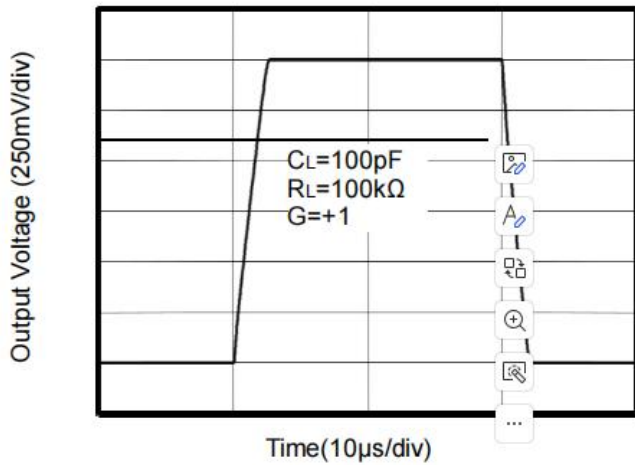
(At $V_S=+5V$, $R_L=100k\Omega$ connected to $V_S/2$, and $V_{OUT}=V_S/2$, unless otherwise noted.)

PARAMETER	SYMBOL	CONDITIONS	6001/2/4				
			TYP	MIN/MAXOVER TEMPERATURE			
			+25°C	+25°C	40°Cto+85°C	UNITS	MIN/MAX
INPUT CHARACTERISTICS							
Input Offset Voltage	V _{OS}	V _{CM} =V _S /2	0.8	3.5	5.6	mV	MAX
Input Bias Current	I _B		1			pA	TYP
Input Offset Current	I _{OS}		1			pA	TYP
Common-Mode Voltage Range	V _{CM}	V _S =5.5V	-0.1 to +5.6			V	TYP
Common-Mode Rejection Ratio	CMRR	V _S =5.5V,V _{CM} =-0.1Vto 4V	70	62	62	dB	MIN
		V _S =5.5V,V _{CM} =-0.1V to 5.6V	68	56	55		
Open-Loop Voltage Gain	A _{OL}	R _L =5kΩ,V _O =+0.1V to +4.9V	80	70	70	dB	MIN
		R _L =10kΩ,V _O =+0.1V to +4.9V	100	94	85		
Input Offset Voltage Drift	ΔV _{OS} /ΔT		2.7			μV/°C	TYP
OUTPUT CHARACTERISTICS							
Output Voltage Swing from Rail	V _{OH}	R _L =100kΩ	4.997	4.980	4.970	V	MIN
	V _{OL}	R _L =100kΩ	5	20	30	mV	MAX
	V _{OH}	R _L =10kΩ	4.992	4.970	4.960	V	MIN
	V _{OL}	R _L =10kΩ	8	30	40	mV	MAX
Output Current	I _{SOURCE}	R _L =100Ω to V _S /2	84	60	45	mA	MIN
	I _{SINK}		75	60	45		
POWER SUPPLY							
Operating Voltage Range				1.8	1.8	V	MIN
				6	6	V	MAX
Power Supply Rejection Ratio	PSRR	V _S =+2.5V to +6V,V _{CM} =+0.5V	82	60	58	dB	MIN
Quiescent Current /Amplifier	I _Q		75	110	125	μA	MAX
DYNAMIC PERFORMANCE(CL=100pF)							
Gain-Bandwidth Product	GBP		1			MHz	TYP
Slew Rate	SR	G=+1,2V Output Step	0.8			V/μs	TYP
Setting Time to 0.1%	t _S	G=+1,2V Output Step	5.3			μs	TYP
Overload Recovery Time		V _{IN} ·Gain =V _S	2.6			μs	TYP
NOISE PERFORMANCE							
Voltage Noise Density	e _n	f=1kHz	27			nV/√Hz	TYP
		f=10kHz	20			nV/√Hz	TYP

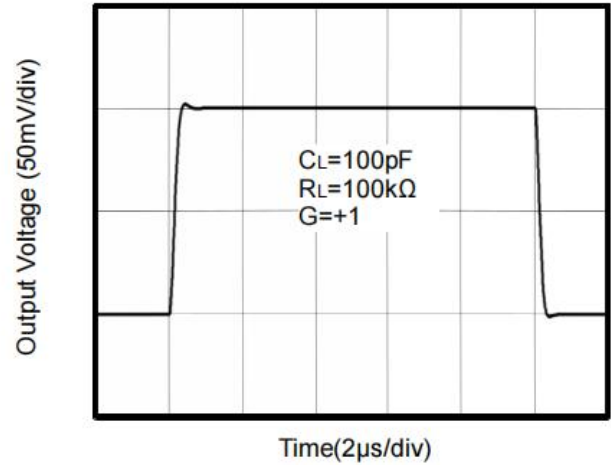
Typical Performance characteristics

At $T_A=+25^{\circ}\text{C}$, $V_S=5\text{V}$, $R_L=100\text{k}\Omega$ connected to $V_S/2$ and $V_{OUT}=V_S/2$, unless otherwise noted.

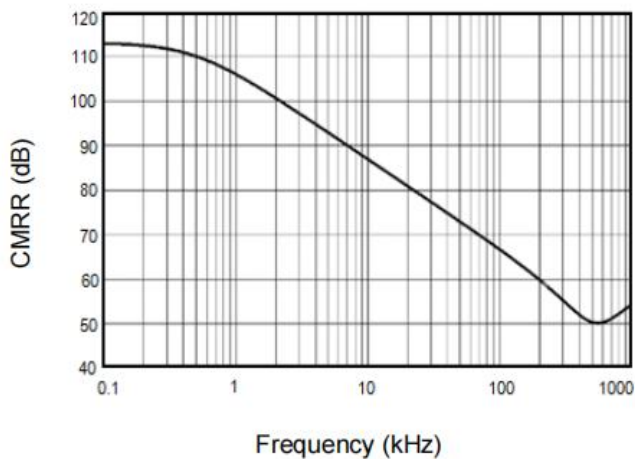
Large Signal Transient Response



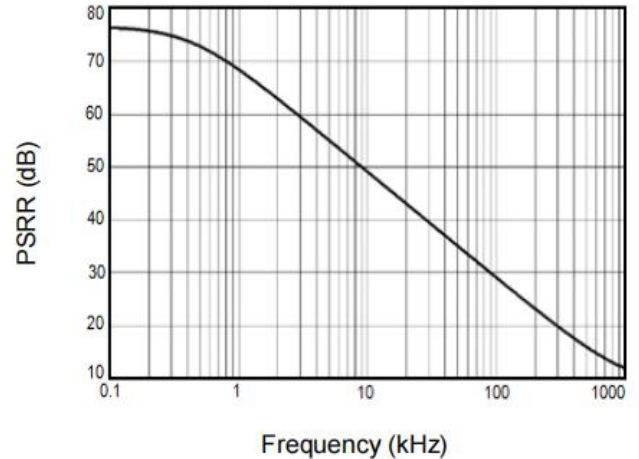
Small Signal Transient Response



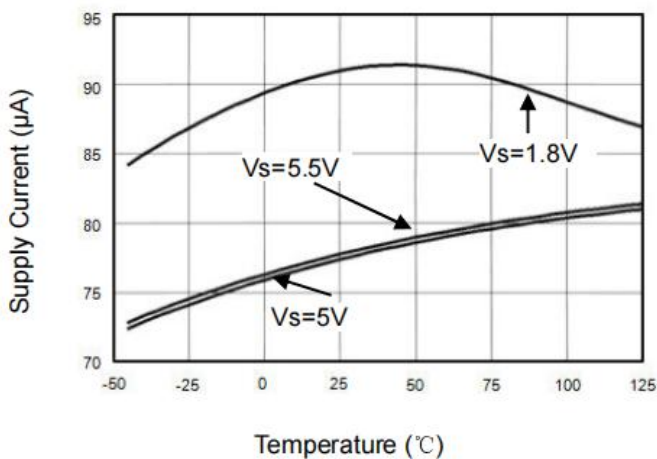
CMRR vs. Frequency



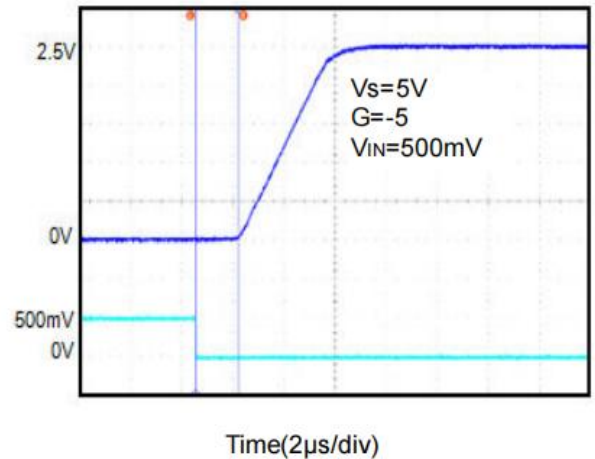
PSRR vs. Frequency



Supply Current vs. Temperature



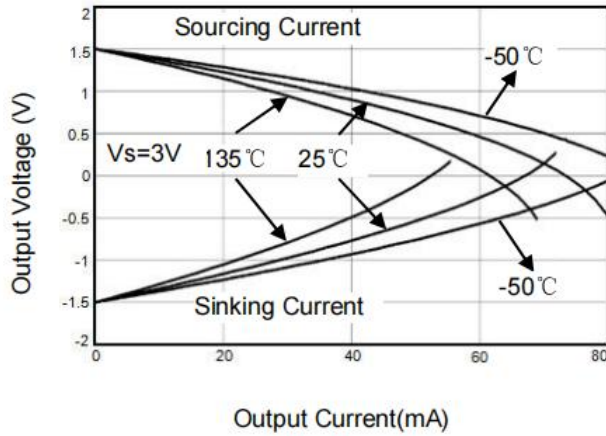
Overload Recovery Time



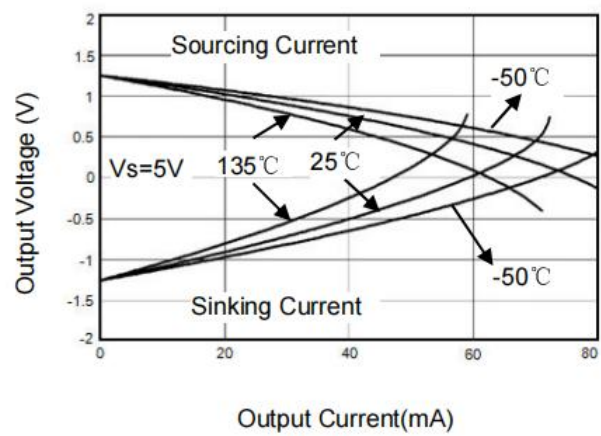
Typical Performance characteristics

At $T_A = +25^\circ\text{C}$, $R_L = 100\text{k}\Omega$ connected to $V_S/2$ and $V_{OUT} = V_S/2$, unless otherwise noted.

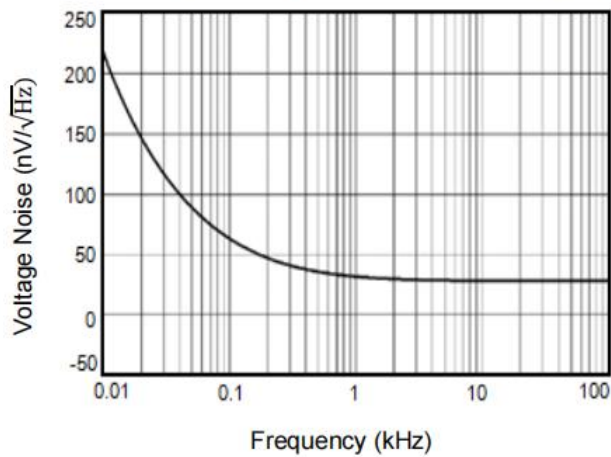
Output Voltage Swing vs. Output Current



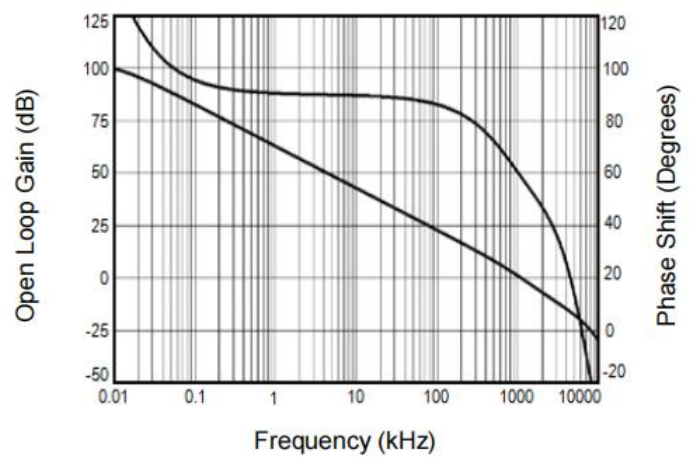
Output Voltage Swing vs. Output Current



Input Voltage Noise Spectral Density vs. Frequency



Open Loop Gain, Phase Shift vs. Frequency



Application Note

Size

600X family series op amps are unity-gain stable and suitable for a wide range of general-purpose applications. The small footprints of the 600X family packages save space on printed circuit boards and enable the design of smaller electronic products.

Power Supply Bypassing and Board Layout

600X family series operates from a single 1.8V to 6V supply or dual $\pm 0.9V$ to $\pm 3V$ supplies. For best performance, a 0.1F ceramic capacitor should be placed close to the V_{DD} pin in single supply operation. For dual supply operation, both V_{DD} and V_{SS} supplies should be bypassed to ground with separate 0.1 μ F ceramic capacitors.

Low Supply Current

The low supply current (typical 75 μ A per channel) of 600X family will help to maximize battery life. They are ideal for battery powered systems.

Operating Voltage

600X family operates under wide input supply voltage (1.8V to 6V). In addition, all temperature specifications apply from -40°C to $+125^{\circ}\text{C}$. Most behavior remains unchanged throughout the full operating voltage range. These guarantees ensure operation throughout the single Li-Ion battery lifetime.

Rail-to-Rail Input

The input common-mode range of 600X family extends 100mV beyond the supply rails ($V_{SS}-0.1V$ to $V_{DD}+0.1V$). This is achieved by using complementary input stage. For normal operation, inputs should be limited to this range.

Rail-to-Rail Output

Rail-to-Rail output swing provides maximum possible dynamic range at the output. This is particularly important when operating in low supply voltages. The output voltage of 600X family can typically swing to less than 10mV from supply rail in light resistive loads ($>100k\Omega$), and 60mV of supply rail in moderate resistive loads (10k Ω).

Capacitive Load Tolerance

The 600X family is optimized for bandwidth and speed, not for driving capacitive loads. Output capacitance will create a pole in the amplifier's feedback path, leading to excessive peaking and potential oscillation. If dealing with load capacitance is a requirement of the application, the two strategies to consider are (1) using a small resistor in series with the amplifier's output and the load capacitance and (2) reducing the bandwidth of the amplifier's feedback loop by increasing the overall noise gain.

Figure 2 shows a unity gain follower using the series resistor strategy. The resistor isolates the output from the capacitance and, more importantly, creates a zero in the feedback path that compensates for the pole created by the output capacitance.

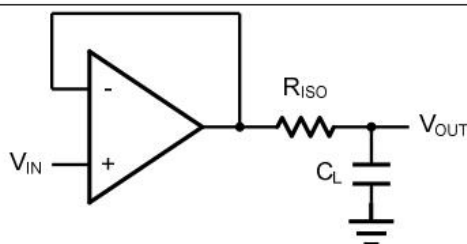


Figure 2 Indirectly Driving a Capacitive Load Using Isolation Resistor

The bigger the R_{ISO} resistor value, the more stable V_{OUT} will be. However, if there is a resistive load R_L in parallel with the capacitive load, a voltage divider (proportional to R_{ISO}/R_L) is formed, this will result in a gain error.

The circuit in Figure 3 is an improvement to the one in Figure 2. R_F provides the DC accuracy by feed-forward the V_{IN} to R_L . C_F and R_{ISO} serve to counteract the loss of phase margin by feeding the high frequency component of the output signal back to the amplifier's inverting input, thereby preserving the phase margin in the overall feedback loop. Capacitive drive can be increased by increasing the value of C_F . This in turn will slow down the pulse response.

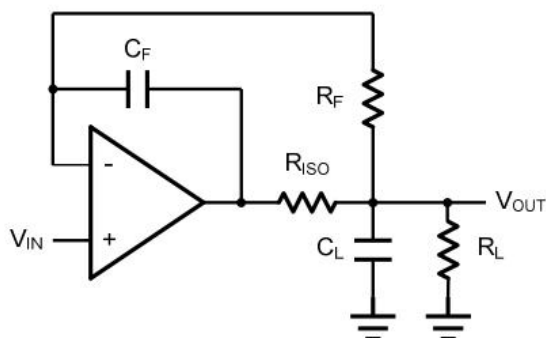


Figure 3. Indirectly Driving a Capacitive Load with DC Accuracy

Typical Application Circuits

Differential amplifier

The differential amplifier allows the subtraction of two input voltages or cancellation of a signal common to the two inputs. It is useful as a computational amplifier in making a differential to single-end conversion or in rejecting a common mode signal. Figure 4. shown the differential amplifier using 600X family

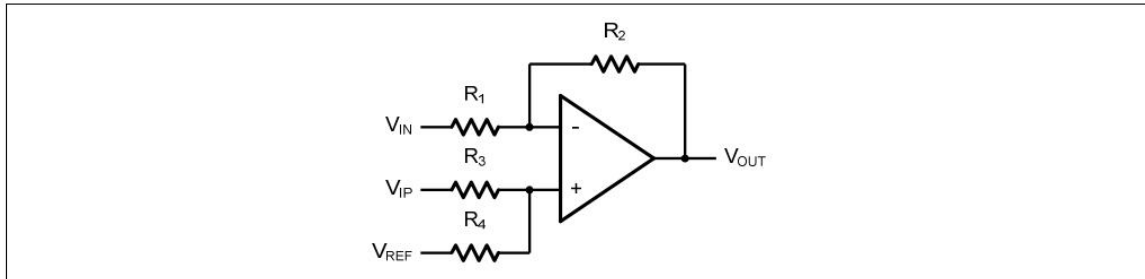


Figure 4. Differential Amplifier

$$V_{OUT} = \left(\frac{R_1 + R_2}{R_3 + R_4} \right) \frac{R_4}{R_1} V_{IN} - \frac{R_2}{R_1} V_{IP} + \left(\frac{R_1 + R_2}{R_3 + R_4} \right) \frac{R_3}{R_1} V_{REF}$$

If the resistor ratios are equal (i.e. $R_1 = R_3$ and $R_2 = R_4$), then

$$V_{OUT} = \frac{R_2}{R_1} (V_{IP} - V_{IN}) + V_{REF}$$

Low Pass Active Filter

The low pass active filter is shown in Figure 5. The DC gain is defined by $-R_2 / R_1$. The filter has a -20dB/decade roll-off after its corner frequency $f_c = 1/(2\pi R_3 C_1)$.

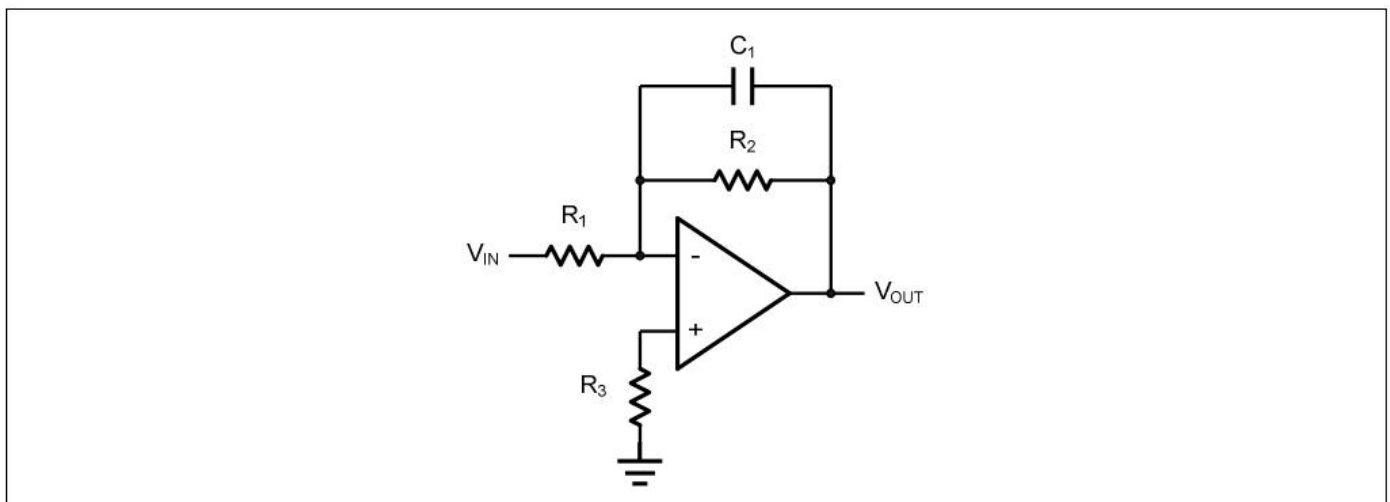


Figure 5. Low Pass Active Filter

Instrumentation Amplifier

The triple 600X family can be used to build a three op-amp instrumentation amplifier as shown in Figure 6. The amplifier in Figure 6 is a high input impedance differential amplifier with gain of R_2 / R_1 . The two differential voltage followers assure the high input impedance of the amplifier.

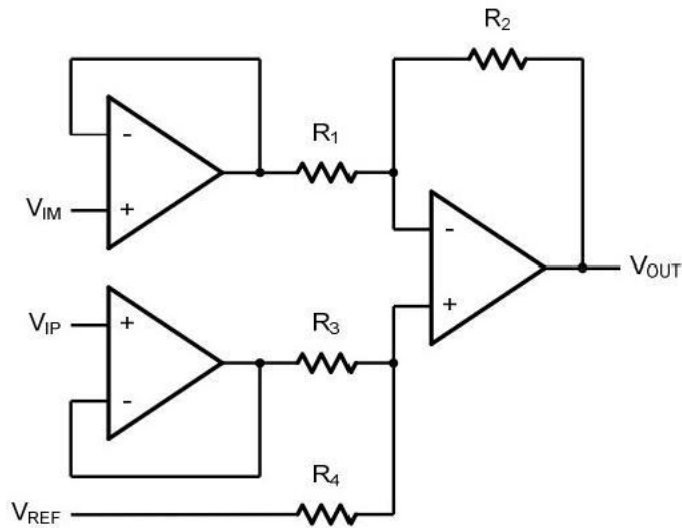
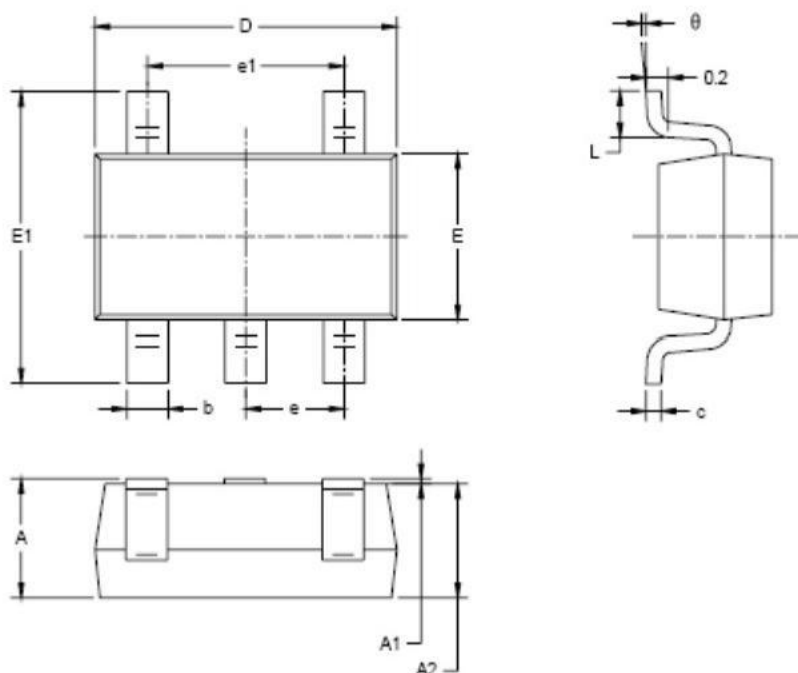


Figure 6. Instrument Amplifier

SOT23-5



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MIN	MAX	MIN	MAX
A	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
c	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E	1.500	1.700	0.059	0.087
E1	2.850	2.950	0.104	0.116
e	0.950 BSC		0.037 BSC	
e1	1.900 BSC		D.075BSC	
L	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°

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