

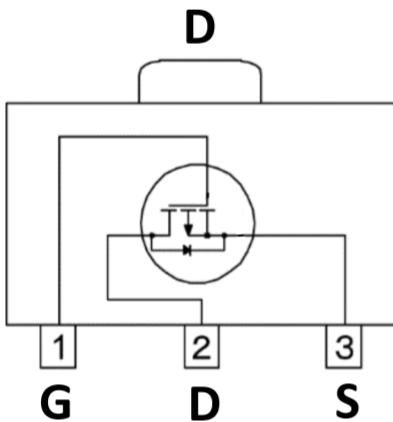
## Product Summary

- $V_{DS}$  -30 V
- $I_{DS}$  -7.0A
- $R_{DS\ (ON)}$  (@  $V_{GS}=-10V$ ) <40mΩ

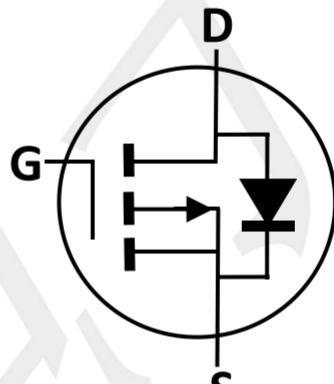
## Application

- PWM applications
- Load switch
- Power management

## Package and Pin Configuration



## Circuit diagram



Equivalent Circuit

## Absolute Maximum Ratings ( $T_A=25^\circ C$ unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNIT
Drain-Source Voltage	$V_{DS}$	-30	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Continuous Drain Current $T_c=25^\circ C$	$I_D$	-7	A
Pulsed Drain Current	$I_{DM}$	-21	A
Total Power Dissipation $T_c=25^\circ C$	$P_{DTOT}$	1.5	W
Operating Junction Temperature Range	$T_J$	-55 to +150	°C
Storage Temperature Range	$T_{stg}$	-55 to +150	°C

Note : When mounted on 1" square PCB (FR4 material).

## Thermal Characteristic

PARAMETER	Symbol	Value	Unit
Junction-to-Ambient Thermal Resistance (Note)	$R_{thJA}$	83	°C/W

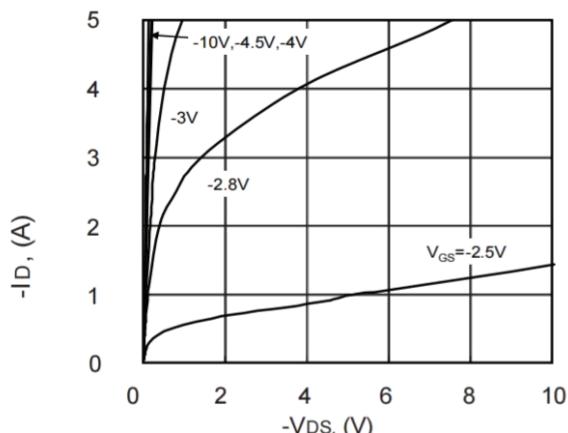
**Electrical Characteristics (T<sub>A</sub>=25°C unless otherwise noted)**

PARAMETER	CONDITIONS	SYMBOL	MIN	TYP	MAX	UNIT
<b>Static</b>						
Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =-250uA	BV <sub>DSS</sub>	-30	--	--	V
Gate-Source Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> = -250uA	V <sub>GS(th)</sub>	-1.0	-1.5	-2.5	V
Gate-Source Leakage	V <sub>DS</sub> =0V, V <sub>GS</sub> = ±20V	I <sub>GSS</sub>	--	--	±100	nA
Zero Gate Voltage Drain Current	V <sub>DS</sub> = -24V, V <sub>GS</sub> =0V	I <sub>DSS</sub>	--	--	-1	μA
Drain-Source On-State Resistance (Note 1)	V <sub>GS</sub> = -10V, I <sub>D</sub> = -7A	R <sub>DS(on)</sub>	--	27	35	mΩ
	V <sub>GS</sub> = -4.5V, I <sub>D</sub> = -4A		--	37	40	
Forward Transconductance (Note 2)	V <sub>DS</sub> = -5V, I <sub>D</sub> = -4.6A	g <sub>fs</sub>	--	10	--	S
<b>Dynamic</b> (Note 2)						
Total Gate Charge (Note 3)	V <sub>DS</sub> = -15V, I <sub>D</sub> = -5A, V <sub>GS</sub> = -5V	Q <sub>g</sub>	--	13	--	nC
Gate-Source Charge (Note 3)		Q <sub>gs</sub>	--	3.5	--	
Gate-Drain Charge (Note 3)		Q <sub>gd</sub>	--	4.5	--	
Input Capacitance	V <sub>DS</sub> = -55V, V <sub>GS</sub> = 0V, F= 1.0MHz	C <sub>iss</sub>	--	950	--	pF
Output Capacitance		C <sub>oss</sub>	--	100	--	
Reverse Transfer Capacitance		C <sub>rss</sub>	--	95	--	
<b>Switching</b>						
Turn-On Delay Time (Note 3)	V <sub>DD</sub> = -15V, R <sub>L</sub> = 6Ω, V <sub>GS</sub> = -10V, R <sub>GEN</sub> = 10Ω	t <sub>d(on)</sub>	--	10	--	nS
Rise Time (Note 3)		t <sub>r</sub>	--	15	--	
Turn-Off Delay Time (Note 3)		t <sub>d(off)</sub>	--	90	--	
Fall Time (Note 3)		t <sub>f</sub>	--	50	--	
<b>Source-Drain Diode Ratings and Characteristics</b> (Note 2)						
Forward Voltage	V <sub>GS</sub> = 0V, I <sub>F</sub> = -4.6A	V <sub>SD</sub>	--	-0.8	-1.2	V
Continuous Source Current	Integral reverse diode in the MOSFET	I <sub>S</sub>	--	--	-7	A
Pulsed Current (Note 1)		I <sub>SM</sub>	--	--	-21	A

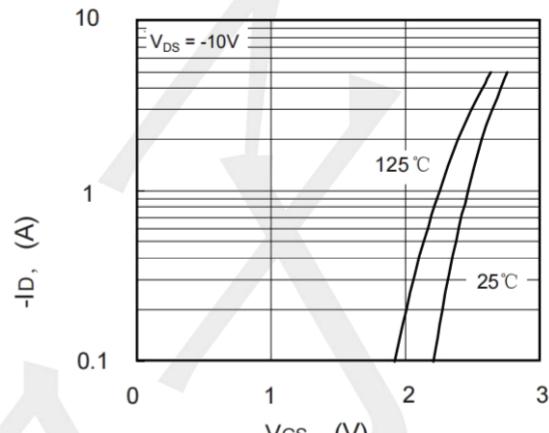
Notes:

1. Pulse test; pulse width ≤ 300 μS, duty cycle ≤ 2%.
2. Guaranteed by design, not subject to production testing.
3. Independent of operating temperature

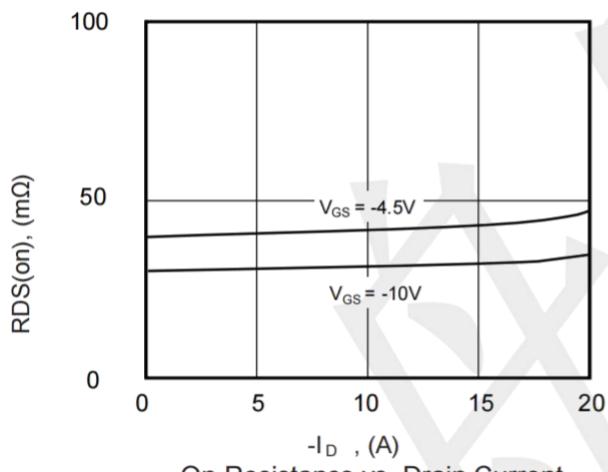
**TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)



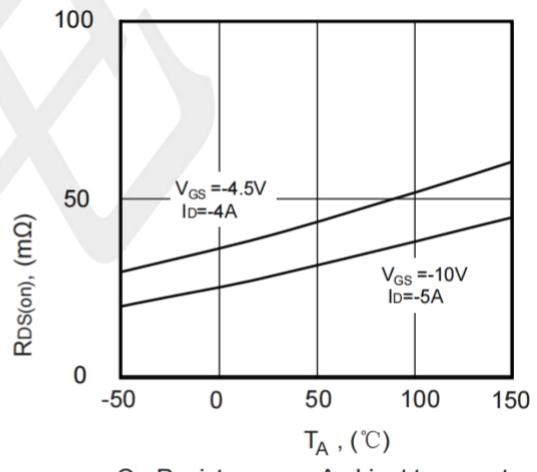
Typical Output Characteristics



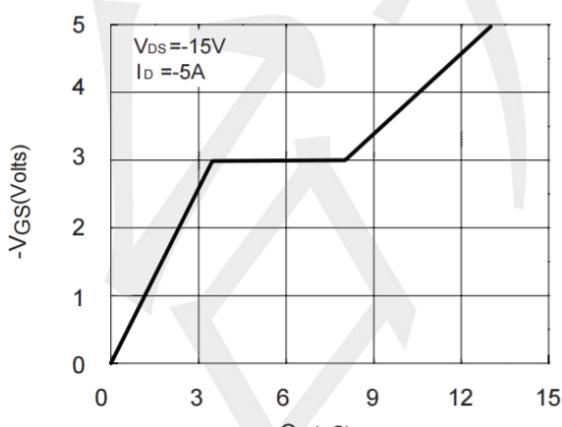
Transfer characteristics



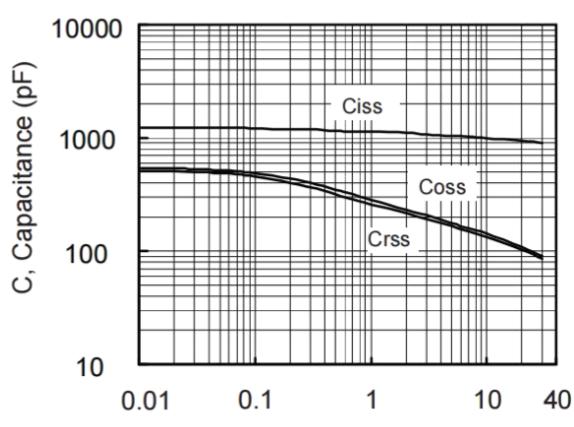
On-Resistance vs. Drain Current



On-Resistance vs. Ambient temperature



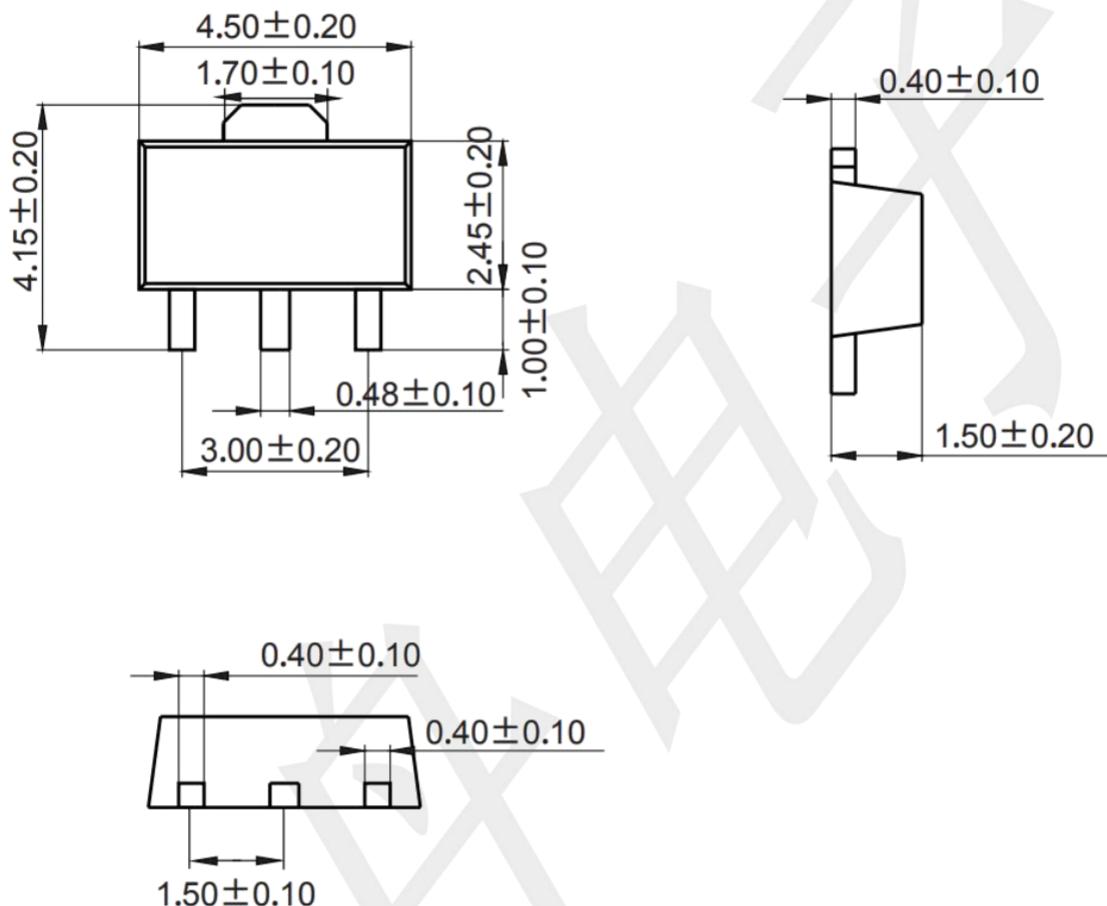
Gate Charge Characteristics



Capacitance Characteristics

**Package Information -(unit: mm)**

**SOT89-3**



**Mounting Pad Layout (unit: mm)**

