



100V/420A N-Channel Power MOSFET

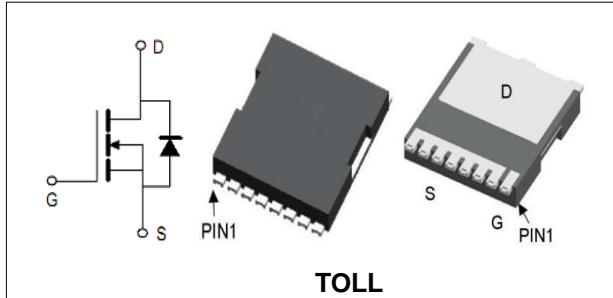
Features

- Ultra Low Gate Charge cause lower driving requirements
- 100% Avalanche Tested

V _{DS}	100	V
I _D (Package limit)	420	A
R _{DS(on),Typ} @ V _{GS} =10V	1.2	mΩ

Applications

- Synchronous Rectification for AC/DC Quick Charger
- Battery management
- UPS

**Order Information**

Product	Package	Marking	Reel Size	Reel	Carton
PGT10N015H	TOLL	PGT10N015H	13inch	1500PCS	12000PCS

Absolute Maximum Ratings

Symbol	Parameter	Rating	Unit
Common Ratings (T_c=25°C Unless Otherwise Noted)			
V _{(BR)DSS}	Drain-Source Breakdown Voltage	100	V
V _{GS}	Gate-Source Voltage	±20	V
T _J	Maximum Junction Temperature	175	°C
T _{STG}	Storage Temperature Range	-55 to 175	°C
I _S	Diode Continuous Forward Current	T _c =25°C	A
Mounted on Large Heat Sink			
E _{AS}	Single Pulse Avalanche Energy (Note1)	2450	mJ
I _{DM}	Pulse Drain Current Tested (Note2)	T _c =25°C	A
I _D	Continuous Drain current (Silicon limit)	433	A
	Continuous Drain current (Package limit)	420	
P _D	Maximum Power Dissipation	T _c =25°C	W
R _{θJC}	Thermal Resistance Junction-to-Case	0.28	°C/W
R _{θJA}	Thermal Resistance Junction-to-Ambient (Note3)	45	°C/W

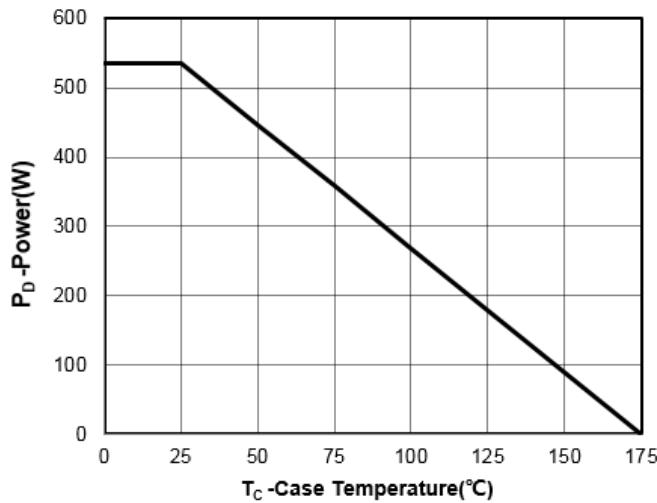
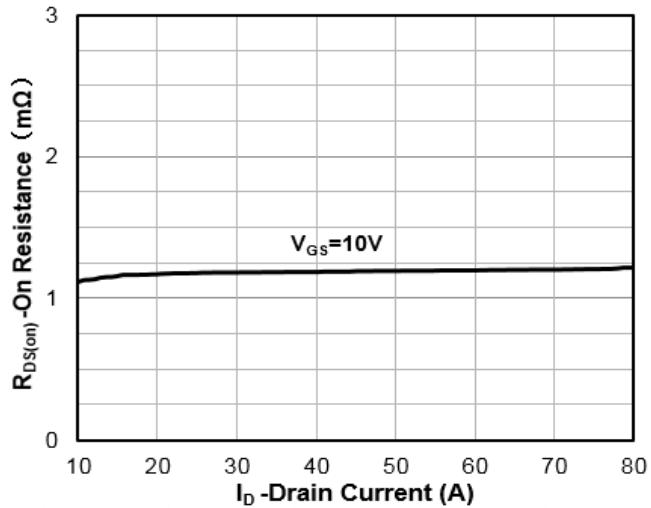
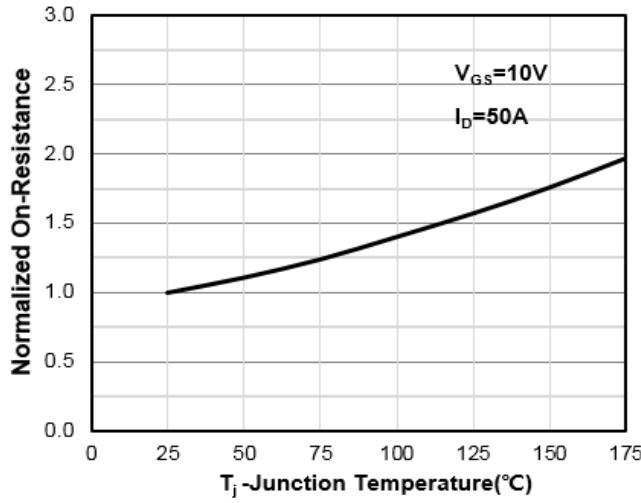
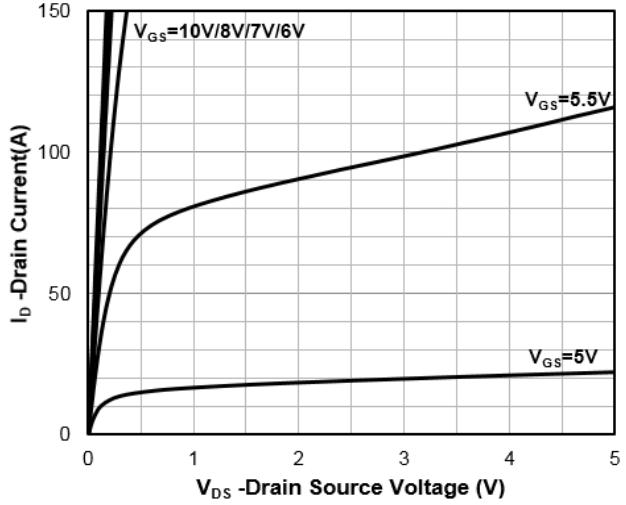
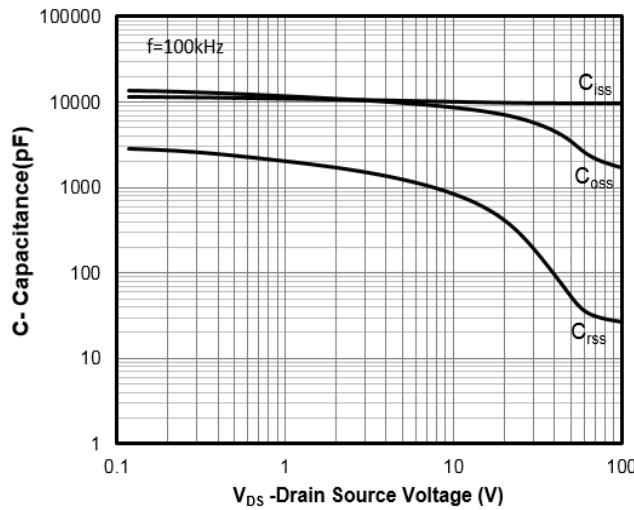
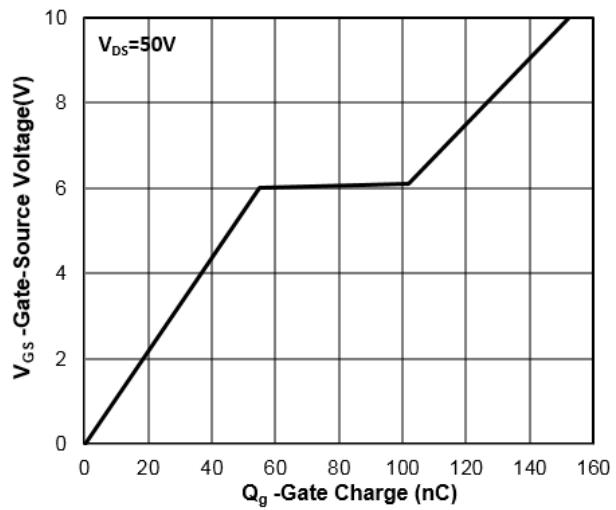


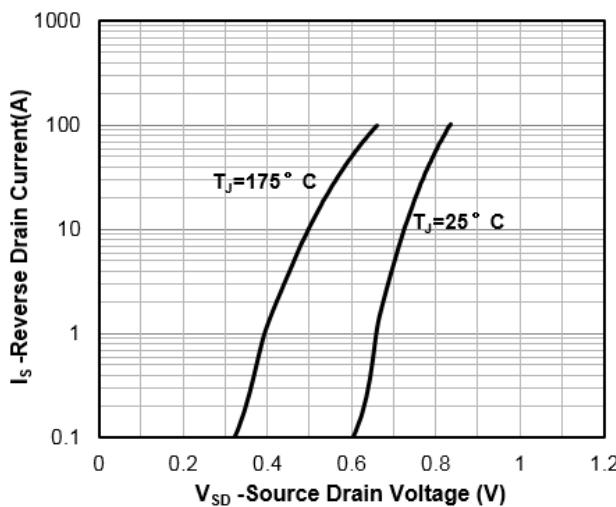
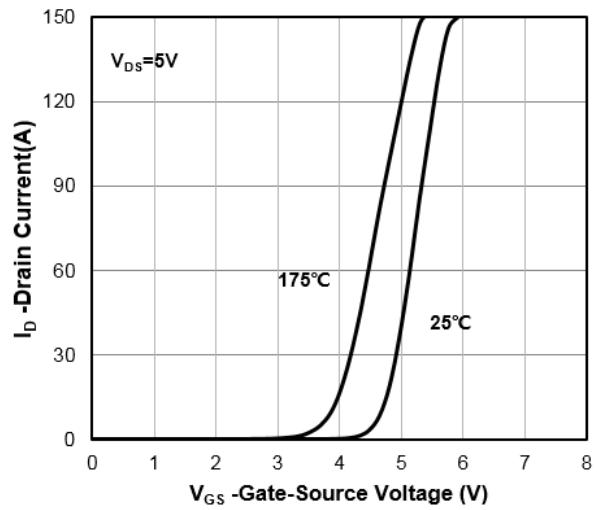
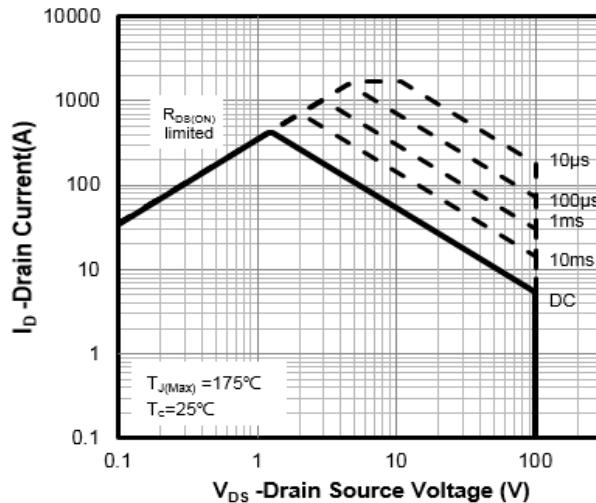
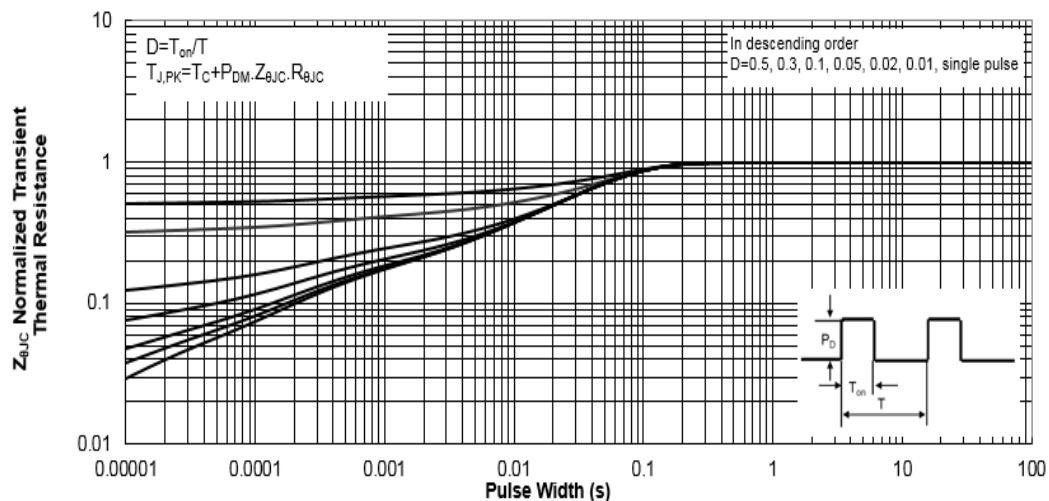
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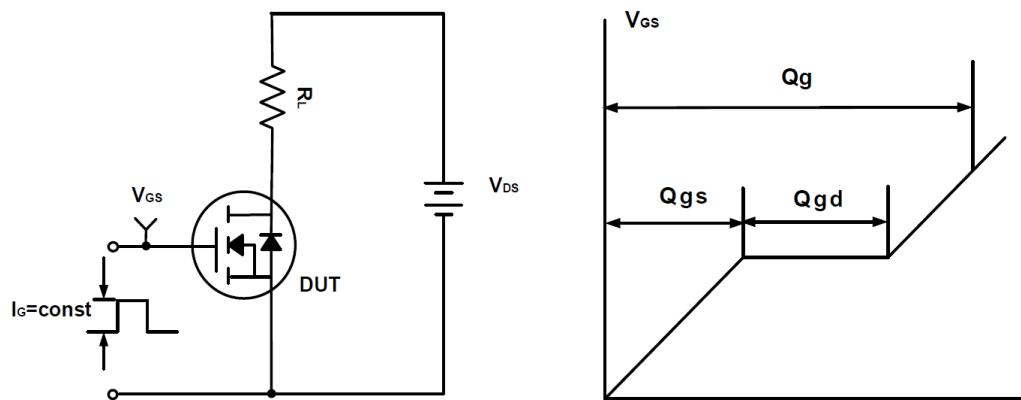
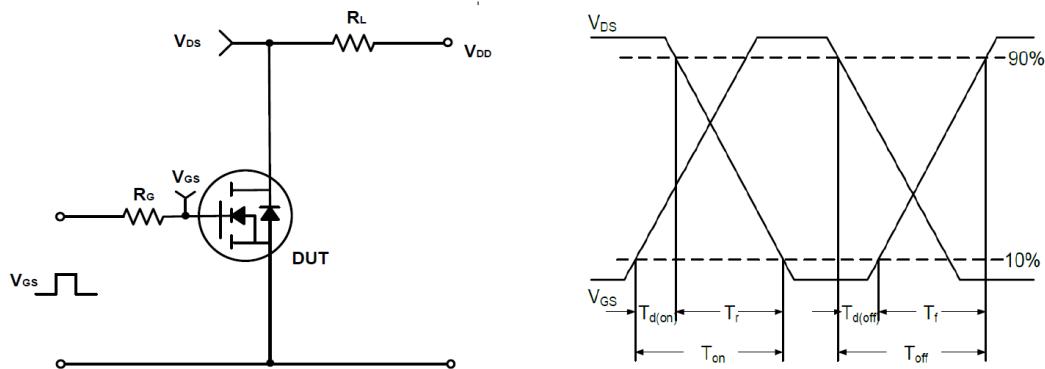
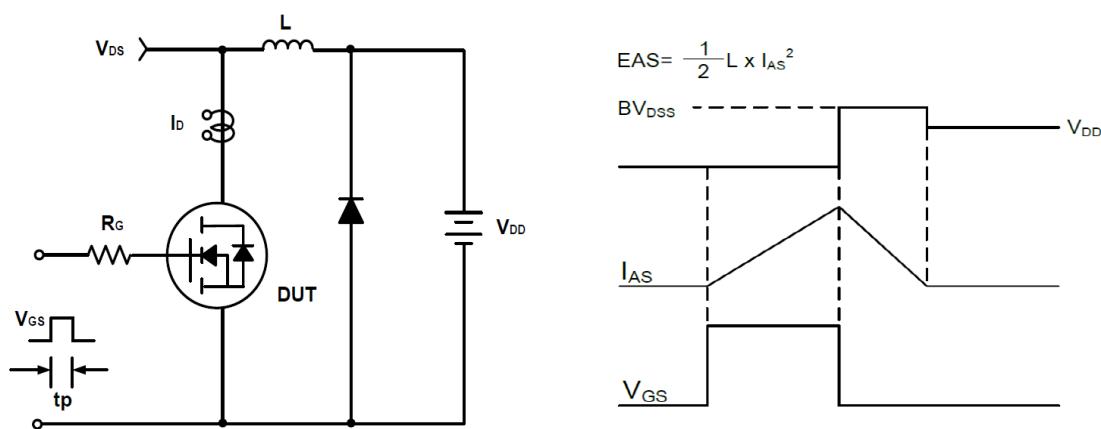
Symbol	Parameter	Condition	Min.	Typ.	Max.	Unit
Static Electrical Characteristics @ $T_j = 25^\circ\text{C}$ (unless otherwise stated)						
$V_{(\text{BR})\text{DSS}}$	Drain- Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$ $I_D=250\mu\text{A}$	100	--	--	V
I_{DSS}	Zero Gate Voltage Drain current	$V_{\text{DS}}=100\text{V}$, $V_{\text{GS}}=0\text{V}$	--	--	1	μA
I_{GSS}	Gate-Body Leakage Current	$V_{\text{GS}}=\pm 20\text{V}$, $V_{\text{DS}}=0\text{V}$	--	--	± 100	nA
$V_{\text{GS(TH)}}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}$, $I_D=250\mu\text{A}$	2	--	4	V
$R_{\text{DS(ON)}}$	Drain-Source On-State Resistance (Note4)	$V_{\text{GS}}=10\text{V}$, $I_D=50\text{A}$	--	1.2	1.45	$\text{m}\Omega$
Dynamic Electrical Characteristics @ $T_j = 25^\circ\text{C}$ (unless otherwise stated) (Note5)						
C_{iss}	Input Capacitance	$V_{\text{DS}}=50\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=100\text{kHz}$	--	9780	--	pF
C_{oss}	Output Capacitance		--	3510	--	pF
C_{rss}	Reverse Transfer Capacitance		--	52	--	pF
Q_g	Total Gate Charge	$V_{\text{DS}}=50\text{V}$, $I_D=100\text{A}$, $V_{\text{GS}}=10\text{V}$	--	152	--	nC
Q_{gs}	Gate-Source Charge		--	55	--	nC
Q_{gd}	Gate-Drain Charge		--	47	--	nC
R_G	Gate resistance	$f=1\text{MHz}$	--	1	--	Ω
Switching Characteristics (Note5)						
$t_{\text{d(on)}}$	Turn-on Delay Time	$V_{\text{DD}}=50\text{V}$, $R_G=10\Omega$, $I_D=100\text{A}$, $V_{\text{GS}}=10\text{V}$	--	88	--	ns
t_r	Turn-on Rise Time		--	226	--	ns
$t_{\text{d(off)}}$	Turn-off Delay Time		--	88	--	ns
t_f	Turn-off Fall Time		--	76	--	ns
Source- Drain Diode Characteristics @ $T_j = 25^\circ\text{C}$ (unless otherwise stated)						
V_{SD}	Forward on voltage	$I_{\text{SD}}=50\text{A}$, $V_{\text{GS}}=0\text{V}$	--	--	1.2	V
t_{rr}	Reverse Recovery Time	$I_S=100\text{A}$, $dI/dt=100\text{A}/\mu\text{s}$	--	160	--	nS
Q_{rr}	Reverse Recovery Charge		--	768	--	nC

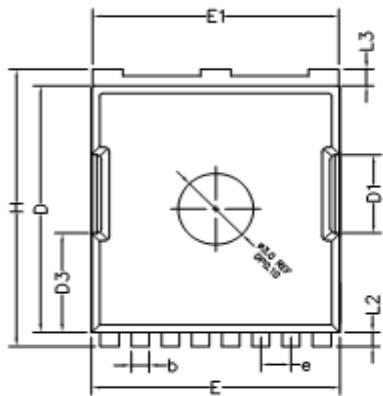
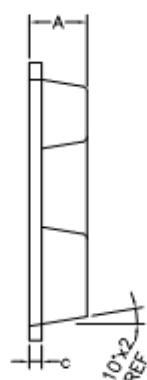
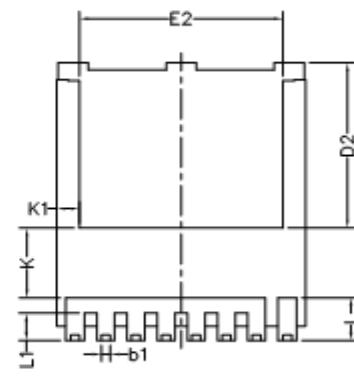
Note:

1. Limited by $T_{j\text{max}}$, starting $T_j = 25^\circ\text{C}$, $R_G = 25\Omega$, $V_{\text{DD}} = 40\text{V}$, $V_{\text{GS}} = 10\text{V}$. Part not recommended for use above this value.
2. Repetitive Rating: Pulse width limited by maximum junction temperature.
3. The value of R_{BJA} is measured with the device mounted on 1in2 FR-4 board with 2oz. Copper, in a still air environment with $T_A = 25^\circ\text{C}$
4. Pulse Test: pulse width ≤ 300 us, duty cycle $\leq 2\%$.
5. Guaranteed by design, not subject to production testing.

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Typical Characteristics

Figure1: Power De-rating

Figure2: Rdson- Drain Current

Figure3: Rdson-Junction Temperature

Figure4: Output Characteristics

Figure5: Capacitance vs VDS

Figure6: Gate Charge

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Figure7: Typical Source-Drain Diode Forward Voltage

Figure8: Transfer Characteristics

Figure9: Safe Operation Area

Figure10: Normalized Maximum Transient Thermal Impedance

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Test Circuit and Waveform:

Figure A Gate Charge Test Circuit & Waveforms

Figure B Switching Test Circuit & Waveforms

Figure C Unclamped Inductive Switching Circuit & Waveforms

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TOLL Package Outline Dimensions (Units: mm)

TOP VIEW

SIDE VIEW

BOTTEM VIEW

SIDE VIEW

COMMON DIMENSIONS (UNITS OF MEASURE IS mm)			
	MIN	NORMAL	MAX
A	2.200	2.300	2.400
b	0.600	0.700	0.900
b1	0.300	—	0.500
c	0.400	0.500	0.600
D	10.280	10.380	10.480
D1	3.200	3.300	3.400
D2	6.850	6.950	7.050
D3	4.18REF		
E	9.800	9.900	10.000
E1	9.700	9.800	9.900
E2	8.000	8.100	8.200
e	1.200BSC		
H	11.480	11.680	11.880
L	1.600	1.800	2.100
L1	1.000	1.150	1.300
L2	0.600 TYPE		
L3	0.600 TYPE		
K	2.900 TYPE		
K1	0.900 TYPE		