GOODWORK

30N20

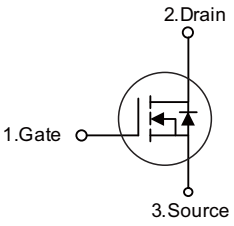
N-channel Power Planar MOSFET

Features

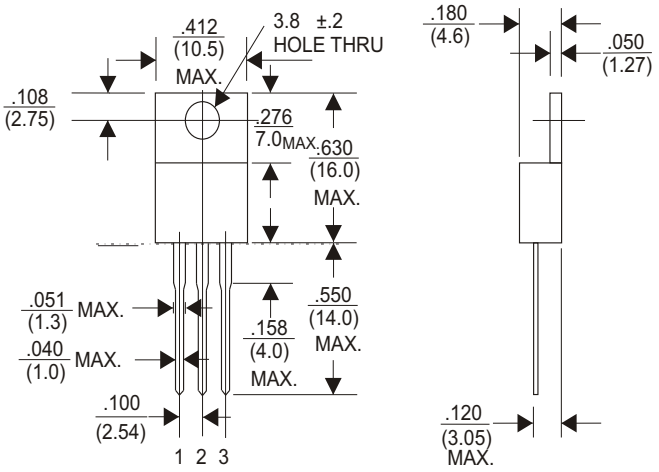
- Excellent $R_{DS(ON)}$ and Low Gate Charge
- 100% UIS Tested
- 100% ΔVds Tested
- Halogen-free; RoHS-compliant

Applications

- Load Switch
- PWM Application
- Power Management



TO-220



Dimensions in inches and (millimeters)

Absolute Maximum Ratings (@ T_C = 25°C unless otherwise specified)

Symbol	Parameter		Value	Unit
V_{DS}	Drain-to-Source Voltage		200	V
V_{GS}	Gate-to-Source Voltage		±30	V
I_D	Continuous Drain Current	T_C = 25°C	30	A
		T_C = 100°C	18	
I_{DM}	Pulsed Drain Current [1]		Refer to Fig.4	A
E_{AS}	Single Pulsed Avalanche Energy [2]		551	mJ
P_D	Power Dissipation	T_C = 25°C	208	W
		T_C = 100°C	83	
T_J, T_{STG}	Junction & Storage Temperature Range		-55 to 150	°C

Thermal Characteristics

Symbol	Parameter	Max	Unit
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient[3]	61	°C/W
$R_{\theta JC}$	Thermal Resistance, Junction to Case	0.6	

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Electrical Characteristics (T_J = 25°C unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
Off Characteristics						
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	I_D = 250μA, V_{GS} = 0V	200	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	V_{DS} =200V, V_{GS} = 0V	-	-	1.0	μA
I_{GSS}	Gate-Body Leakage Current	V_{DS} = 0V, V_{GS} = ±30V	-	-	±100	nA
On Characteristics						
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS} = V_{GS}, I_D = 250μA	2.0	3.0	4.0	V
$R_{DS(ON)}$	Static Drain-Source ON-Resistance(4)	V_{GS} = 10V, I_D =10A	-	82	120	mΩ
Dynamic Characteristics						
R_g	Gate Resistance	f = 1MHz	-	2.6	-	Ω
C_{iss}	Input Capacitance	V_{GS} = 0V, V_{DS} = 100V, f = 1MHz		1569		pF
C_{oss}	Output Capacitance			126		pF
C_{rss}	Reverse Transfer Capacitance			21		pF
Q_g	Total Gate Charge	V_{GS} = 0 to 10V V_{DS} = 100V, I_D = 28A	25	35	48	nC
Q_{gs}	Gate Source Charge		8	11	14	nC
Q_{gd}	Gate Drain("Miller") Charge		10	14	18	nC
Switching Characteristics						
$t_{d(on)}$	Turn-On DelayTime	V_{GS} = 10V, V_{DD} = 100V I_D= 28A, R_{GEN} = 24Ω	-	21	-	ns
t_r	Turn-On Rise Time		-	76	-	ns
$t_{d(off)}$	Turn-Off DelayTime		-	77	-	ns
t_f	Turn-Off Fall Time		-	60	-	ns
Body Diode Characteristics						
I_S	Maximum Continuous Body Diode Forward Current		-	-	28	A
I_{SM}	Maximum Pulsed Body Diode Forward Current		-	-	112	A
V_{SD}	Body Diode Forward Voltage	V_{GS} = 0V, I_S = 26A	-		1.2	V
trr	Body Diode Reverse Recovery Time	I_F = 28A, di/dt = 100A/us	122	171	232	ns
Qrr	Body Diode Reverse Recovery Charge		-	1123	-	nC

Notes: 1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.

2. E_{AS} condition: Starting T_J=25C, V_{DD}=50V, V_G=10V, R_G=25ohm, L=10mH, I_{AS}=10.5A, V_{DD}=0V during time in avalanche.

3. $R_{\theta JA}$ is measured with the device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square pad layout.

4. Pulse Test: Pulse Width≤300μs, Duty Cycle≤0.5%.

RATING AND CHARACTERISTIC CURVES (30N20)

Figure 1: Power De-rating

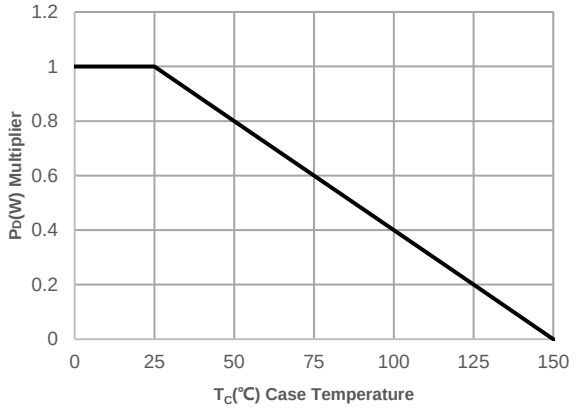


Figure 2: Current De-rating

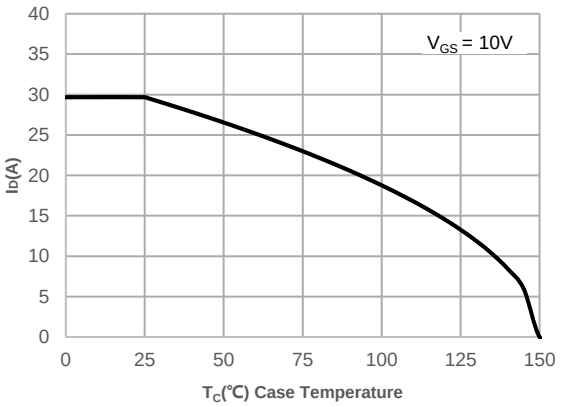


Figure 3: Normalized Maximum Transient Thermal Impedance

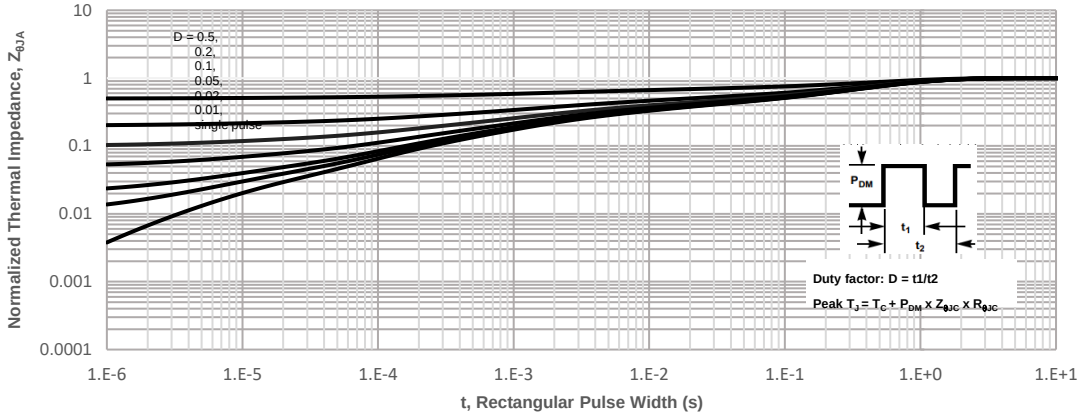
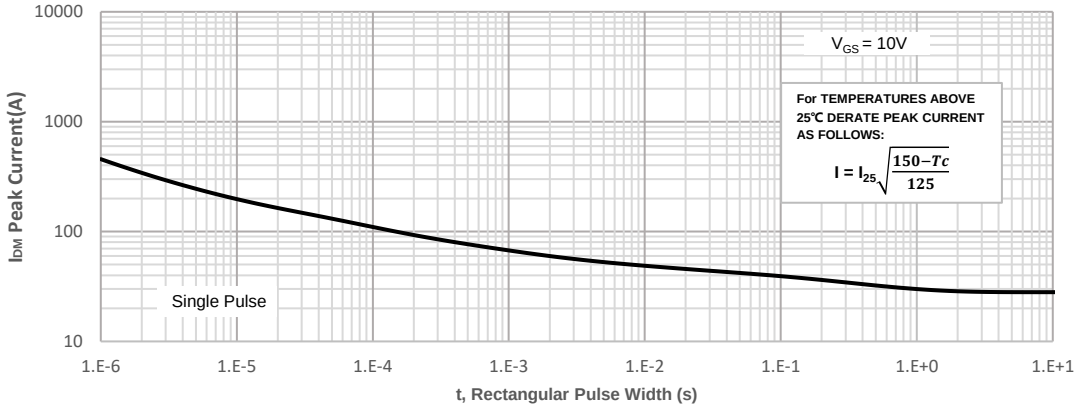


Figure 4: Peak Current Capacity



RATING AND CHARACTERISTIC CURVES (30N20)

Figure 5: Output Characteristics

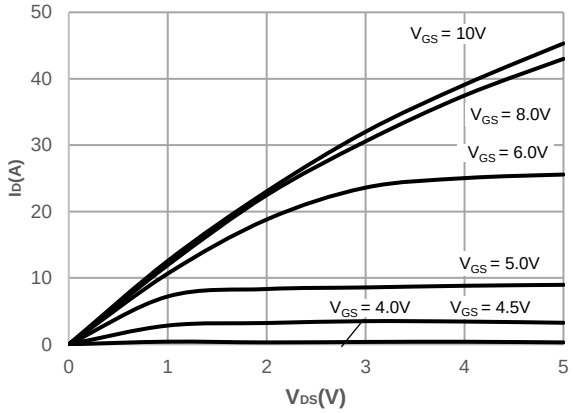


Figure 6: Typical Transfer Characteristics

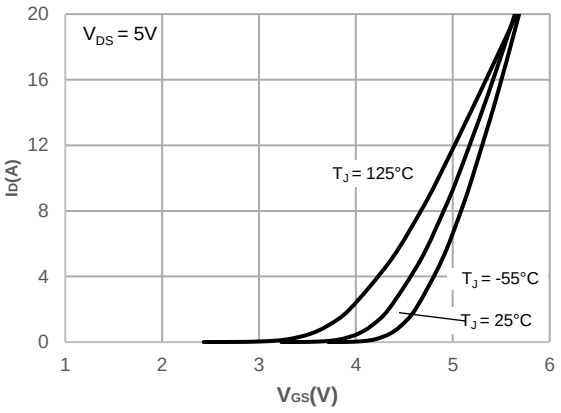


Figure 7: On-resistance vs. Drain Current

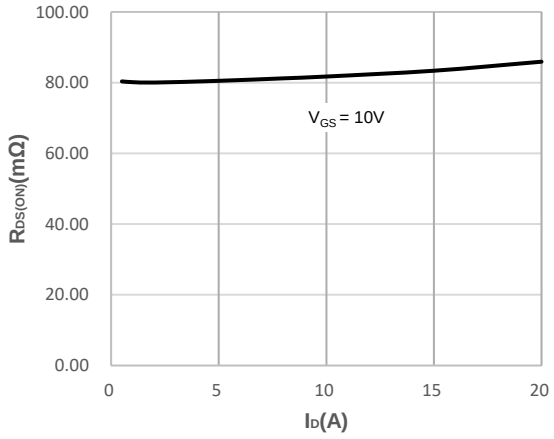


Figure 8: Body Diode Characteristics

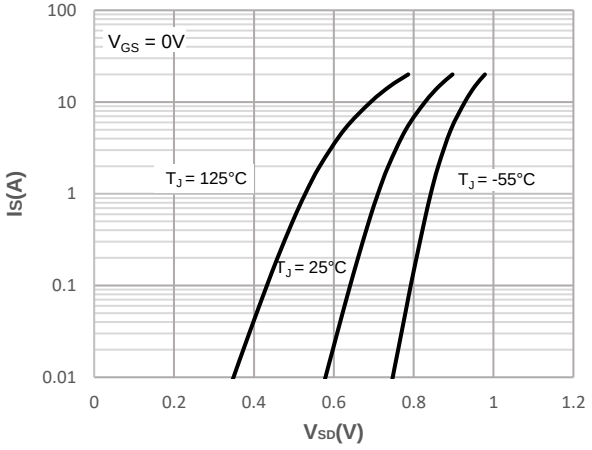


Figure 9: Gate Charge Characteristics

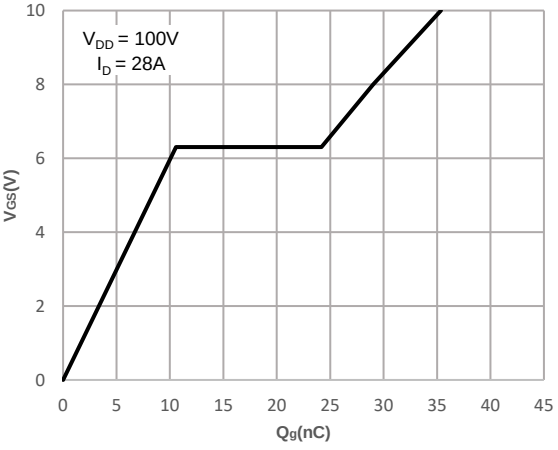
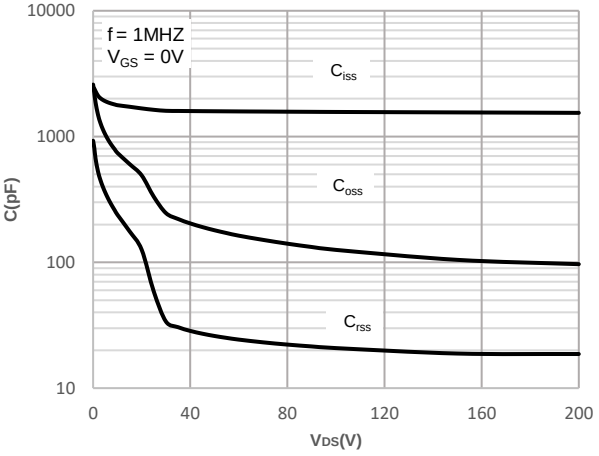


Figure 10: Capacitance Characteristics



RATING AND CHARACTERISTIC CURVES (30N20)

Figure 11: Normalized Breakdown voltage vs. Junction Temperature

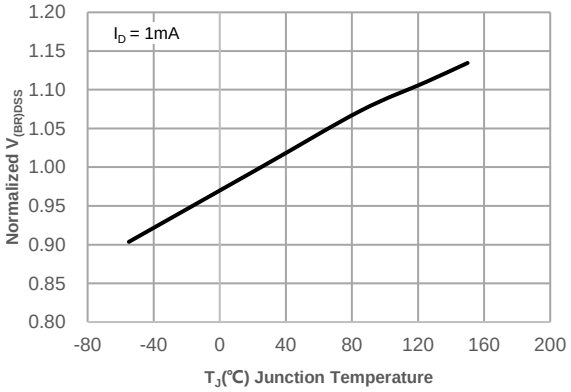


Figure 12: Normalized on Resistance vs. Junction Temperature

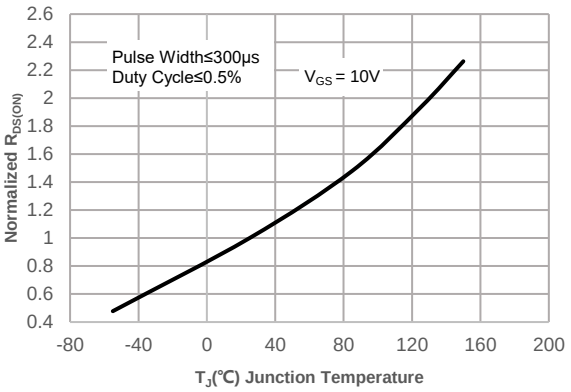


Figure 13: Normalized Threshold Voltage vs. Junction Temperature

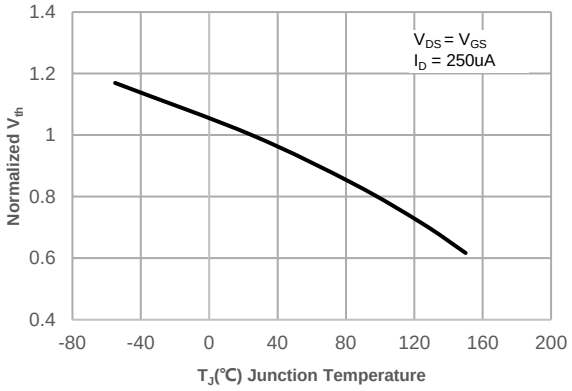


Figure 14: $R_{DS(ON)}$ vs. V_{GS}

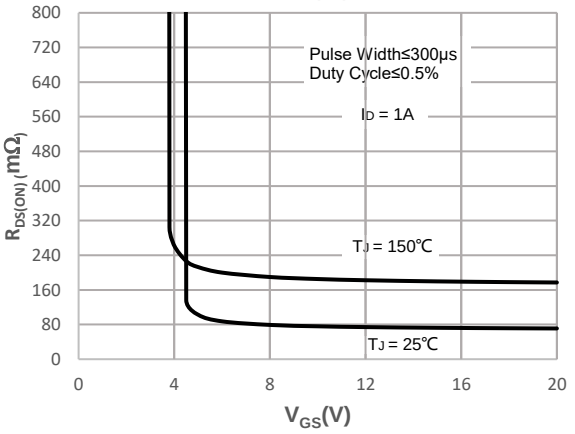
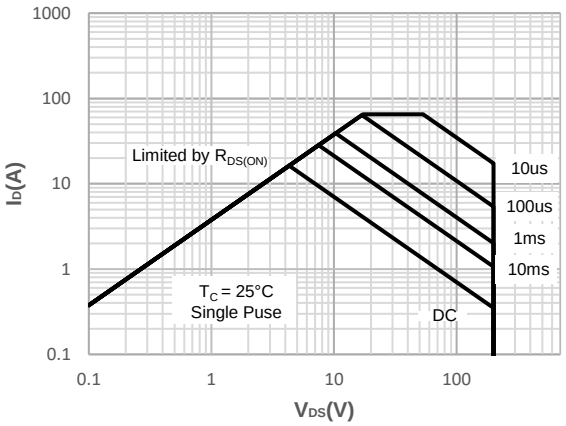


Figure 15: Maximum Safe Operating Area



Test Circuit

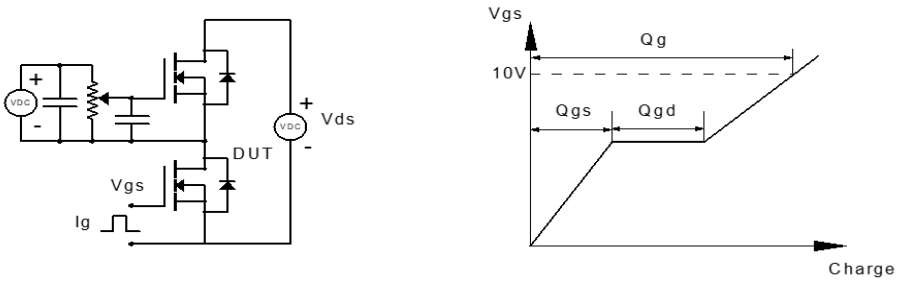


Figure 1: Gate Charge Test Circuit & Waveform

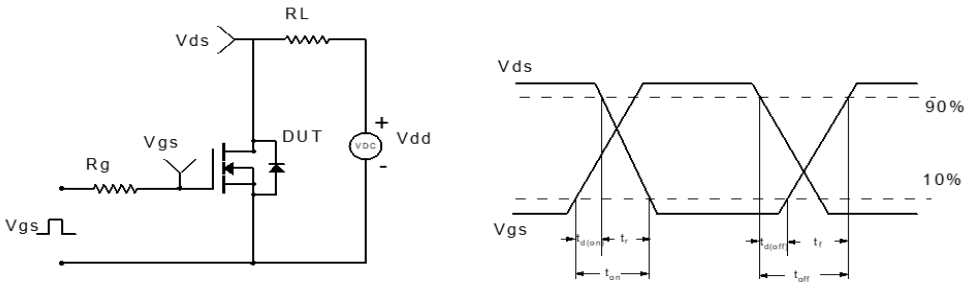


Figure 2: Resistive Switching Test Circuit & Waveform

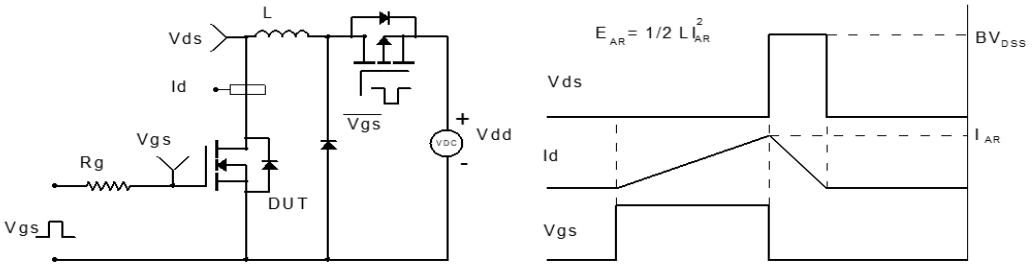


Figure 3: Unclamped Inductive Switching Test Circuit& Waveform

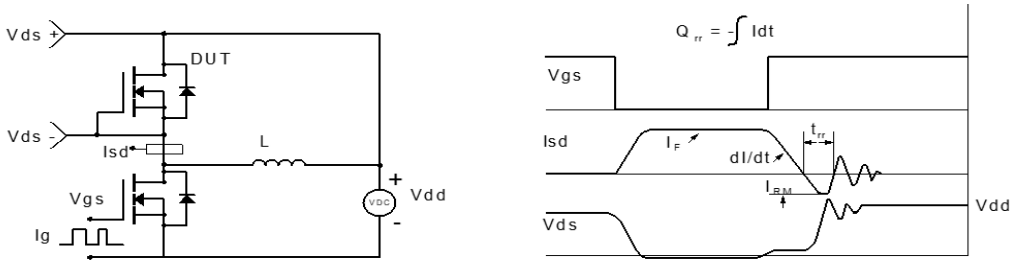


Figure 4: Diode Recovery Test Circuit & Waveform