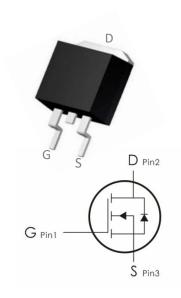


Description:

This N-Channel MOSFET uses advanced trench technology and design to provide excellent $R_{DS(on)}$ with low gate charge. It can be used in a wide variety of applications.

Features:

- 1) $V_{DS}=30V,I_{D}=120A,R_{DS(ON)}<3.5m \Omega @V_{GS}=10V (Typ: 2.9m)$
- 2) Low gate charge.
- 3) Green device available.
- 4) Advanced high cell denity trench technology for ultra low R_{DS(ON)}.
- 5) Excellent package for good heat dissipation.



Package Marking and Ordering Information:

Part NO.	Marking	Package	Packing
DOB120N03	120N03	TO- 263	800 pcs/Reel

Absolute Maximum Ratings: (T_c=25℃ unless otherwise noted)

Symbol	Parameter	Ratings	Units
V _{DS}	Drain-Source Voltage	30	V
V _{GS}	Gate-Source Voltage	±20	٧
	Continuous Drain Current-T _C =25°C	120	
I _D	Continuous Drain Current-T _C =100℃	89	Α
	Pulsed Drain Current ¹	560	
E _{AS}	Single Pulse Avalanche Energy ²	125	mJ
P _D	Power Dissipation ($T_c=25^{\circ}C$)	125	W
T _J , T _{STG}	Operating and Storage Junction Temperature Range	-55 to +150	$^{\circ}$ C

Thermal Characteristics:

Symbol	Parameter	Max	Units
R _{eJC}	Thermal Resistance, Junction to Case	1	
R _{OJA}	Thermal Resistance, Junction to Ambient	62	°C/W



Electrical Characteristics: (T_c=25 ℃ unless otherwise noted)

Symbol	Parameter	Conditions	Min	Тур	Max	Units
Off Characteristics	Off Characteristics					
BV _{DSS}	Drain-Sourtce Breakdown Voltage	V _{GS} =0V,I _D =250 µ A	30			V
I _{DSS}	Zero Gate Voltage Drain Current	V _{GS} =0V, V _{DS} =30V			1	μ Α
I _{GSS}	Gate-Source Leakage Current	V_{GS} = \pm 20V, V_{DS} =0A			±100	nA
On Characteristics						
V _{GS(th)}	Gate-Source Threshold Voltage	V _{GS} =V _{DS} , I _D =250 μ A	1	1.6	2.2	V
		V _{GS} =10V,I _D =30A		2.9	3.5	m $Ω$
R _{DS(ON)}	Drain-Source On Resistance ³	V _{GS} =4.5V,I _D =20A		4.6	6	m $Ω$
Dynamic Characterist	ics					
C _{iss}	Input Capacitance			2756		
C _{oss}	Output Capacitance	V _{DS} =15V, V _{GS} =0V, f=1MHz		353		pF
C _{rss}	Reverse Transfer Capacitance			327		
Switching Characteris	tics					
t _{d(on)}	Turn-On Delay Time			11.5		ns
t _r	Rise Time	V _{DD} =15V, I _D =30A,		30.4		ns
t _{d(off)}	Turn-Off Delay Time	R_{ENG} =3 Ω , V_{GS} =10 V		49		ns
t _f	Fall Time			19		ns
\mathbf{Q}_{g}	Total Gate Charge			60.9		nc
\mathbf{Q}_{gs}	Gate-Source Charge	V _{GS} =10V, V _{DS} =15V,		12.6		nc
\mathbf{Q}_{gd}	Gate-Drain "Miller" Charge	I _D =30A		13.6		nc
Drain-Source Diode Characteristics						
V _{SD}	Diode Forward Voltage	V _{GS} =0V, I _{SD} =30A			1.2	V
Is	Continuous Drain Curren				120	А
I _{SM}	Pulsed Drain Current	$V_D=V_G=0V$			560	А
Trr	Reverse Recovery Time	I _F =30A,		16		ns
Qrr	Reverse Recovery Charge	dI/dt=100A/us		7		nc



Notes:

- 1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.
- 2. E_{AS} condition: Starting T_J =25C, V_{DD} =30V, V_G =10V, R_G =25ohm, L=0.5mH, I_{AS} =28A
- 3. Pulse Test: Pulse Width≤300µs, Duty Cycle≤0.5%.

Test Circuit

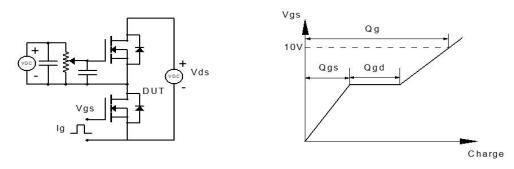


Figure 1: Gate Charge Test Circuit & Waveform

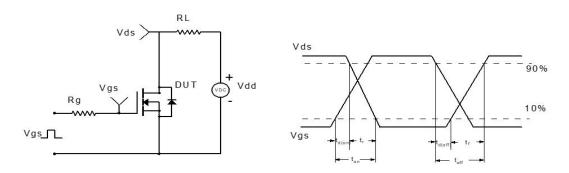


Figure 2: Resistive Switching Test Circuit & Waveform

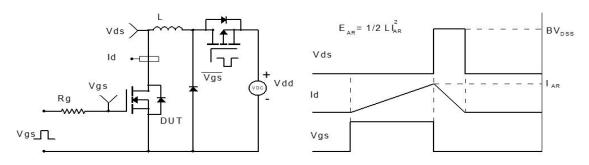


Figure 3: Unclamped Inductive Switching Test Circuit& Waveform

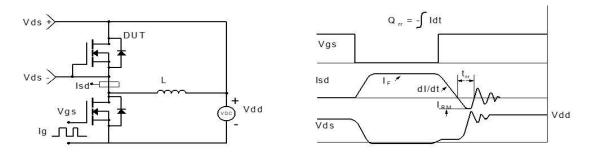
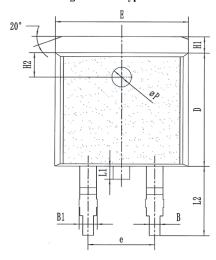


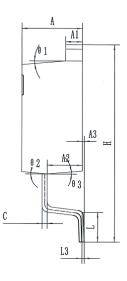
Figure 4: Diode Recovery Test Circuit & Waveform



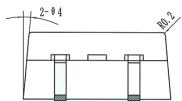
TO-263 Package Information: Unit:mm

Package Outline Type-A

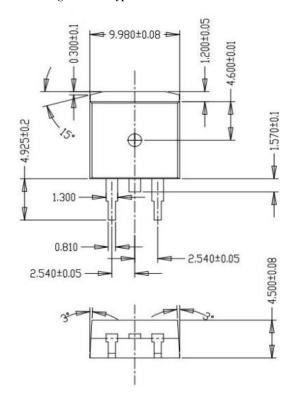


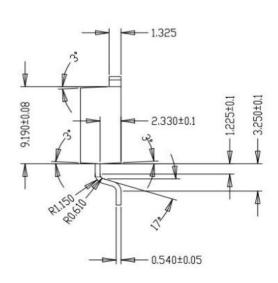


COMMON DIMENSIONS			
SYMBOL	MM		
	MIN	NOM	MAX
A	4.50	4.60	4.70
A1	1.22	1.27	1.32
A2	2.57	2.67	2.77
A3	0.00		0.15
В	0.76	0.81	0.87
B1	1.32	1.37	1.42
C	0.33	0.38	0.43
D	8. 55	8.65	8.75
е	5. 08 BSC		
Е	10.06	10.16	10.26
Н	14.80	15.00	15. 20
H1	1.17	1.27	1.37
H2	1.85 REF		
L	2.09	2.39	2.69
L1	0.80	1.00	1.20
L2	4.88	5.08	5. 28
L3	0. 25 REF		
φP	1.40	1.50	1.60
θ1	3°	5°	7°
θ2	3°	5°	7°
θ3	3°	5°	7°
θ4	3°	5°	7°



Package Outline Type-B







Marking Information:

①. Doingter LOGO

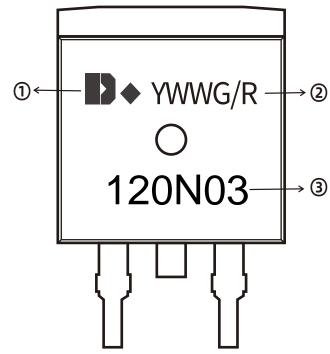
②. Date Code(YWWG / R)

Y: Year Code, last digit of the year

WW: Week Code(01-53)

G/R: G(Green) /R(Lead Free)

③. Part NO.



Previous Version

Version	Date	Subjects (major changes since last revision)
2.0	2024-07-27	Release of final version

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