

Description:

This N+P Channel MOSFET uses advanced trench technology and design to provide excellent $R_{DS(on)}$ with low gate charge.

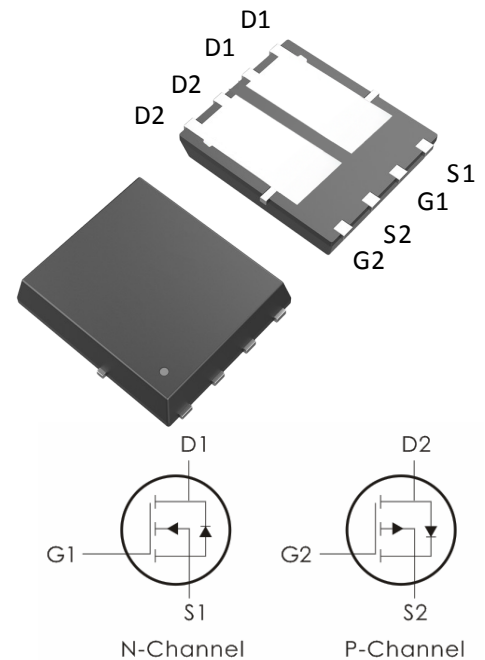
It can be used in a wide variety of applications.

Features:

N-Channel: $V_{DS}=30V, I_D=42A, R_{DS(on)} < 8m\Omega @ V_{GS}=10V$ (Typ: $6.5m\Omega$)

P-Channel: $V_{DS}=-30V, I_D=-35A, R_{DS(on)} < 12m\Omega @ V_{GS}=-10V$ (Typ: $9m\Omega$)

- 1) Low gate charge.
- 2) Green device available.
- 3) Advanced high cell density trench technology for ultra low $R_{DS(on)}$.
- 4) Excellent package for good heat dissipation.
- 5) MSL3



Package Marking and Ordering Information:

Part NO.	Marking	Package	Packing
DON627	N627	DFN5*6-8D	5000 pcs/Reel

Absolute Maximum Ratings: ($T_C=25^\circ C$ unless otherwise noted)

Symbol	Parameter	N-Channel	P-Channel	Units
V_{DS}	Drain-Source Voltage	30	-30	V
V_{GS}	Gate-Source Voltage	± 20	± 20	V
I_D	Continuous Drain Current- $T_C=25^\circ C^1$	42	-35	A
	Continuous Drain Current- $T_C=100^\circ C^1$	29	-24.5	
I_{DM}	Pulsed Drain Current ²	168	-140	A
E_{AS}	Single pulse avalanche energy ³	49	68	mJ
P_D	Power Dissipation - $T_C=25^\circ C$	24	40	W
T_J, T_{STG}	Operating and Storage Junction Temperature Range	-55 to +150		$^\circ C$

Thermal Characteristics:

Symbol	Parameter	N-CH	P-CH	Units
$R_{\theta JC}$	Thermal Resistance, Junction to Cast	5.2	3.1	$^\circ C/W$
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	40	40	$^\circ C/W$

N-Channel Electrical Characteristics: ($T_C=25^{\circ}\text{C}$ unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
Off Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250 μ A	30	---	---	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{GS} =0V, V _{DS} =30V	---	---	1	μ A
I _{GSS}	Gate-Source Leakage Current	V _{GS} =± 20V, V _{DS} =0A	---	---	± 100	nA
On Characteristics						
V _{GS(th)}	Gate-Source Threshold Voltage	V _{GS} =V _{DS} , I _D =250 μ A	1	1.36	2.5	V
R _{DS(on)}	Drain-Source On Resistance ⁴	V _{GS} =10V, I _D =20A	---	6.5	8	m Ω
		V _{GS} =4.5V, I _D =10A	---	9	12	m Ω
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{DS} =15V, V _{GS} =0V, f=1MHz	---	920	---	pF
C _{oss}	Output Capacitance		---	189	---	
C _{rss}	Reverse Transfer Capacitance		---	142	---	
Switching Characteristics						
t _{d(on)}	Turn-On Delay Time	V _{DS} =15V, I _D =15A, R _{ENG} =3 Ω ,V _{GS} =10V	---	7	---	ns
t _r	Rise Time		---	15	---	ns
t _{d(off)}	Turn-Off Delay Time		---	26	---	ns
t _f	Fall Time		---	6	---	ns
Q _g	Total Gate Charge	V _{GS} =10V, V _{DS} =15V, I _D =20A	---	22	---	nC
Q _{gs}	Gate-Source Charge		---	4.5	---	nC
Q _{gd}	Gate-Drain “Miller” Charge		---	5.5	---	nC
Drain-Source Diode Characteristics						
V _{SD}	Diode Forward Voltage	V _{GS} =0V, I _{SD} =15A	---	---	1.2	V
I _S	Continuous Drain Curren	V _D =V _G =0V	---	---	42	A
I _{SM}	Pulsed Drain Current		---	---	168	A
T _{rr}	Reverse Recovery Time	I _F =20A, T _J =25 °C	---	10	---	ns
Q _{rr}	Reverse Recovery Charge	dI/dt=100A/us	---	3	---	nC

Notes:

1. Computed continuous current assumes the condition of $T_{j,Max}$ while the actual continuous current depends on the thermal & electro-mechanical application board design
2. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature
3. EAS condition : $T_J=25^{\circ}C$, $V_{DD}=15V$, $V_G=10V$, $L=0.5mH$
4. Pulse Test: Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 0.5\%$

Test Circuit

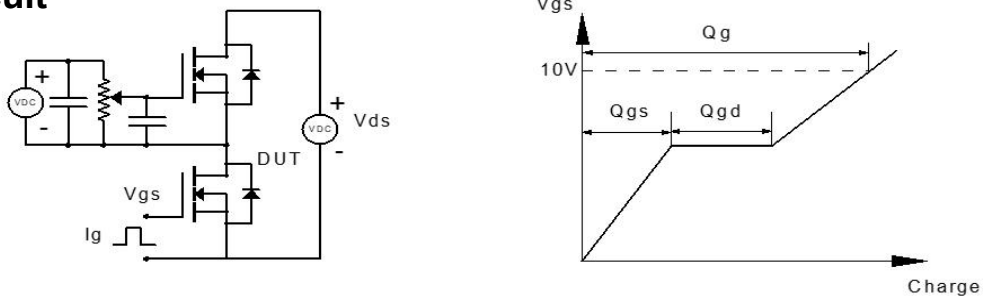


Figure 1: Gate Charge Test Circuit & Waveform

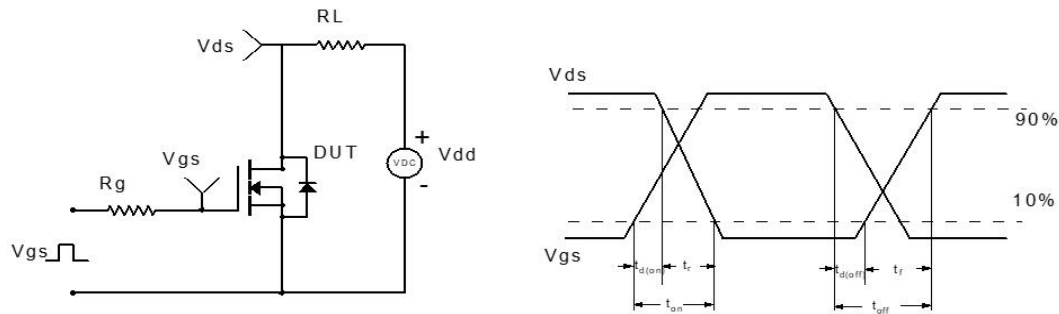


Figure 2: Resistive Switching Test Circuit & Waveform

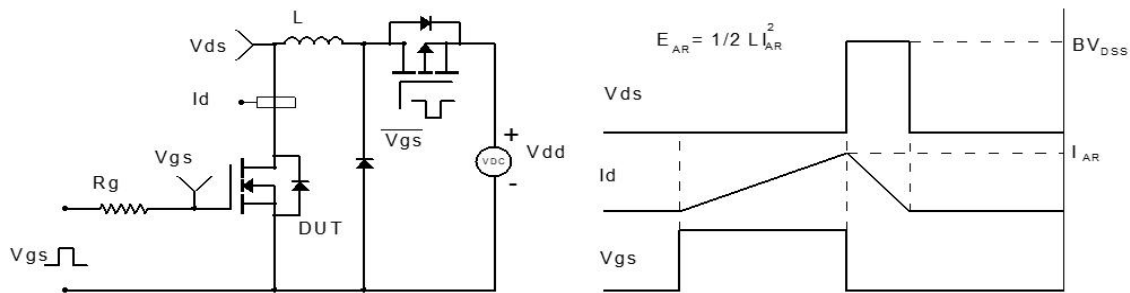


Figure 3: Unclamped Inductive Switching Test Circuit & Waveform

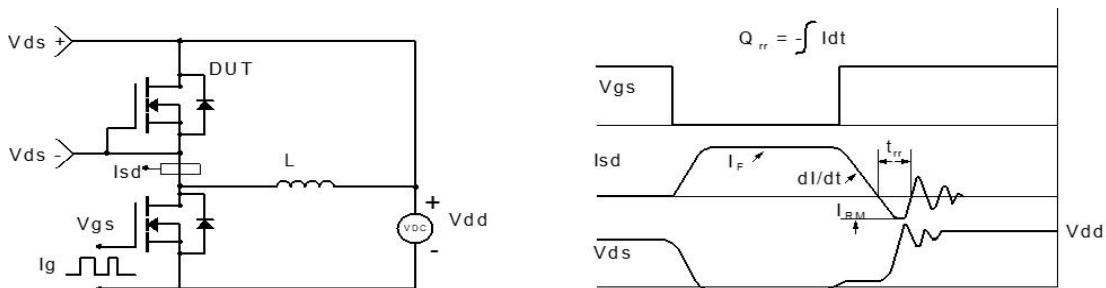


Figure 4: Diode Recovery Test Circuit & Waveform

P-Channel Electrical Characteristics: ($T_C=25^{\circ}\text{C}$ unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
Off Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250 μ A	-30	---	---	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{GS} =0V, V _{DS} =-30V	---	---	-1	μ A
I _{GSS}	Gate-Source Leakage Current	V _{GS} = ± 20V, V _{DS} =0A	---	---	± 100	nA
On Characteristics						
V _{GS(th)}	Gate-Source Threshold Voltage	V _{GS} =V _{DS} , I _D =250 μ A	-1	-1.6	-2.5	V
R _{DS(ON)}	Drain-Source On Resistance ⁴	V _{GS} =-10V, I _D =-10A	---	9	12	m Ω
		V _{GS} =-4.5V, I _D =-5A	---	12	15	
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{DS} =15V, V _{GS} =0V, f=1MHz	---	1780	---	pF
C _{oss}	Output Capacitance		---	267	---	
C _{rss}	Reverse Transfer Capacitance		---	178	---	
Switching Characteristics						
t _{d(on)}	Turn-On Delay Time	V _{DD} = -15V, I _D =-15A V _{GS} = -10V, R _{GEN} =3 Ω	---	8	---	ns
t _r	Rise Time		---	28	---	ns
t _{d(off)}	Turn-Off Delay Time		---	71	---	ns
t _f	Fall Time		---	40	---	ns
Q _g	Total Gate Charge	V _{GS} =-10V, V _{DS} =-15V, I _D =-15A	---	46	---	nC
Q _{gs}	Gate-Source Charge		---	6	---	nC
Q _{gd}	Gate-Drain “Miller” Charge		---	9	---	nC
Drain-Source Diode Characteristics						
I _S	Continuous Drain to Source Diode	V _D =V _G =0V	---	---	-35	A
I _{SM}	Pulsed Drain to Source Diode		---	---	-140	---
T _{rr}	Reverse Recovery Time	I _F =-4A, T _J =25 °C	---	-13.5	---	ns
Q _{rr}	Reverse Recovery Charge	dI/dt=100A/us	---	-3.7	---	nC
V _{SD}	Source-Drain Diode Forward Voltage	V _{GS} =0V, I _S =-1A	---	---	-1	V

Notes:

1. Computed continuous current assumes the condition of $T_{j,Max}$ while the actual continuous current depends on the thermal & electro-mechanical application board design
2. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature
3. EAS condition : $T_J=25^{\circ}C$, $V_{DD}=-15V$, $V_G=-10V$, $L=0.5mH$
4. Pulse Test: Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 0.5\%$

P-Typical Characteristics: ($T_C=25^{\circ}C$ unless otherwise noted)

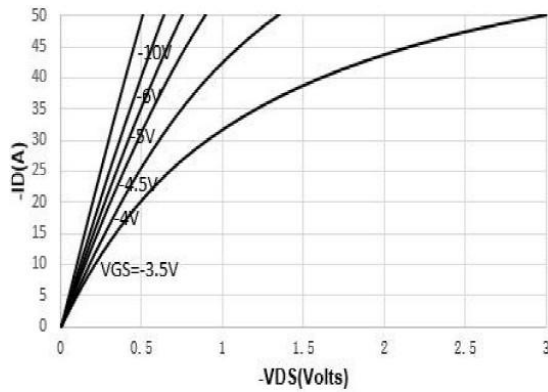


Figure 1. On-Regin Characteristics

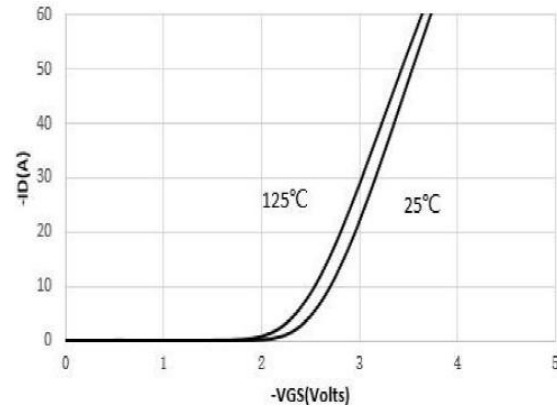


Figure 2. Transfer Characteristics

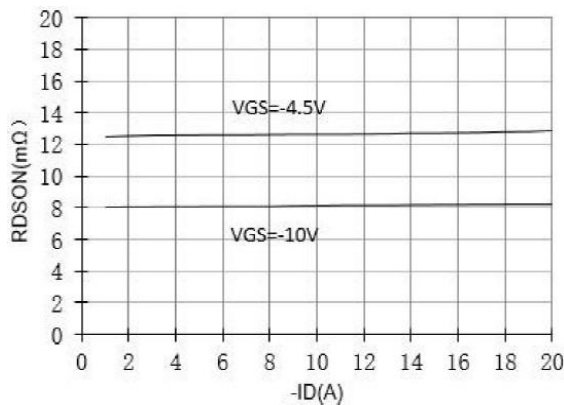


Figure 3. On-Resistance vs. Drain Current and Gate Voltage

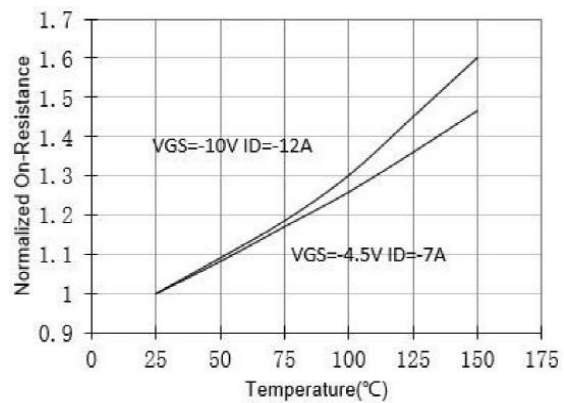


Figure 4. On-Resistance vs. Junction Temperature

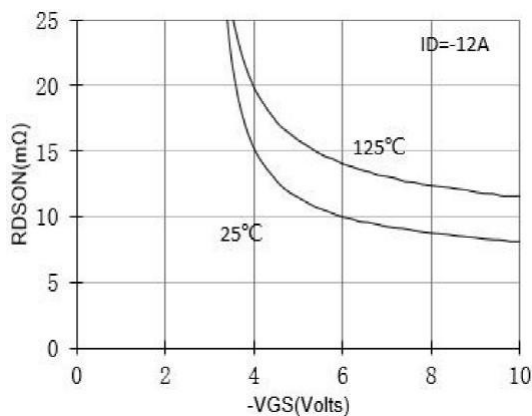


Figure 5. On-Resistance vs. Gate-Source Voltage

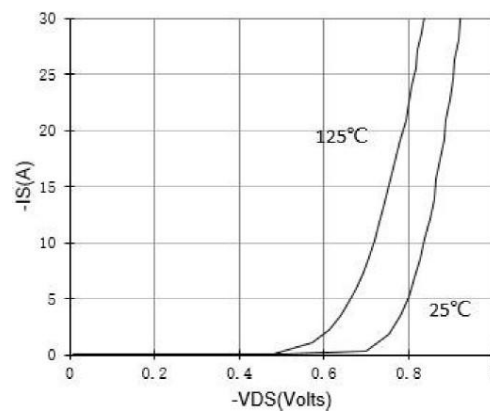


Figure 6. Body-Diode Characteristics

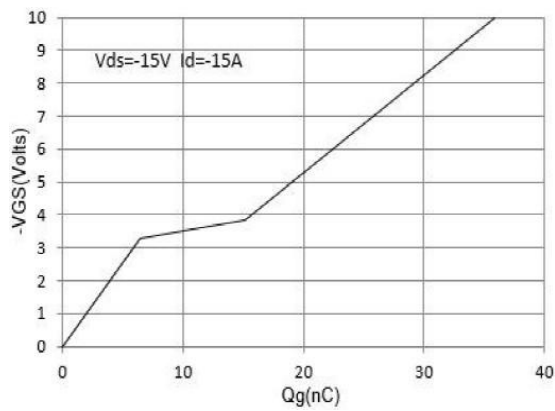


Figure 7. Gate-Charge Characteristics

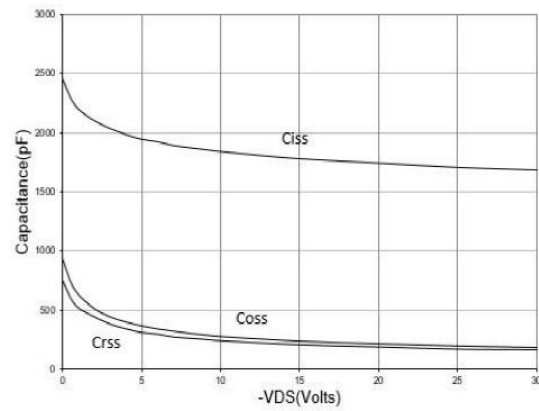


Figure 8. Capacitance Characteristics

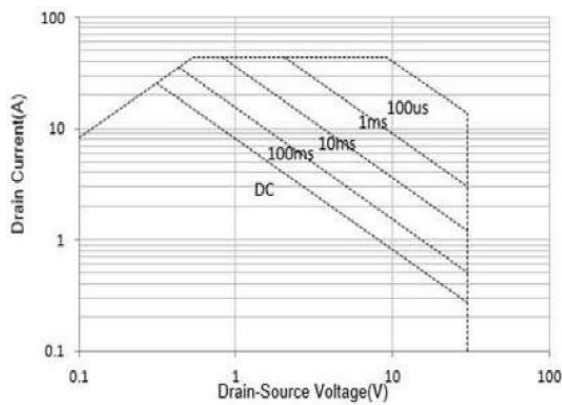


Figure 9. Maximum Forward Biased Safe Operating Area

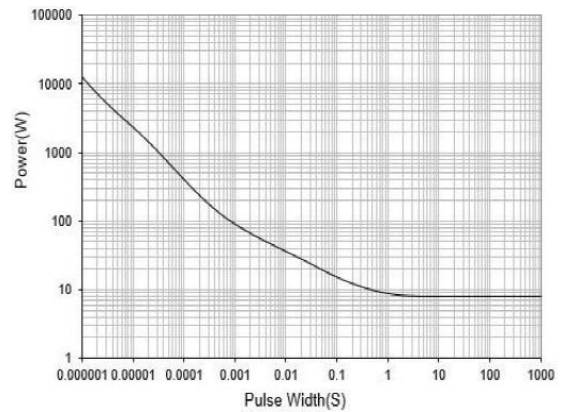


Figure 10. Single Pulse Power Rating Junction-to-Ambient

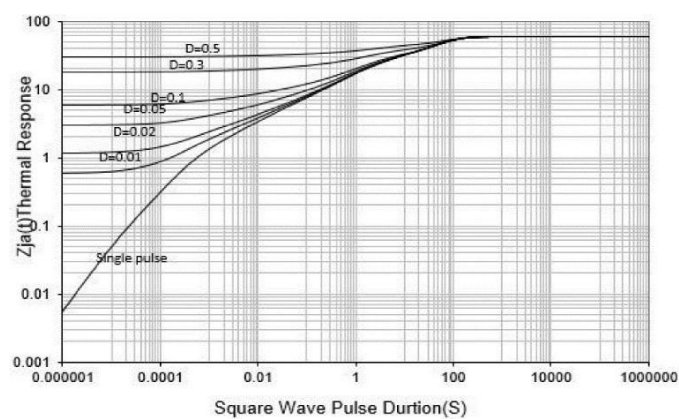
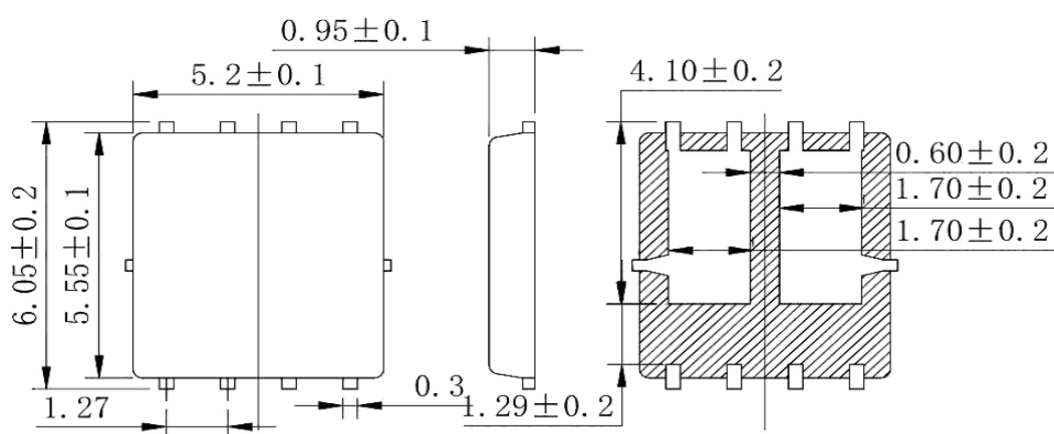
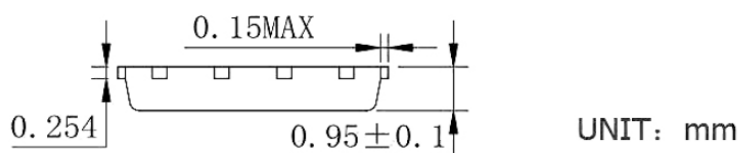


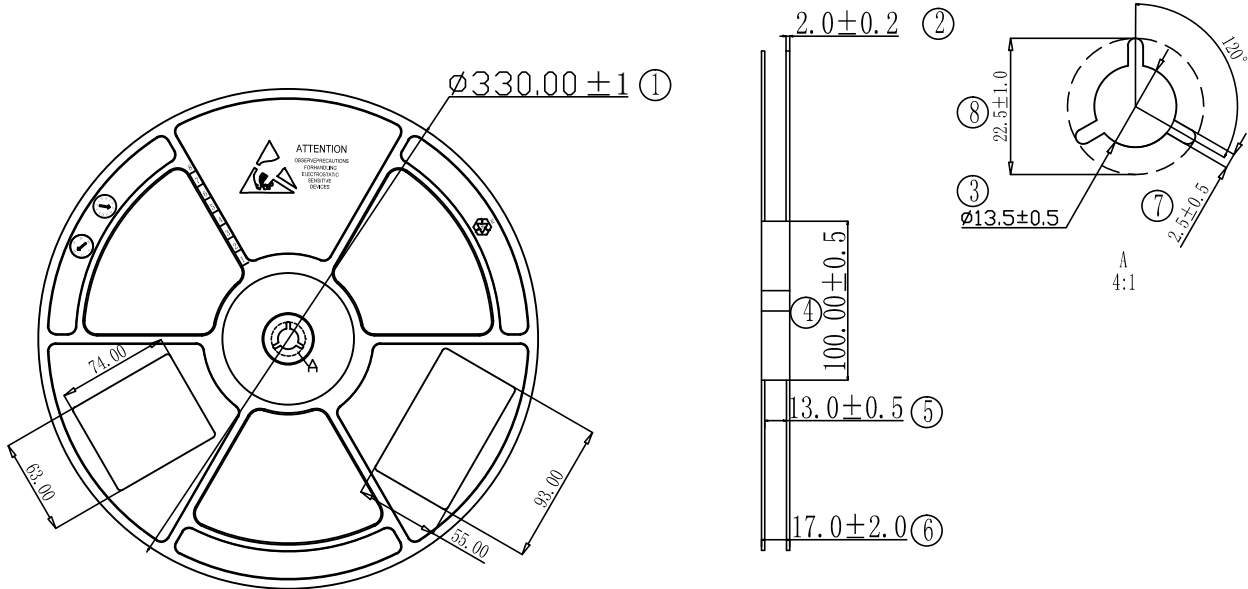
Figure 11. Normalized Maximum Transient Thermal Impedance

DFN5×6-8D Package Information:

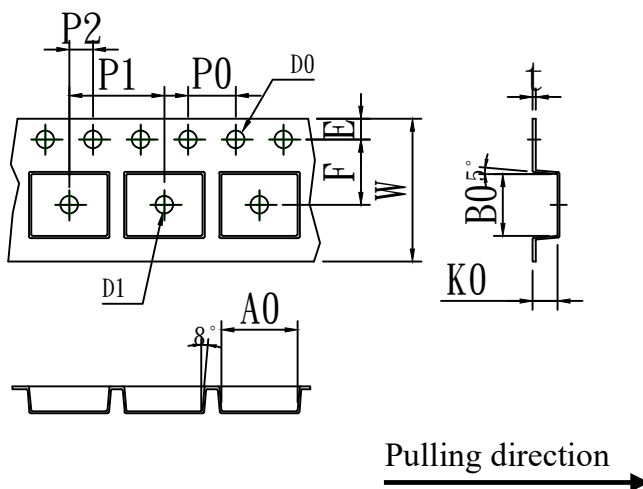


Tape & Reel Information

Dimensions in mm



Symbol	A0	B0	K0	D0	D1	P0	P1	10*P0
Spec	6.15 $\pm$ 0.10	5.40 $\pm$ 0.10	1.30 $\pm$ 0.10	1.55 $\pm$ 0.10	1.55 $\pm$ 0.10	4.00 $\pm$ 0.10	8.00 $\pm$ 0.10	40.00 $\pm$ 0.10
Symbol	W	E	F	P2	t			
Spec	12.00 $\pm$ 0.10	1.75 $\pm$ 0.10	5.50 $\pm$ 0.10	2.00 $\pm$ 0.10	0.20 $\pm$ 0.05			



Marking Information:

①. Doingter LOGO

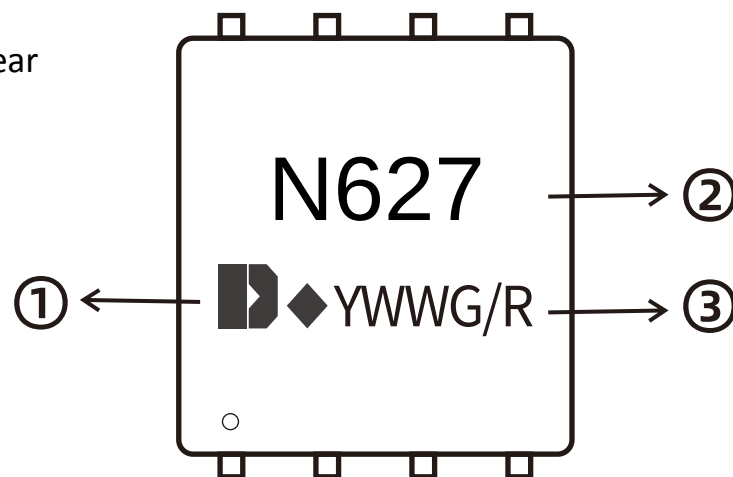
②. Part NO.

③. Date Code(YWWG / R)

Y : Year Code , last digit of the year

WW : Week Code(01-53)

G/R : G(Green) /R(Lead Free)



Previous Version

Version	Date	Subjects (major changes since last revision)
1.0	2025-03-08	Release of final version

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