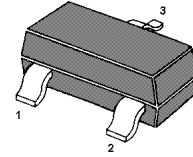
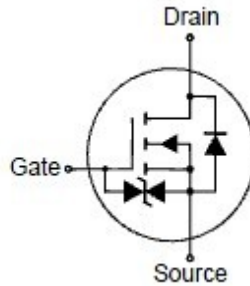


MMBT7002K N-Channel Enhancement Mode Field Effect Transistor

Features

- Low on resistance $R_{DS(ON)}$
- Low gate threshold voltage
- Low input capacitance
- ESD protected up to 2KV

MARKING:72K



1.Gate 2.Source 3.Drain
SOT-23 Plastic Package

Equivalent Circuit

Maximum Ratings & Thermal Characteristics (Ratings at 25°C ambient temperature unless otherwise specified.)

Parameters	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	60	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current	I_D	340	mA
Power Dissipation	P_D	350	mW
Junction Temperature	T_j	150	°C
Storage Temperature	T_{stg}	-50-+150	°C
Thermal Resistance From Junction to Ambient	$R_{\theta JA}$	357	°C/W

Electrical Characteristics (Ratings at 25°C ambient temperature unless otherwise specified).

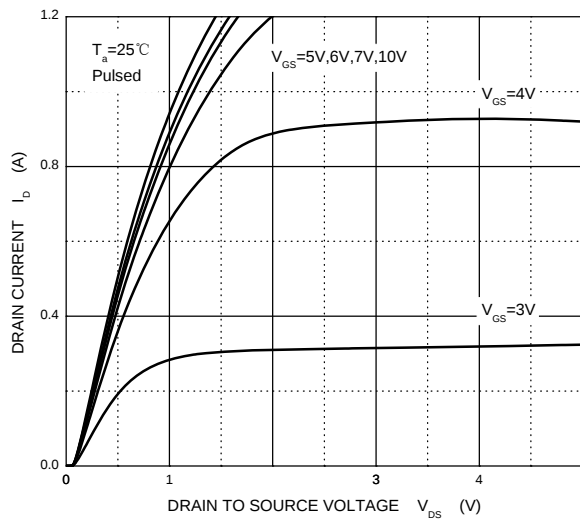
Parameter	Symbols	Test Condition	Limits			Unit
			Min	Typ	Max	
Drain-Source Breakdown Voltage	VDS	VGS=0V, ID=250uA	60			V
Gate-Threshold voltage*	Vth(GS)	VDS=VGS, ID=1mA	1	1.3	2.5	V
Gate-body Leakage	IGSS1	VDS=0V, VGS=±20V			±10	uA
	IGSS	VDS=0V, VGS=±10V			±200	nA
	IGSS	VDS=0V, VGS=±5V			±100	nA
Zero Gate Voltage Drain current	IDSS	VDS=48V, VGS=0V			1	uA
Drain-Source On-Resistance*	RDS(ON)	VGS=10V, ID=500mA		0.9	3	Ω
		VGS=4.5V, IC=200mA		1.1	4	
Diode Forward voltage	VSD	IS=300mA, VGS=0V			1.50	V
Input capacitance**	Ciss	VDS=10V, VGS=0V,f=1MHz			40	pF
Output capacitance**	Coss				30	
Reverse Transfer capacitance**	Crss				10	
SWITCHING TIME						
Turn-on Time**	td(on)	VDD=50V, RL=250Ω, VGS=10V, RGS=50Ω, RG=50Ω			10	ns
Turn-off Time**	td(off)				15	
Reverse recovery Time	trr	VGS=0V, IS=300mA, VR=25V, Dis/dt=-100a/uS		30		ns
GATE-SOURCE ZENER DIODE						
Gate-Source Breakdown Voltage	BVGso	Igs=±1mA(Open Drain)	±21.5		±30	V

Notes: * Pulse Test: Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 2\%$.

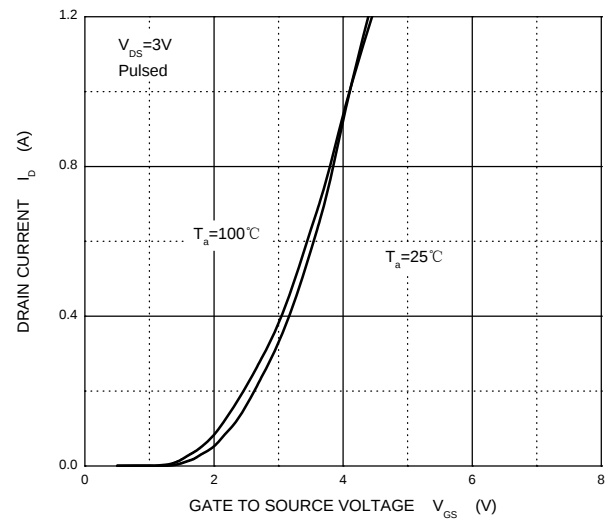
** These parameters have on way to verify.

Typical Characteristics

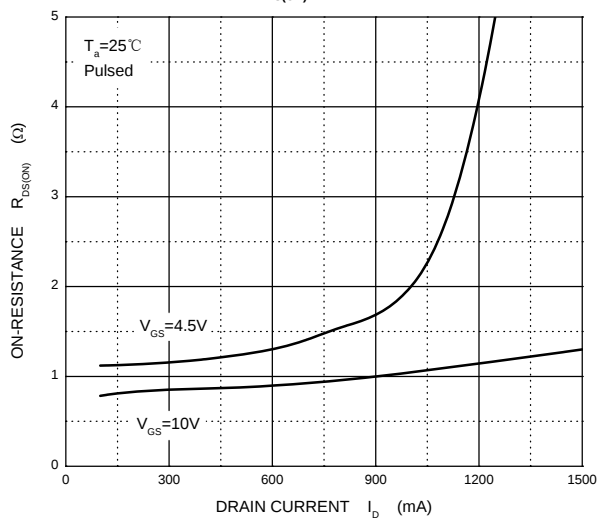
Output Characteristics



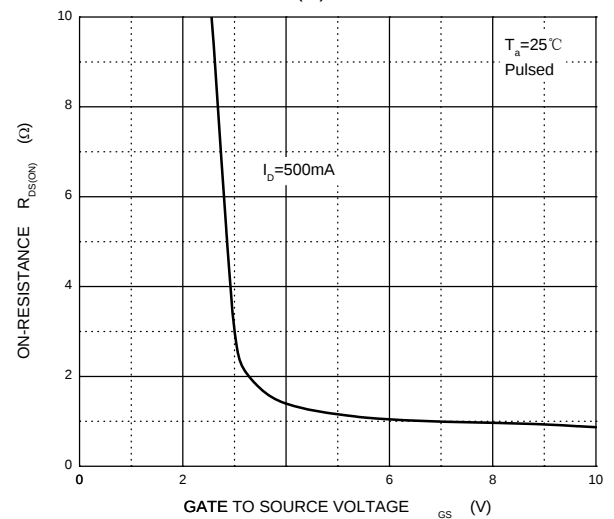
Transfer Characteristics



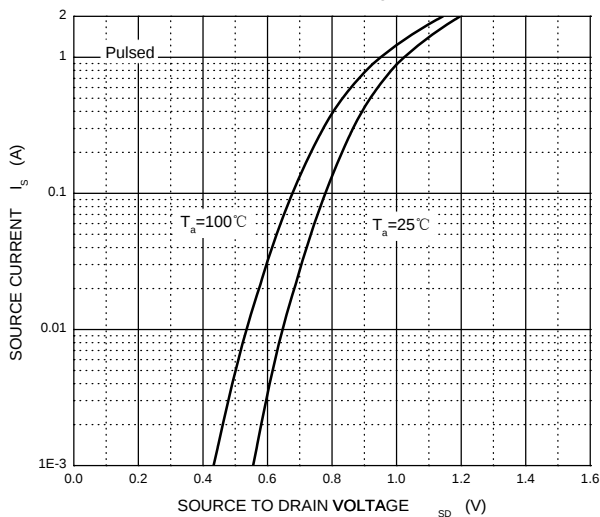
$R_{DS(ON)}$ — I_D



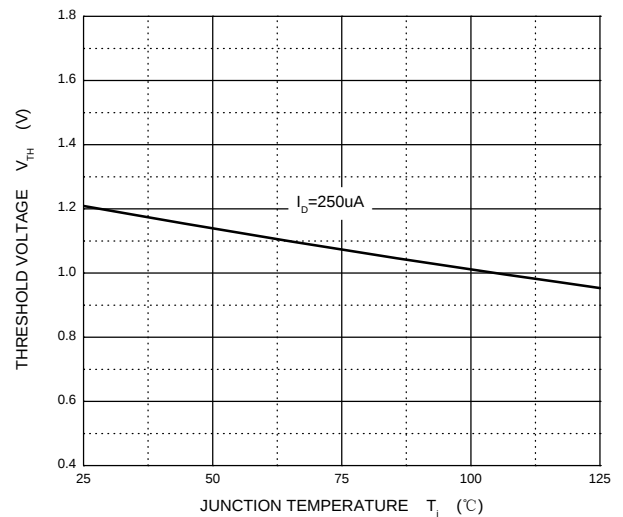
$R_{DS(ON)}$ — V_{GS}



I_S — V_{SD}



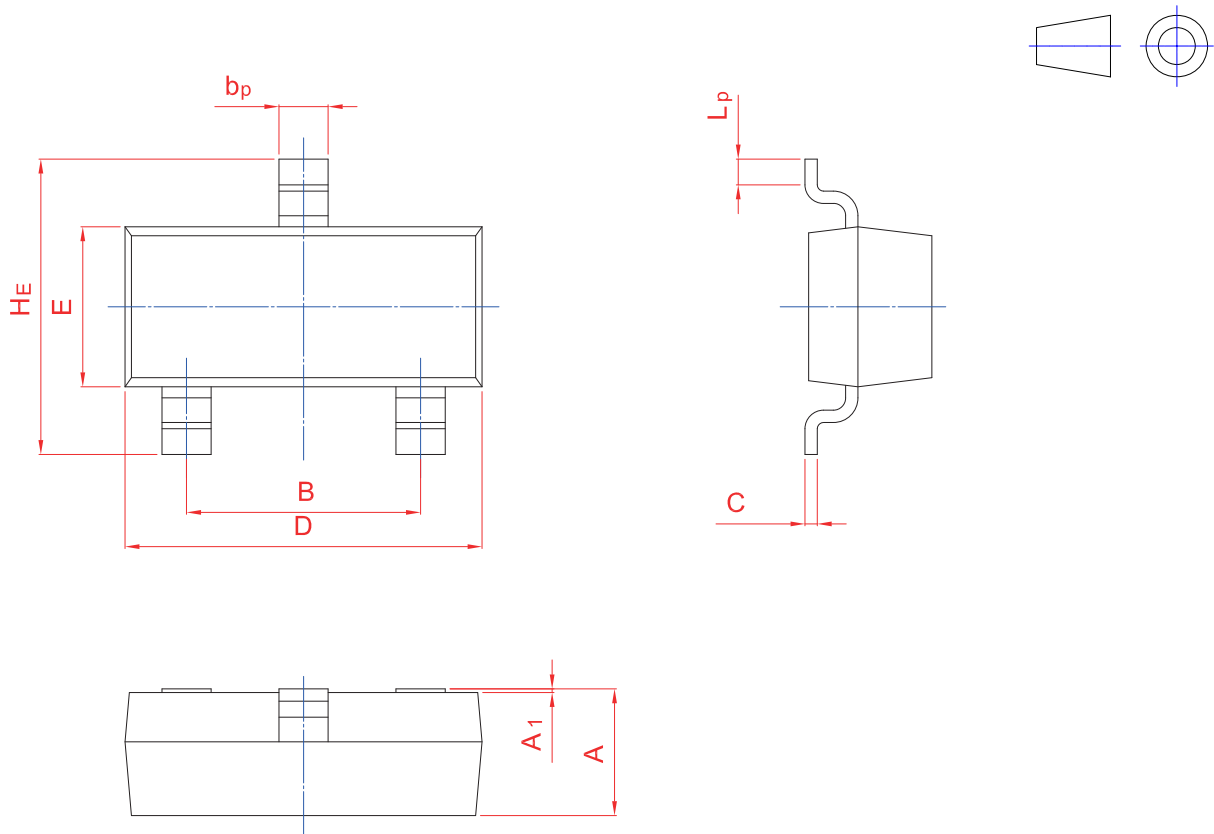
Threshold Voltage



PACKAGE OUTLINE

Plastic surface mounted package; 3 leads

SOT-23



UNIT	A	B	b_p	C	D	E	H_E	A_1	L_p
mm	1.40 0.89	2.04 1.78	0.51 0.30	0.19 0.08	3.10 2.70	1.65 1.20	3.00 2.20	0.100 0.013	0.50 0.20