

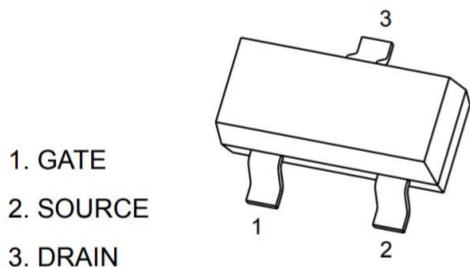
## Product Summary

- $V_{DS}$  20 V
- $R_{DS(ON)}$  ( $V_{GS}=4.5V$ )  $\leq 250m\Omega$
- $R_{DS(ON)}$  ( $V_{GS}=2.5V$ )  $\leq 300m\Omega$

## Application

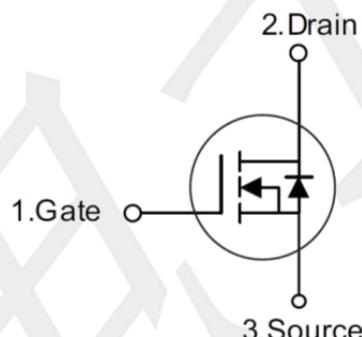
- Interfacing Switching
- Load Switch
- Portable equipment and battery

## Package and Pin Configuration



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## Circuit diagram



## Absolute Maximum Ratings ( $T_A=25^\circ C$ unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNIT
Drain-Source Voltage	$V_{DS}$	20	V
Gate-Source Voltage	$V_{GS}$	$\pm 8$	V
Continuous Drain Current $T_A=25^\circ C$	$I_D$	0.8	A
Continuous Drain Current $T_A=70^\circ C$	$I_D$	0.7	A
Pulsed Drain Current ( $t = 100 \mu s$ )	$I_{DM}$	1.8	A
Maximum Power Dissipation $T_A=25^\circ C$	$P_D$	0.54	W
Operating Junction Temperature Range	$T_J$	-55 to +150	°C
Storage Temperature Range	$T_{stg}$	-55 to +150	°C

## Thermal Characteristic

PARAMETER	Symbol	Value	Unit
Thermal Resistance from Junction to Ambient( $t \leq 10s$ )	$R_{\theta JA}$	450	°C/W

Note : When mounted on 1" square PCB (FR4 material).

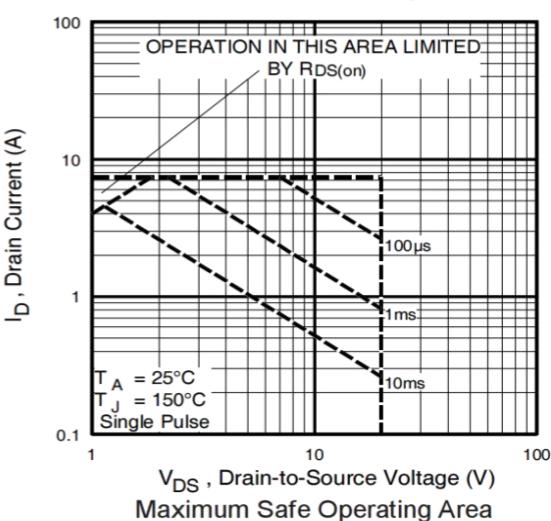
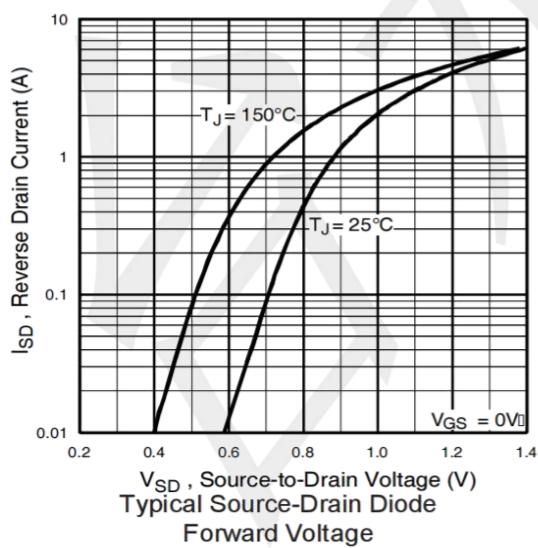
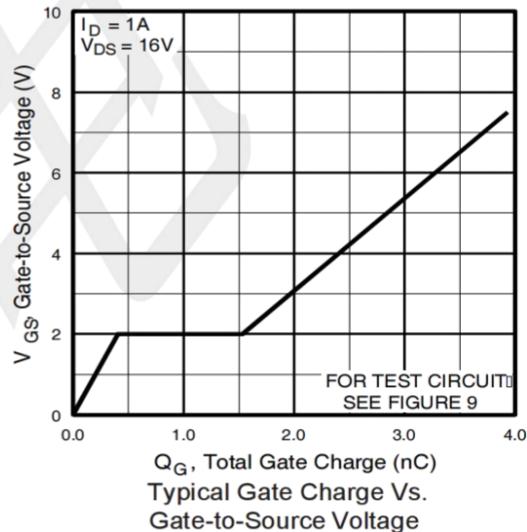
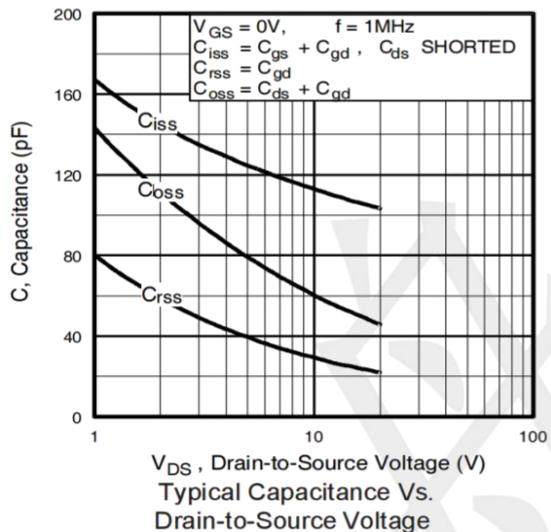
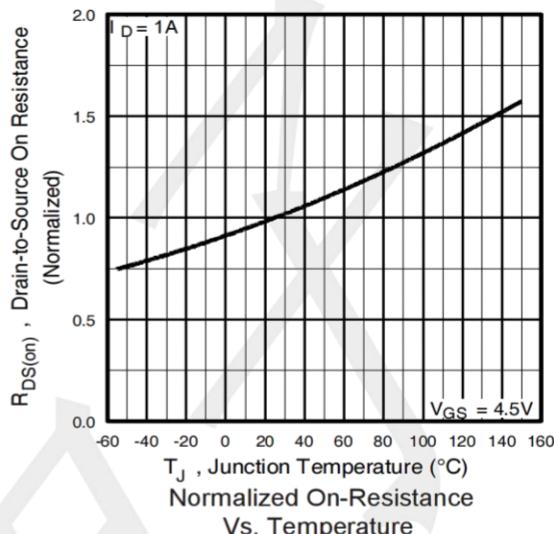
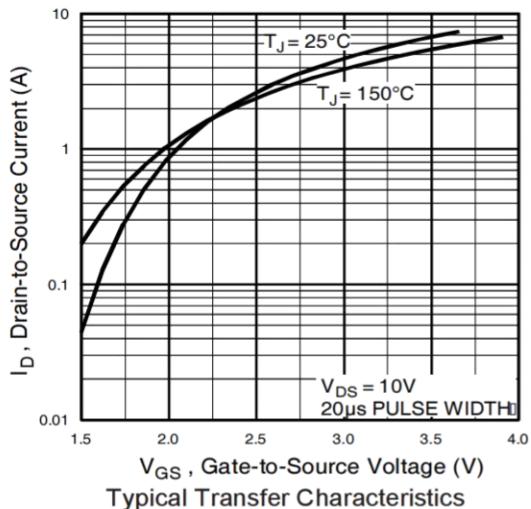
**Electrical Characteristics** ( $T_A=25^\circ\text{C}$  unless otherwise noted)

PARAMETER	CONDITIONS	SYMBOL	MIN	TYP	MAX	UNIT
<b>Static</b>						
Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}$ , $I_D= 250\mu\text{A}$	$BV_{DSS}$	20	--	--	V
Gate-Source Threshold Voltage	$V_{DS}=V_{GS}$ , $I_D= 250\mu\text{A}$	$V_{GS(\text{th})}$	0.5	0.75	1.1	V
Gate-Source Leakage	$V_{DS}=0\text{V}$ , $V_{GS}= \pm 12\text{V}$	$I_{GSS}$	--	--	$\pm 100$	nA
Zero Gate Voltage Drain Current	$V_{DS}= 20\text{V}$ , $V_{GS}=0\text{V}$	$I_{DSS}$	--	--	1.0	$\mu\text{A}$
Drain-Source On-State Resistance (Note 1)	$V_{GS}= 4.5\text{V}$ , $I_D= 0.55\text{A}$	$R_{DS(\text{on})}$	--	180	250	$\text{m}\Omega$
	$V_{GS}= 2.5\text{V}$ , $I_D= 0.45\text{A}$		--	230	300	
Forward Transconductance (Note 2)	$V_{DS}= 10\text{V}$ , $I_D= 0.5\text{A}$	$g_{fs}$	--	1.7	--	S
<b>Dynamic</b> (Note 2)						
Input Capacitance	$V_{DS} = 10\text{V}$ , $V_{GS} = 0\text{V}$ , $f = 1.0\text{MHz}$	$C_{iss}$	--	120	--	$\text{pF}$
Output Capacitance		$C_{oss}$	--	55	--	
Reverse Transfer Capacitance		$C_{rss}$	--	26	--	
<b>Switching</b>						
Turn-On Delay Time (Note 3)	$V_{DS} = 10\text{V}$ , $V_{GS}=4.5\text{V}$ , $I_D = 1.0\text{A}$ $R_G = 6\Omega$ ,	$t_{d(on)}$	--	2.6	--	$\text{nS}$
Rise Time (Note 3)		$t_r$	--	9.4	--	
Turn-Off Delay Time (Note 3)		$t_{d(off)}$	--	9.8	--	
Fall Time (Note 3)		$t_f$	--	4.8	--	
Total Gate Charge	$V_{DS} = 10\text{V}$ , $I_D= 1.0\text{A}$ , $V_{GS}= 4.5\text{V}$	$Q_g$	--	2.8	--	$\text{nC}$
Gate Source Charge		$Q_{gs}$	--	0.5	--	
Gate Drain Charge		$Q_{gd}$	--	1.2	--	
<b>Source-Drain Diode Ratings and Characteristics</b> (Note 2)						
Forward Voltage	$V_{GS} = 0\text{V}$ , $I_F = 0.5\text{A}$	$V_{SD}$	--	0.7	1.2	V
Continuous Source Current	Integral reverse diode in the MOSFET	$I_s$	--	--	0.8	A
Pulsed Current (Note 1)		$I_{SM}$	--	--	1.8	A

Notes:

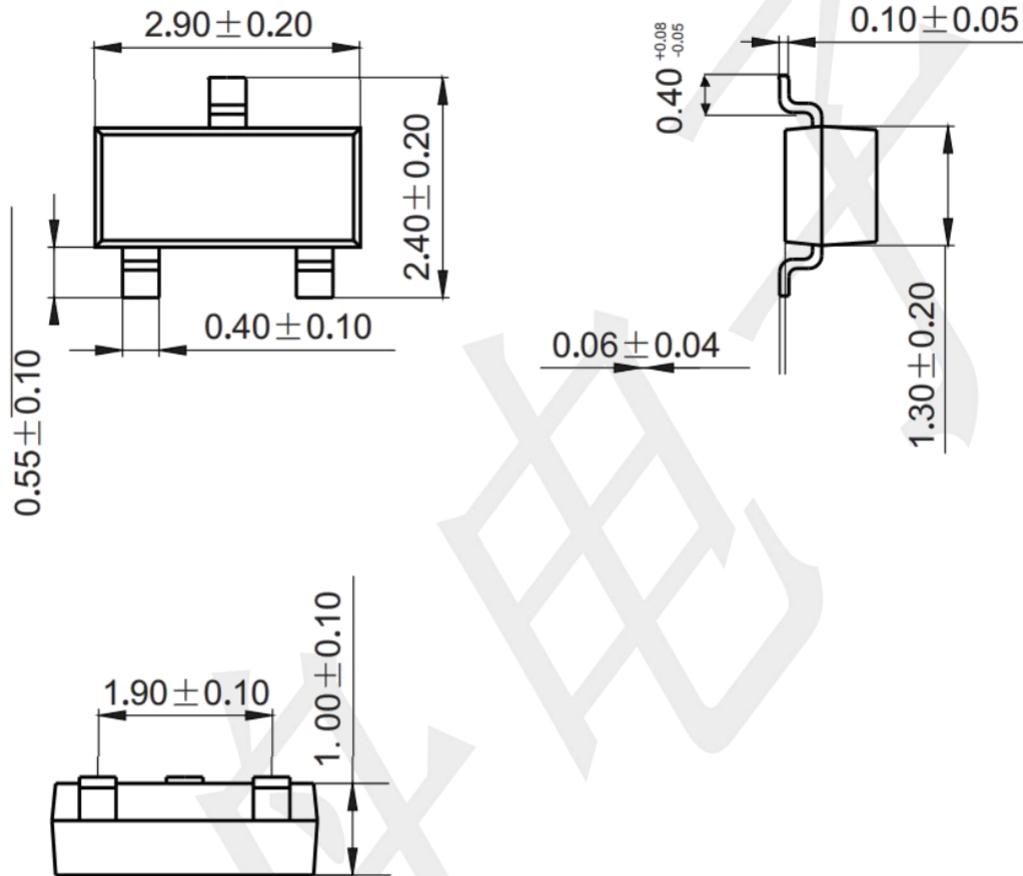
1. Pulse test; pulse width  $\leq 300 \mu\text{s}$ , duty cycle  $\leq 2\%$ .
2. Guaranteed by design, not subject to production testing.
3. Independent of operating temperature

## TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



**Package Outline Dimensions (unit: mm)**

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**Mounting Pad Layout (unit: mm)**

