

1、General Description

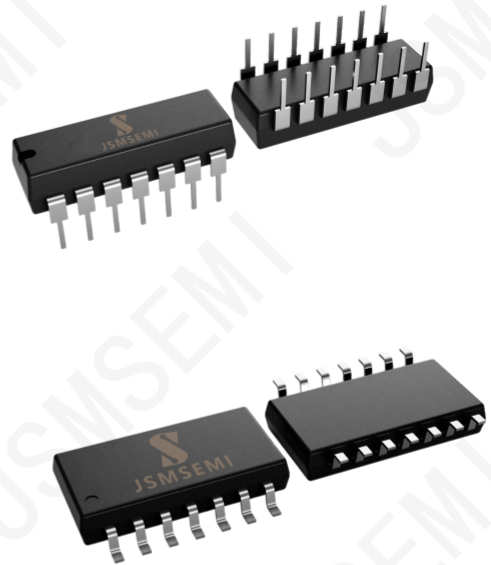
The CD4069 is a general purpose hex unbuffered inverter.

Each inverter has a single stage.

It operates over a recommended V_{DD} power supply range of 3V to 15V referenced to V_{SS} (usually ground). Unused inputs must be connected to V_{DD} , V_{SS} , or another input.

Features:

- Wide supply voltage range from 3V to 15V
- Fully static operation
- 5V, 10V, and 15V parametric ratings
- Standardized symmetrical output characteristics
- Specified from -40°C to $+125^{\circ}\text{C}$
- Packaging information: DIP14/SOP14/TSSOP14



2、Block Diagram And Pin Description

2.1、Block Diagram

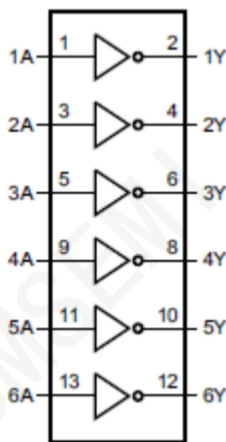


Figure 1. Functional diagram

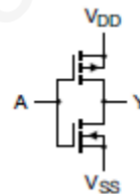
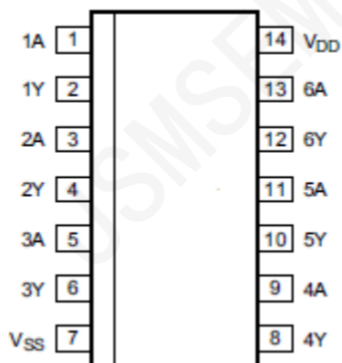


Figure 2. Schematic diagram (one inverter)

Ordering Information

Order number	Package	Marking	Operation Temperature Range	MSL Grade	Ship,Quantity	Green
CD4069BE-JSM	DIP-14	CD4069BE	-40 to 125°C	3	TUBE,1000	RoHS
CD4069BM-JSM	SOP-14	CD4069BM	-40 to 125°C	3	T&R,2500	RoHS
CD4069BPWR-JSM	TSSOP-14	CD4069B	-40 to 125°C	3	T&R,2500	RoHS

2.2、Pin Configurations



2.3、Pin Description

Pin No.	Pin Name	Description
1	1A	data input
2	1Y	data output
3	2A	data input
4	2Y	data output
5	3A	data input
6	3Y	data output
7	V _{SS}	ground (0V)
8	4Y	data output
9	4A	data input
10	5Y	data output
11	5A	data input
12	6Y	data output
13	6A	data input
14	V _{DD}	supply voltage

2.4、Function Table

Input	Output
nA	nY
L	H
H	L

Note: H=HIGH voltage level; L=LOW voltage level.

3、Electrical Parameter

3.1、Absolute Maximum Ratings

(Voltages are referenced to V_{SS} (ground=0V), unless otherwise specified.)

Parameter	Symbol	Conditions	Min.	Max.	Unit
supply voltage	V_{DD}	-	-0.5	+18	V
DC input current	I_{IK}	any one input	-	± 10	mA
input voltage	V_I	all inputs	-0.5	$V_{DD}+0.5$	V
storage temperature	T_{stg}	-	-65	+150	°C
total power dissipation	P_{tot}	-	-	500	mW
device dissipation	P	per output transistor	-	100	mW
soldering temperature	T_L	10s	DIP	245	°C
			SOP/TSSOP	260	

3.2、Recommended Operating Conditions

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
supply voltage	V_{DD}	-	3	-	15	V
ambient temperature	T_{amb}	in free air	-40	-	+125	°C

3.3、Electrical Characteristics

3.3.1、DC Characteristics 1

($T_{amb}=25^{\circ}\text{C}$, voltages are referenced to V_{SS} (ground=0V), unless otherwise specified.)

Parameter	Symbol	Conditions (V)			$T_{amb}=25^{\circ}\text{C}$			Unit
		V_O	V_{IN}	V_{DD}	Min.	Typ.	Max.	
supply current	I_{DD}	-	0, 5	5	-	-	1	uA
		-	0, 10	10	-	-	1	uA
		-	0, 15	15	-	-	1	uA
LOW-level output current	I_{OL}	0.4	0, 5	5	0.41	-	-	mA
		0.5	0, 10	10	0.55	-	-	mA
		1.5	0, 15	15	1.7	-	-	mA
HIGH-level output current	I_{OH}	4.6	0, 5	5	-0.41	-	-	mA
		2.5	0, 5	5	-1.6	-	-	mA
		9.5	0, 10	10	-0.65	-	-	mA
		13.5	0, 15	15	-2.0	-	-	mA
LOW-level output voltage	V_{OL}	-	0, 5	5	-	0	0.05	V
		-	0, 10	10	-	0	0.05	V
		-	0, 15	15	-	0	0.05	V
HIGH-level output voltage	V_{OH}	-	0, 5	5	4.95	5	-	V
		-	0, 10	10	9.95	10	-	V
		-	0, 15	15	14.95	15	-	V
LOW-level input voltage	V_{IL}	0.5, 4.5	-	5	-	-	1	V
		1, 9	-	10	-	-	2	V
		1.5, 13.5	-	15	-	-	2.5	V
HIGH-level input voltage	V_{IH}	0.5	-	5	4	-	-	V
		1	-	10	8	-	-	V
		1.5	-	15	12.5	-	-	V
input leakage current	I_I	-	0, 15	15	-	-	± 1	uA

3.3.2、DC Characteristics 2

($T_{amb} = -40^{\circ}\text{C}$ to $+85^{\circ}\text{C}$, voltages are referenced to V_{SS} (ground=0V), unless otherwise specified.)

Parameter	Symbol	Conditions (V)			$T_{amb} = -40^{\circ}\text{C}$		$T_{amb} = +85^{\circ}\text{C}$		Unit
		V_O	V_{IN}	V_{DD}	Min.	Max.	Min.	Max.	
supply current	I_{DD}	-	0, 5	5	-	7.5	-	7.5	μA
		-	0, 10	10	-	15	-	15	μA
		-	0, 15	15	-	30	-	30	μA
LOW-level output current	I_{OL}	0.4	0, 5	5	0.5	-	0.34	-	mA
		0.5	0, 10	10	0.63	-	0.46	-	mA
		1.5	0, 15	15	2	-	1.4	-	mA
HIGH-level output current	I_{OH}	4.6	0, 5	5	-0.5	-	-0.34	-	mA
		2.5	0, 5	5	-1.8	-	-1.3	-	mA
		9.5	0, 10	10	-0.75	-	-0.55	-	mA
		13.5	0, 15	15	-2.4	-	-1.65	-	mA
LOW-level output voltage	V_{OL}	-	0, 5	5	-	0.05	-	0.05	V
		-	0, 10	10	-	0.05	-	0.05	V
		-	0, 15	15	-	0.05	-	0.05	V
HIGH-level output voltage	V_{OH}	-	0, 5	5	4.95	-	4.95	-	V
		-	0, 10	10	9.95	-	9.95	-	V
		-	0, 15	15	14.95	-	14.95	-	V
LOW-level input voltage	V_{IL}	0.5, 4.5	-	5	-	1	-	1	V
		1, 9	-	10	-	2	-	2	V
		1.5, 13.5	-	15	-	2.5	-	2.5	V
HIGH-level input voltage	V_{IH}	0.5	-	5	4	-	4	-	V
		1	-	10	8	-	8	-	V
		1.5	-	15	12.5	-	12.5	-	V
input leakage current	I_I	-	0, 15	15	-	± 10	-	± 10	μA

3.3.3、DC Characteristics 3

($T_{amb}=-40^{\circ}\text{C}$ to $+125^{\circ}\text{C}$, voltages are referenced to V_{SS} (ground=0V), unless otherwise specified.)

Parameter	Symbol	Conditions (V)			$T_{amb}=-40^{\circ}\text{C}$		$T_{amb}=+125^{\circ}\text{C}$		Unit
		V_O	V_{IN}	V_{DD}	Min.	Max.	Min.	Max.	
supply current	I_{DD}	-	0, 5	5	-	7.5	-	7.5	μA
		-	0, 10	10	-	15	-	15	μA
		-	0, 15	15	-	30	-	30	μA
LOW-level output current	I_{OL}	0.4	0, 5	5	0.5	-	0.29	-	mA
		0.5	0, 10	10	0.63	-	0.38	-	mA
		1.5	0, 15	15	2	-	1.2	-	mA
HIGH-level output current	I_{OH}	4.6	0, 5	5	-0.5	-	-0.3	-	mA
		2.5	0, 5	5	-1.8	-	-1.15	-	mA
		9.5	0, 10	10	-0.75	-	-0.45	-	mA
		13.5	0, 15	15	-2.4	-	-1.4	-	mA
LOW-level output voltage	V_{OL}	-	0, 5	5	-	0.05	-	0.05	V
		-	0, 10	10	-	0.05	-	0.05	V
		-	0, 15	15	-	0.05	-	0.05	V
HIGH-level output voltage	V_{OH}	-	0, 5	5	4.95	-	4.95	-	V
		-	0, 10	10	9.95	-	9.95	-	V
		-	0, 15	15	14.95	-	14.95	-	V
LOW-level input voltage	V_{IL}	0.5, 4.5	-	5	-	1	-	1	V
		1, 9	-	10	-	2	-	2	V
		1.5, 13.5	-	15	-	2.5	-	2.5	V
HIGH-level input voltage	V_{IH}	0.5	-	5	4	-	4	-	V
		1	-	10	8	-	8	-	V
		1.5	-	15	12.5	-	12.5	-	V
input leakage current	I_I	-	0, 15	15	-	± 10	-	± 10	μA

3.3.4、AC Characteristics

($T_{amb}=25^{\circ}\text{C}$, $V_{SS}=0\text{V}$, $t_r, t_f=20\text{ns}$, $C_L=50\text{pF}$, $R_L=200\text{k}\Omega$, unless otherwise specified.)

Parameter	Symbol	Conditions		Min.	Typ.	Max.	Unit
propagation delay time	t_{PHL}, t_{PLH}	see Figure 4	$V_{DD}=5\text{V}$	-	55	110	ns
			$V_{DD}=10\text{V}$	-	30	60	ns
			$V_{DD}=15\text{V}$	-	25	50	ns
transition time	t_{THL}, t_{TLH}	see Figure 4	$V_{DD}=5\text{V}$	-	100	200	ns
			$V_{DD}=10\text{V}$	-	50	100	ns
			$V_{DD}=15\text{V}$	-	40	80	ns
input capacitance	C_I	any input		-	10	15	pF

4、Testing Circuit

4.1、AC Testing Circuit

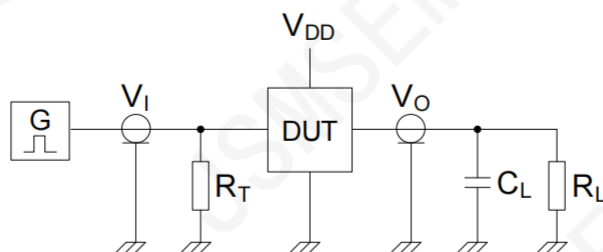


Figure 3. Test circuit for switching times

Definitions for test circuit:

DUT=Device Under Test.

C_L =Load capacitance including jig and probe capacitance.

R_T =Termination resistance should be equal to the output impedance Z_o of the pulse generator.

R_L = Load resistance.

4.2、AC Testing Waveforms

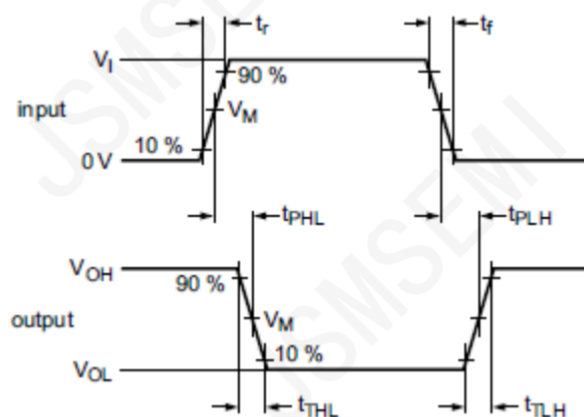


Figure 4. Propagation delay, output transition time

4.3、Measurement Points

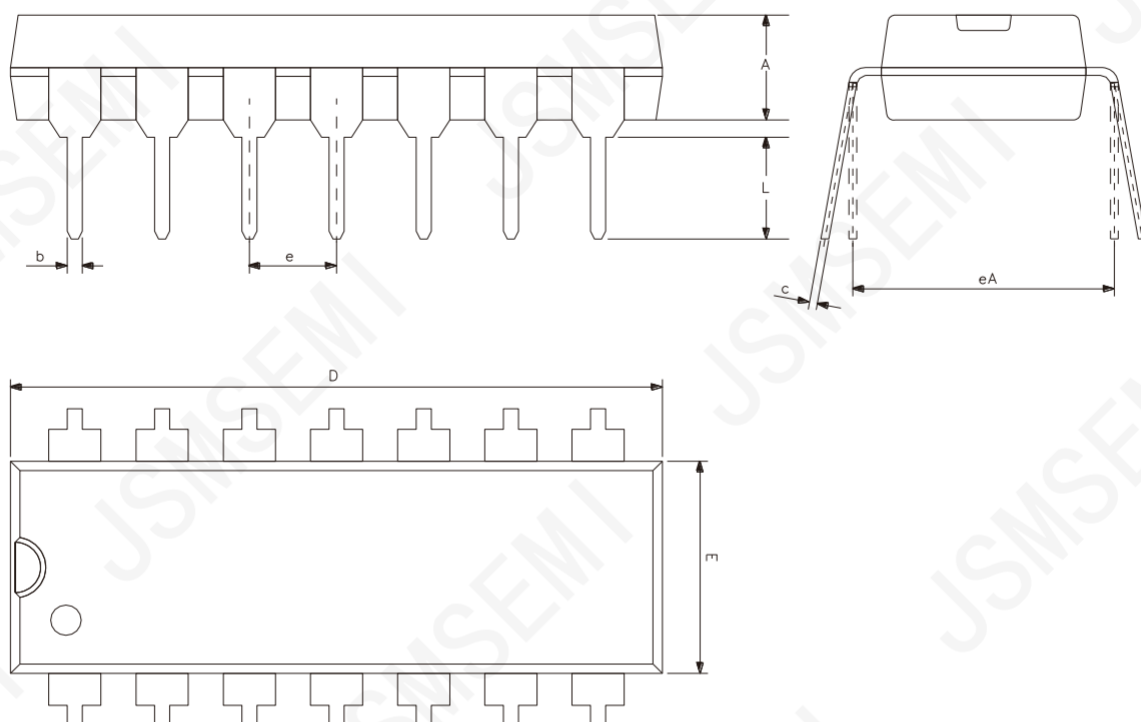
Supply voltage	Input	Output
V_{DD}	V_M	V_M
5V to 15V	$0.5 \times V_{DD}$	$0.5 \times V_{DD}$

4.4、Test Data

Supply voltage	Input		Load
V_{DD}	V_I	t_r, t_f	C_L
5V to 15V	V_{SS} or V_{DD}	$\leq 20\text{ns}$	50pF

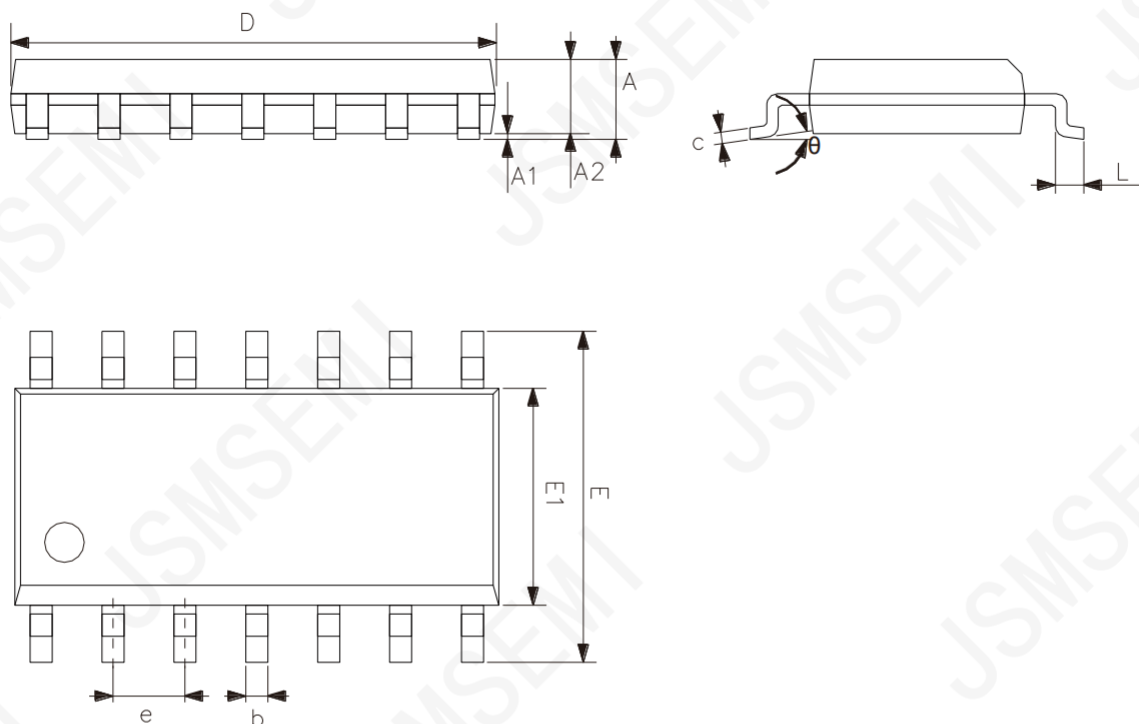
5、Package Information

5.1、DIP14



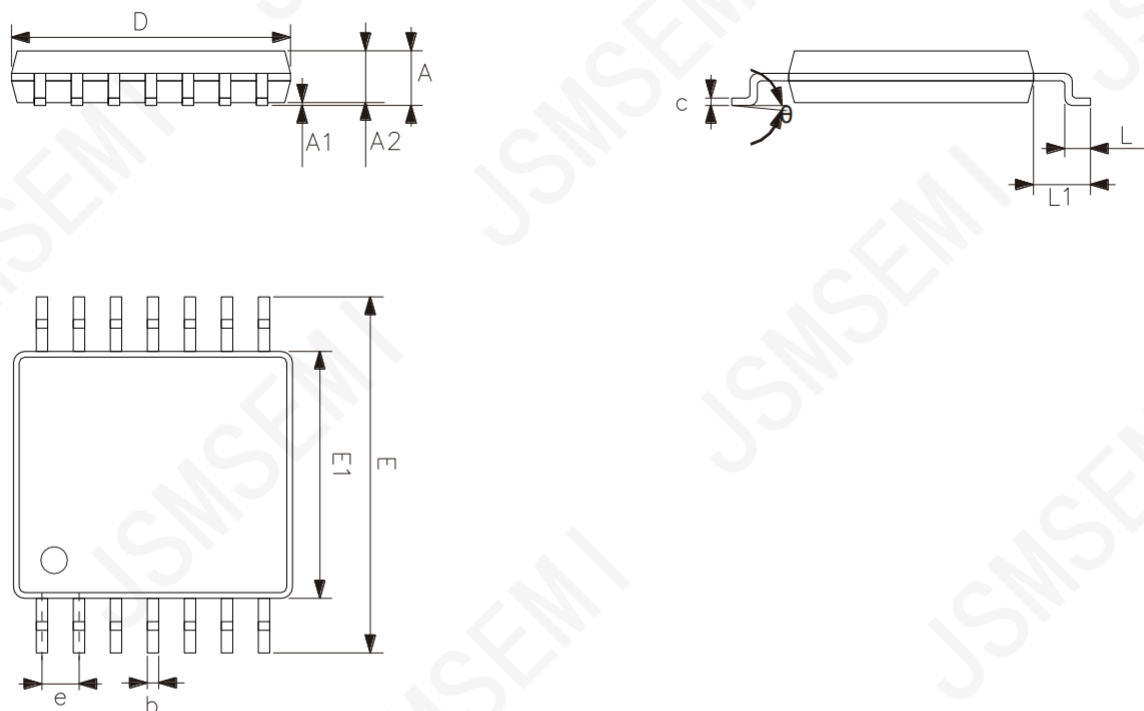
Symbol	Dimensions (mm)	
	Min.	Max.
A	3.05	3.60
b	0.33	0.56
c	0.20	0.36
D	18.80	19.40
E	6.20	6.60
e	2.54	
eA	7.62	10.90
L	2.92	-

5.2、SOP14



Symbol	Dimensions (mm)	
	Min.	Max.
A	1.50	1.75
A1	0.05	0.25
A2	1.30	-
b	0.33	0.50
c	0.19	0.25
D	8.43	8.76
E	5.80	6.25
E1	3.75	4.00
e	1.27	
L	0.40	0.89
θ	0°	8°

5.3、TSSOP14



Symbol	Dimensions (mm)	
	Min.	Max.
A	-	1.20
A1	0.05	0.15
A2	0.80	1.05
b	0.19	0.30
c	0.09	0.20
D	4.90	5.10
E1	4.30	4.50
E	6.20	6.60
e	0.65	
L	0.45	0.75
L1	1.00	
θ	0°	8°

6、Statements And Notes

6.1、The name and content of Hazardous substances or Elements in the product

Part name	Hazardous substances or Elements									
	Lead and lead compounds	Mercury and mercury compounds	Cadmium and cadmium compounds	Hexavalent chromium compounds	Polybrominated biphenyls	Polybrominated biphenyl ethers	Dibutyl phthalate	Butylbenzyl phthalate	Di-2-ethylhexyl phthalate	Diisobutyl phthalate
Lead frame	○	○	○	○	○	○	○	○	○	○
Plastic resin	○	○	○	○	○	○	○	○	○	○
Chip	○	○	○	○	○	○	○	○	○	○
The lead	○	○	○	○	○	○	○	○	○	○
Plastic sheet installed	○	○	○	○	○	○	○	○	○	○
explanation	○: Indicates that the content of hazardous substances or elements in the detection limit of the following the SJ/T11363-2006 standard. ×: Indicates that the content of hazardous substances or elements exceeding the SJ/T11363-2006 Standard limit requirements.									

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