PRODUCT DESCRIPTION

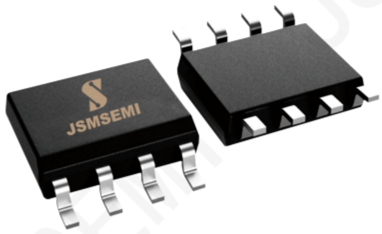
The JSM8632XS/TR(dual) are low noise, low voltage, and low power operational amplifiers that can be designed into a wide range of applications. The JSM8632XS/TR have a high gain bandwidth product of 6MHz and a slew rate of 3.7V/μs.

The JSM8632XS/TR are designed to provide optimal performance in low voltage and low noise systems.

They provide rail-to-rail output swing into heavy loads. The input common mode voltage range includes ground, and the maximum input offset voltage is 3.5mV for JSM8632XS/TR. They are specified over the extended industrial temperature range (-40°C to +125°C).

The operating supply range is from 2V to 5.5V.

The JSM8632XS/TR dual is available in Green SOP-8 packages.



SOP-8

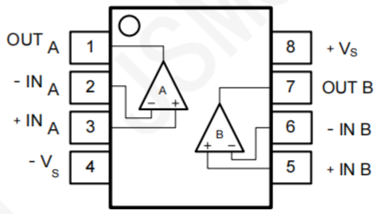


Figure 1. Pin Assignment Diagram

FEATURES

- Rail-to-Rail Input and Output
  3.5mV Maximum V_{OS}
- High Gain-Bandwidth Product: 6MHz
- High Slew Rate: 3.7V/**μs**
- Settling Time to 0.1% with 2V Step: 0.5**μs**
- Overload Recovery Time: 0.9**μs**
- Low Noise: 13nV/ $\sqrt{Hz}$ at 1kHz
- Supply Voltage Range: 2V to 5.5V
- Input Voltage Range: -0.1V to +5.6V with V_S = 5.5V

ABSOLUTE MAXIMUM RATINGS

Supply Voltage, $+V_S$ to $-V_S$ 6V
Input Common Mode Voltage Range
.... $(-V_S) - 0.3V$ to $(+V_S) + 0.3V$
Storage Temperature Range ....-65°C to +150°C
Junction Temperature ....+150°C
Package Thermal Resistance @ T_A = 25°C
SOP-8, θ_{JA} .... 125°C/W
Lead Temperature (Soldering 10sec)
....+260°C
ESD Susceptibility
HBM 8000V
MM..400V
CDM 1000V

RECOMMENDED OPERATING CONDITIONS

Operating Temperature Range ....-40°C to +125°C

Note: Stress greater than those listed under Absolute Maximum Ratings may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions outside those indicated in the operational sections of this specification are not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

Ordering Information

MODEL	PACKAGE DESCRIPTION	SPECIFIED TEMPERATURE RANGE	ORDERING NUMBER	PACKING OPTION
JSM8632	SOP-8	-40℃to +125℃	JSM8632XS/TR	Tape and Reel, 2500

Electrical Characteristics

(At Vs=5V,TA=+25℃,VCM=Vs/2,RL=600Ω,unless otherwise ngted)

PARAMETER	CONDITIONS	JSM8632XS/TR						
		TYP	MIN/MAX OVER TEMPERATURE					
		+25℃	+25℃	0℃ to 70℃	-40℃ to 85℃	-40 ℃ to 125℃	UNITS	MIN / MAX
INPUT CHARACTERISTICS								
Input Offset Voltage (V_{OS})		0.8	3.	3.	4.	4.	mV	MAX
Input Bias Current (I_B)		1					pA	TYP
Input Offset Current (I_{OS})		1					pA	TYP
Input Common Mode Voltage Range (V_{CM})	V_S = 5.5V	-0.1 to +5.6					V	TYP
Common Mode Rejection Ratio (CMRR)	V_S = 5.5V, V_{CM} = -0.1V to 4V	90	57	97	37	66	dB	MIN
	V_S = 5.5V, V_{CM} = -0.1V to 5.6V	83					dB	MIN
Open-Loop Voltage Gain (A_{OL})	R_L = 600Ω,V_O = 0.15V to 4.85V	97	39	08	08	57	dB	MIN
	R_L = 10kΩ,V_O = 0.05V to 4.95V	108					dB	MIN
Input Offset Voltage Drift ($\Delta V_{OS}/\Delta_T$)		2.4	0	7	6	9	μV/℃	TYP
OUTPUT CHARACTERISTICS								
Output Voltage Swing from Rail	R_L = 600Ω	0.1					V	TYP
	R_L = 10kΩ	0.015					V	TYP
Output Current (I_{OUT})		53	49	45	40	35	mA	MIN
Closed-Loop Output Impedance	f = 200kHz, G = 1	3					Ω	TYP
POWER-DOWN DISABLE								
Turn-On Time		4					μs	TYP
Turn-Off Time		1.2					μs	TYP
DISABLE Voltage-Off			0.8				V	MAX
DISABLE Voltage-On			2				V	MIN
POWER SUPPLY								
Operating Voltage Range			2.1	2.1	2.1	2.1	V	MIN
			5.5	5.5	5.5	5.5	V	MAX
Power Supply Rejection Ratio (PSRR)	V_S = +2.5V to +5.5V V_{CM} = (-V_S) + 0.5V	91	74	72	72	68	dB	MIN
Quiescent Current/Amplifier (I_Q)	I_{OUT} = 0	470	650	727	750	815	μA	MAX

Electrical Characteristics

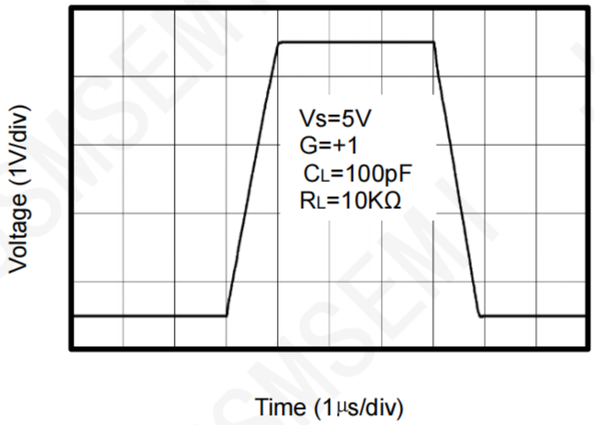
(At Vs=5V,T_A=+25℃,V_{CM}=V_S/2,RL=600Ω,unless otherwise ngted)

PARAMETER	CONDITIONS	JSM8632XS/TR TYP +25℃	MIN/MAX OVER TEMPERATURE +25℃	0℃ to 70℃	-40℃ to 85℃	-40℃to 125℃	UNITS	MIN / MAX
DYNAMIC PERFORMANCE								
Gain-Bandwidth Product (GBP)	R_L = 10kΩ, C_L = 100pF	6					MHz	TYP
Phase Margin (φ_O)	R_L = 10kΩ, C_L = 100pF	53					Degrees	TYP
Full Power Bandwidth (BWP)	＜1% distortion, R_L = 600Ω	250					kHz	TYP
Slew Rate (SR)	G = +1, 2V Step, R_L = 10kΩ	4.2					V/µs	TYP
Settling Time to 0.1% (t_S)	G = +1, 2V Step, R_L = 600Ω	0.4					µs	TYP
Overload Recovery Time	V_{IN} ·Gain = VS, R_L = 600Ω	2.5					µs	TYP
NOISE PERFORMANCE								
Voltage Noise Density (e_n)	f = 1kHz	13					$nV/\sqrt{Hz}$	TYP
	f = 10kHz	9.5					$nV/\sqrt{Hz}$	TYP

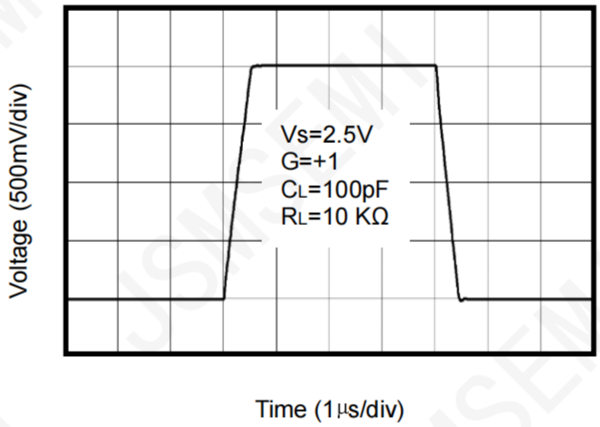
Typical Performance characteristics

(At Vs=5V, T_A = +25℃, V_{CM} = Vs/2, R_L = 600Ω, unless otherwise noted.)

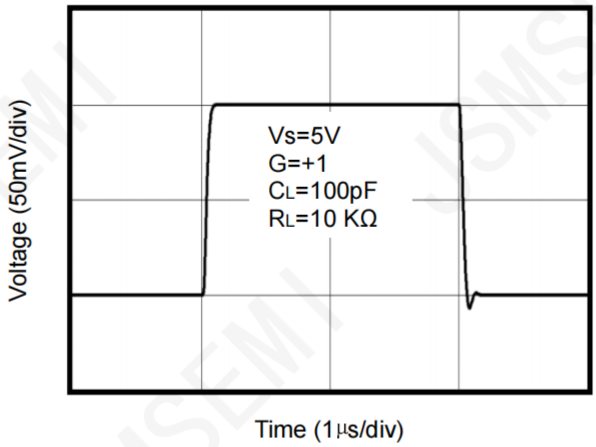
Large-Signal Step Response



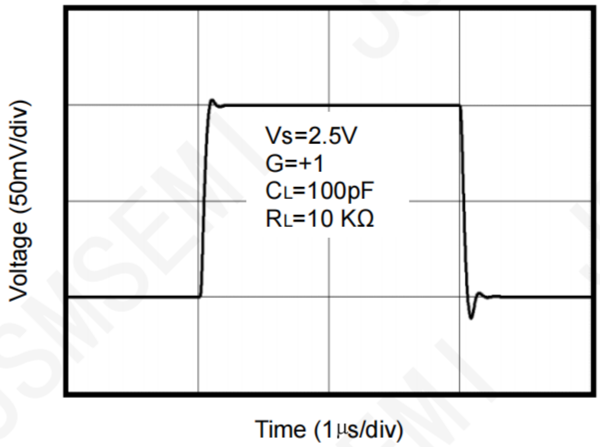
Large-Signal Step Response



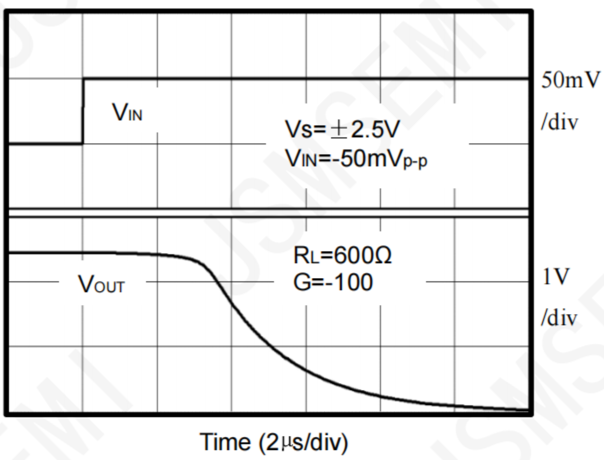
Small-Signal Step Response



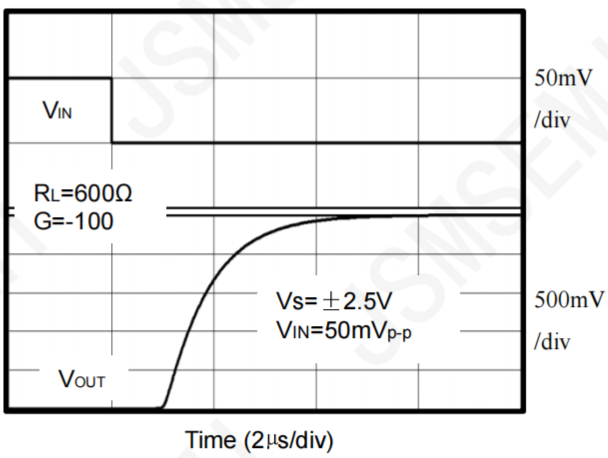
Small-Signal Step Response



Positive Overload Recovery



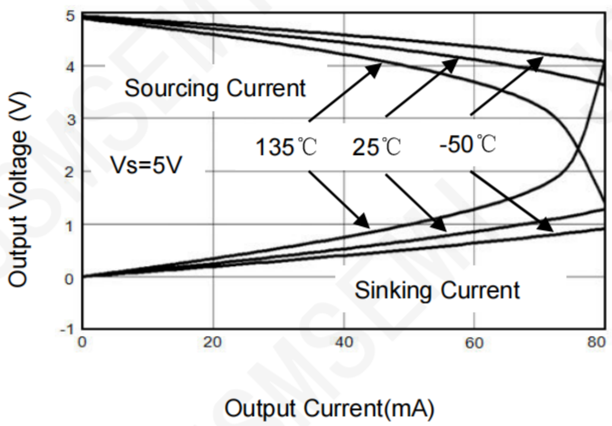
Negative Overload Recovery



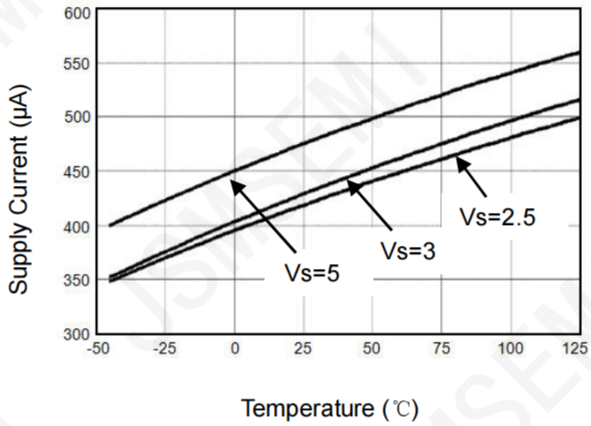
Typical Performance characteristics

(At Vs=5V, T_A = +25℃, V_{CM} = Vs/2, R_L = 600Ω, unless otherwise noted.)

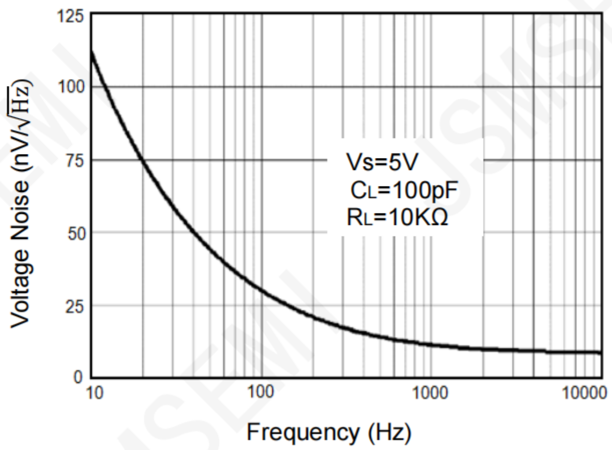
Output Voltage Swing vs.Output Current



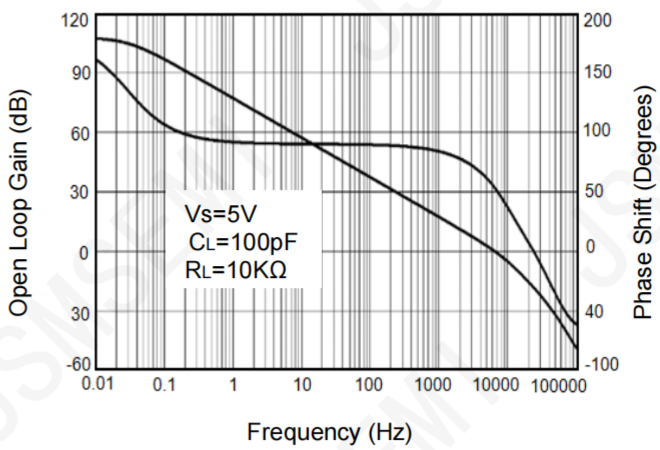
Supply Current vs. Temperature



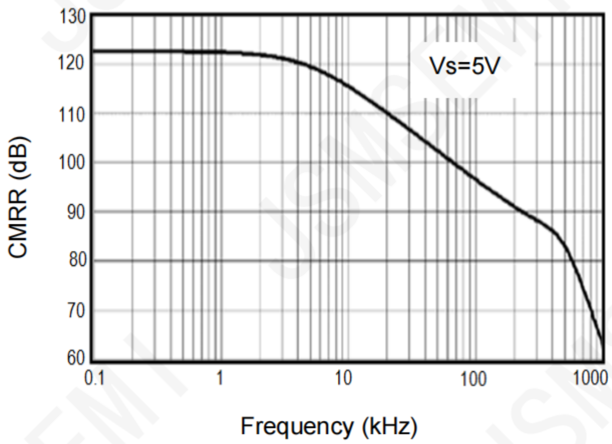
Input Voltage Noise Spectral Density vs. Frequency



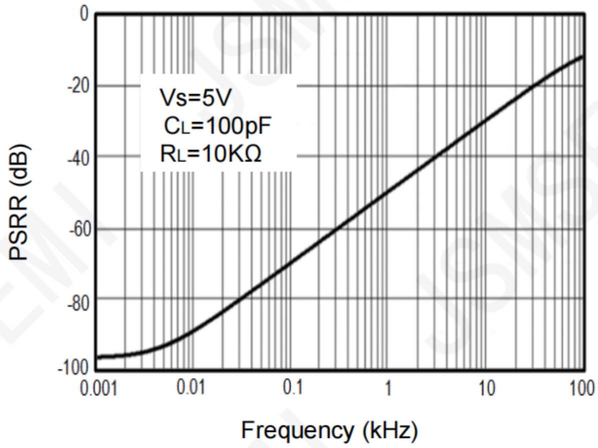
Open Loop Gain, Phase Shift vs. Frequency



CMRR vs. Frequency



PSRR vs. Frequency



Application Note Size

JSM8632XS/TR series op amps are unity-gain stable and suitable for a wide range of general-purpose applications.

The small footprints of the JSM8632XS/TR series packages save space on printed circuit boards and enable the design of smaller electronic products.

Power Supply Bypassing and Board Layout JSM8632XS/TR series operates from a single 2.1V to 5.5V supply or dual ±1.05V to ±2.75V supplies. For best performance, a 0.1μF ceramic capacitor should be placed close to the V_{DD} pin in single supply operation. For dual supply operation, both V_{DD}

and V_{SS} supplies should be bypassed to ground with separate 0.1μF ceramic capacitors.

Low Supply Current

The low supply current (typical 470uA per channel) of JSM8632XS/TR series will help to maximize battery life. They are ideal for battery powered systems

Operating Voltage

JSM8632XS/TR series operate under wide input supply voltage (2.1V to 5.5V). In addition, all temperature specifications apply from -40 °C to +125 °C. Most behavior remains unchanged throughout the full operating voltage range. These guarantees ensure operation throughout the single Li-Ion battery lifetime

Rail-to-Rail Input

The input common-mode range of JSM8632XS/TR series extends 100mV beyond the supply rails (V_{SS}-0.1V to V_{DD}+0.1V). This is achieved by using complementary input stage. For normal operation, inputs should be limited to this range.

Rail-to-Rail Output

Rail-to-Rail output swing provides maximum possible dynamic range at the output. This is particularly important when operating in low supply voltages. The output voltage of JSM8632XS/TR series can typically swing to less than 2mV from supply rail in light resistive loads (>100kΩ), and 60mV of supply rail in moderate resistive loads (10kΩ).

Capacitive Load Tolerance

The JSM8632XS/TR family is optimized for bandwidth and speed, not for driving capacitive loads. Output capacitance will create a pole in the amplifier's feedback path, leading to excessive peaking and potential oscillation. If dealing with load capacitance is a requirement of the application, the two strategies to consider are (1) using a small resistor in series with the amplifier's output and the load capacitance and (2) reducing the bandwidth of the amplifier's feedback loop by increasing the overall noise gain.Figure 2. shows a unity gain follower using the series resistor strategy. The resistor isolates the output from the capacitance and, more importantly, creates a zero in the feedback path that compensates for the pole created by the output capacitance.

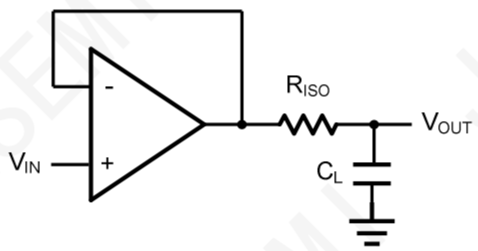


Figure 2. Indirectly Driving a Capacitive Load Using Isolation Resistor

The bigger the R_{ISO} resistor value, the more stable V_{OUT} will be. However, if there is a resistive load R_L in parallel with the capacitive load, a voltage divider (proportional to R_{ISO}/R_L) is formed, this will result in a gain error.

The circuit in Figure 3 is an improvement to the one in Figure 2. R_F provides the DC accuracy by feed-forward the V_{IN} to R_L. C_F and R_{ISO} serve to counteract the loss of phase margin by feeding the high frequency component of the output signal back to the amplifier's inverting input, thereby preserving the phase margin in the overall feedback loop. Capacitive drive can be increased by increasing the value of C_F. This in turn will slow down the pulse response.

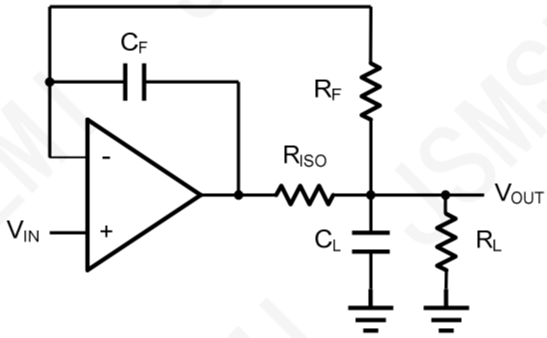


Figure 3. Indirectly Driving a Capacitive Load with DC Accuracy

Typical Application Circuits

Differential amplifier

The differential amplifier allows the subtraction of two input voltages or cancellation of a signal common the two inputs. It is useful as a computational amplifier in making a differential to single-end conversion or in rejecting a common mode signal. Figure 4. shown the differential amplifier using JSM8632XS/TR.

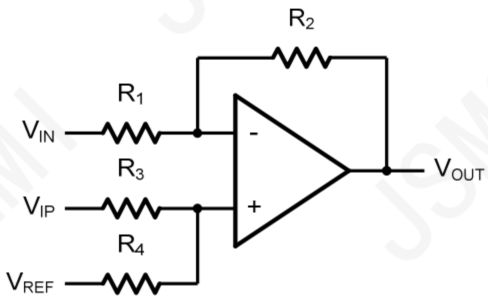


Figure 4. Differential Amplifier

$$V_{OUT}=\left(\frac{R_1+R_2}{R_3+R_4}\right)\frac{R_4}{R_1}V_{IN}-\frac{R_2}{R_1}V_{IP}+\left(\frac{R_1+R_2}{R_3+R_4}\right)\frac{R_3}{R_1}V_{REF}$$

If the resistor ratios are equal (i.e. $R_1=R_3$ and $R_2=R_4$), then

$$V_{OUT}=\frac{R_2}{R_1}(V_{IP}-V_{IN})+V_{REF}$$

Low Pass Active Filter

The low pass active filter is shown in Figure 5. The DC gain is defined by $-R_2/R_1$. The filter has a -20dB/decade roll-off after its corner frequency $f_C=1/(2\pi R_3C_1)$.

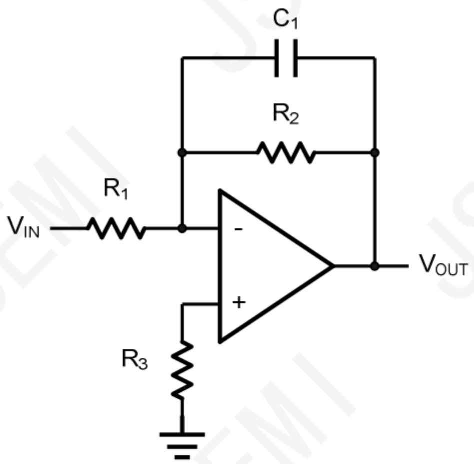


Figure 5. Low Pass Active Filter

Instrumentation Amplifier

The triple JSM8632XS/TR can be used to build a three-op-amp instrumentation amplifier as shown in Figure 6. The amplifier in Figure 6 is a high input impedance differential amplifier with gain of R_2/R_1. The two differential voltage followers assure the high input impedance of the amplifier.

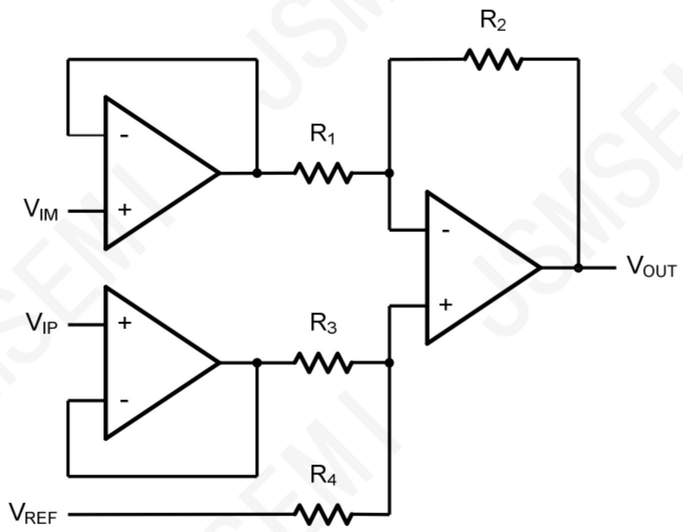
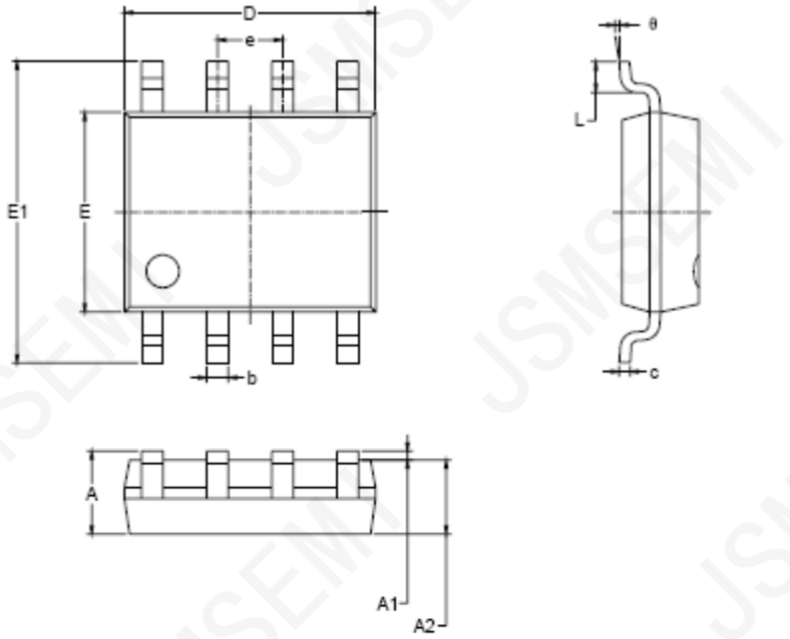


Figure 6. Instrument Amplifier

SOP-8



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MIN	MAX	MIN	MAX
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.006	0.010
D	4.700	5.100	0.185	0.200
E	3.800	4.000	0.150	0.157
E1	5.800	6.200	0.228	0.244
e	1.27 BSC		0.050 BSC	
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°