# MSKSEMI 美森科







TVC



TSS



MOV



GDT



DIEL

## MS2N7002M3

Product specification





#### **Features**

- High Density Cell Design for Low RDS(ON)
- Voltage Controlled Small Signal Switch
- Small Outline Surface Mount Package
- RoHS compliant / Green EMC

#### **Reference News**

- Notebook
- Smartphone
- Battery Protection
- Hand-held Instruments

BVDSS	RDSON	ID
60V	2.2Ω	0.34A

#### **Reference News**

PACKAGE OUTLINE	PIN Configuration	Marking
D	G	RK
SOT-723		

#### MAXIMUM RATINGS (Ta=25℃ unless otherwise noted)

Symbol	Parameter	Value	Unit
V <sub>DS</sub>	Drain-Source Voltage	60	V
V <sub>GS</sub>	Gate-Source Voltage	±20	V
VEBO	Emitter-Base Voltage	6	V
ΙD	Drain Current-Continuous	0.34	Α
Po	Power Dissipation	0.15	W
R. JA	Thermal Resistance From Junction To Ambient	833	°C/W
Tj	Junction Temperature	150	$^{\circ}$
Tstg	Storage Temperature	-55~+150	$^{\circ}$

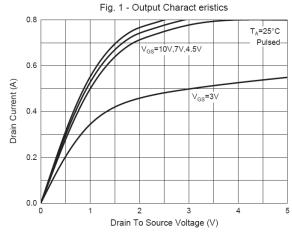


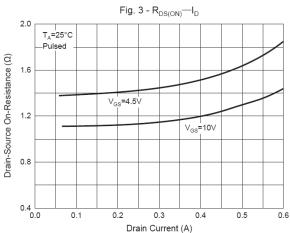
## **ELECTRICAL CHARACTERISTICS @ 25°C Unless Otherwise Specified**

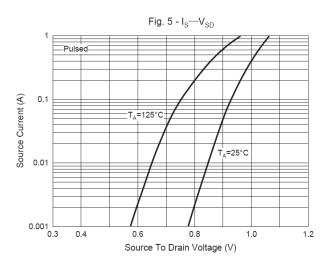
Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	60			V
$V_{\text{GS(th)}}$	Gate-Threshold Voltage	V <sub>DS</sub> =VGS, I <sub>D</sub> =250μA	1.0	1.4	2.5	V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> =48V, V <sub>GS</sub> =0V			1.0	uA
	O to Porto Lordon	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V			±10	μΑ
$I_{GSS}$	Gate-Body Leakage Current	V <sub>GS</sub> =±10V, V <sub>DS</sub> =0V			±200	nA
		V <sub>GS</sub> =±5V, V <sub>DS</sub> =0V			±100	nA
	R <sub>DS(on)</sub> Drain-Source On-Resistance	V <sub>GS</sub> =10V, I <sub>D</sub> =500mA		1.3	4.0	Ω
$R_{DS(on)}$		V <sub>GS</sub> =4.5V,I <sub>D</sub> =200mA		1.4	4.5	
$Q_r$	Recovered Charge	$V_{GS}$ =0V, $I_{S}$ =300mA, $V_{R}$ =25V dl/dt=-100A/ $\mu$ s		30		nC
Dynamic (	Characteristics					
$C_{iss}$	Input Capacitance				40	
$C_{oss}$	Output Capacitance	V <sub>DS</sub> =10V,V <sub>GS</sub> =0V, f=1MHz			30	pF
$C_{rss}$	Reverse Transfer Capacitance				10	
Switching	Characteristics	I	1	-	1	
t <sub>d(on)</sub>	Turn-on Delay Time	$R_{GS} = 50 \text{K}, R_{GEN} = 25 \Omega$			10	
t <sub>d(off)</sub>	Turn-off Delay Time				15	nS
t <sub>rr</sub>	Reverse Recovery Time			30		110
Source-Drain Diode Characteristics						
$V_{SD}$	Diode Forward Voltage	V <sub>GS</sub> =0V, I <sub>S</sub> =200mA		0.97	1.5	V

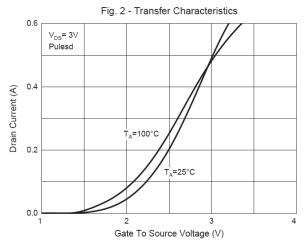


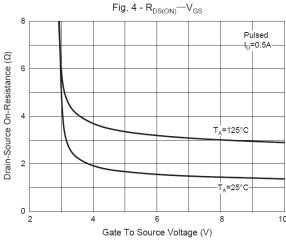
#### **Curve Characteristics**

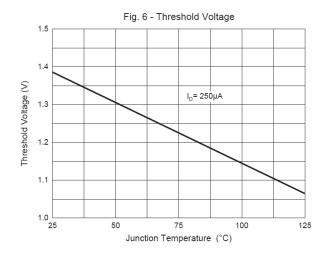






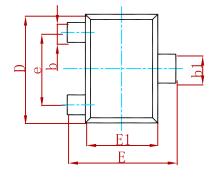


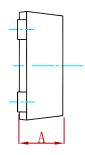


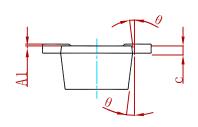




#### **PACKAGEMECHANICALDATA**

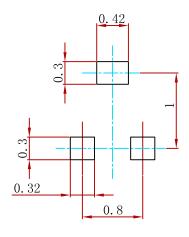






Cumbal	Dimensions In Millimeters		Dimensions In Inches	
Symbol	Min.	Max.	Min.	Max.
Α	0.430	0.500	0.017	0.020
A1	0.000	0.050	0.000	0.002
b	0.170	0.270	0.007	0.011
b1	0.270	0.370	0.011	0.015
С	0.080	0.150	0.003	0.006
D	1.150	1.250	0.045	0.049
E	1.150	1.250	0.045	0.049
E1	0.750	0.850	0.030	0.033
е	0.800	OTYP.	0.03	ITYP.
θ	7° F	REF.	7° F	REF.

## **Suggested Pad Layout**



#### Note:

- 1.Controlling dimension:in millimeters.
- 2.General tolerance:± 0.05mm.
- 3. The pad layout is for reference purposes only.

### **REELSPECIFICATION**

P/N	PKG	QTY
MS2N7002M3	SOT-723	8000



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