

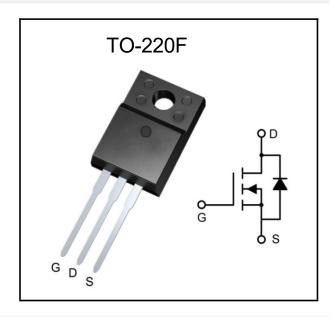
N-channel Super Junction MOSFET

Features Package

- V_{(BR)DSS}=650V, I_D=20A
- $R_{DS(on)}@V_{GS}=10V$, $TYP=160m\Omega$
- · 100% avalanche tested
- · Fast Switching
- · Low Gate Charge
- Low R_{DS(on)} per chip area(Low FOM)

Applications

- Switch Mode Power Supply(SMPS)
- · TV and Lighting



General Description

BMF65R190 is a N-channel power MOSFET designed according to super junction technology. This device has very low on-resistance and hard ruggedness for switching applications. It's very low conduction loss and fast switching can make applications more efficient and faster.

Absolute Maximum Ratings (TJ=25 ℃ unless otherwise specified)

Symbol	Parameter		Value	Units
V _{DS}	Drain-Source Voltage		650	V
V _{GS}	Gate - Source Voltage	±30	V	
		T _C =25°C	20	•
I _D	Drain Current	T _C =100°C	13	Α
I _{DM}	Drain Current-Pulse ¹	T _C =25°C	60	
E _{AS}	Avalanche Energy ²		320	mJ
P _D	Power Dissipation	T _C =25℃	34	W
T _J , T _{stg}	Operating Junction and Storage Tempera	-55 to +150	င	
R _{⊝JC}	Thermal Resistance, Junction-to-	Case	3.7	9C AA/
R _{⊝JA}	Thermal Resistance, Junction-to-A	80	°C/W	









N-channel Super Junction MOSFET

Electrical Characteristics (T_J=25℃ unless otherwise noted)

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Units				
Off Characteristics										
Drain-Source Breakdown Voltage	BV _{DSS}	$V_{GS} = 0V$, $I_D = 250uA$	650	_	_	V				
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =650V, V _{GS} =0V	_	_	1	uA				
Gate- Source Leakage Current	I _{GSS}	$V_{GS} = \pm 30 V, V_{DS} = 0 V$	_	_	±100	nA				
On Characteristics										
Gate Threshold Voltage	V _{GS(TH)}	$V_{GS} = V_{DS}$, I_{DS} =250uA	2	3	4	V				
Static Drain-source On Resistance	R _{DS(on)}	VGS =10V, ID =10A	_	160	190	mΩ				
	Dynam	ic Characteristics								
Gate Resistance	R _g	f = 1MHz	_	3	_	Ω				
Input capacitance	C _{iss}	V _{DS} =50V,	_	1480	_	pF				
Output capacitance	Coss	V _{GS} =0V,	_	102	_					
Reverse transfer capacitance	C _{rss}	f = 400kHz	_	2.5	_					
	Switchi	ng Characteristics								
Turn-on delay time	T _{d(on)}	V _{DD} =325V,	_	24	_					
Turn-on Rise time	Tr	V _{GS} =10V,	_	35	_	no				
Turn -Off Delay Time	$T_{d(off)}$	I _D =20A,	_	110	_	ns				
Turn -Off Fall time	T _f	$R_G = 25\Omega$	_	28	_					
Gate to Source Charge	Q_{gs}	V _{DS} =520V,	_	7.2	_					
Gate to Drain Charge	Q _{gd}	V _{GS} =10V,	_	16	_	nC				
Gate to Drain Charge	Qg	I _D =20A	_	36	_					
	Source - Drain	n Diode Characteristics								
Diode Forward Voltage	V _{SD}	V _{GS} =0V, I _S =20A	_	_	1.4	V				
Reverse Recovery Time	t _{rr}	1 = 20 \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	_	345	_	ns				
Reverse Recovery Current	I _{rr}	I _{SD} =20A, d _I /dt=100A/us	_	35.5	_	Α				
Reverse Recovery Charge	Q _{rr}		_	5	_	uC				

Notes:

- (1) Calculated continuous current based on maximum allowable junction temperature. Note that current limitations arising from heating of the device leads may occur with some lead mounting arrangements.
- (2) L=10mH, $\mathbf{R}_{\mathbf{G}}$ =25Ω, Start $\mathbf{T}_{\mathbf{J}}$ =25°C.





Typical Performance Characteristics

Figure 1: Output Characteristics

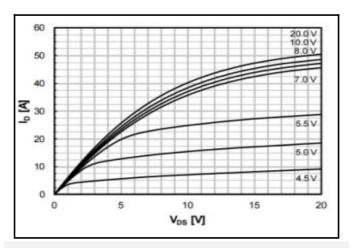


Figure 3: On-state Resistance

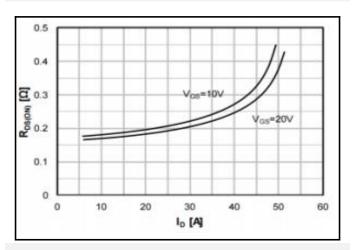


Figure 5: Transfer Characteristics

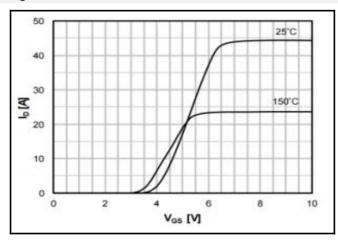


Figure 2: Capacitances

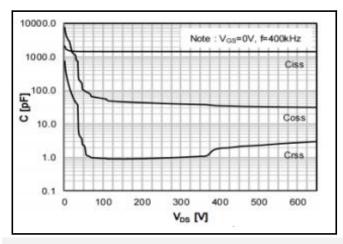


Figure 4: On-state Resistance with Temperature

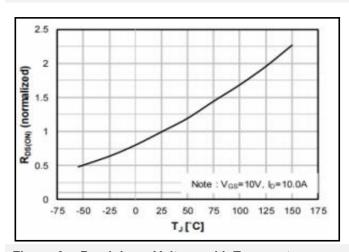
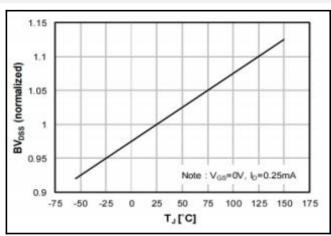


Figure 6: Breakdown Voltage with Temperature



BORN INTERTECHNOLOGY, INC. ALL RIGHTS RESERVED



Typical Performance Characteristics

Figure 7: Gate Charge

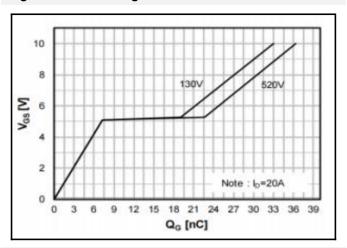


Figure 9: Maximum Transient Thermal Characteristics

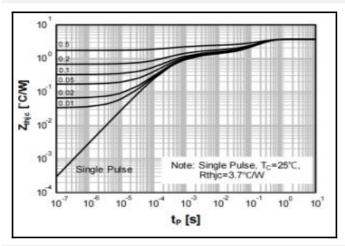


Figure 11: Power Dissipation

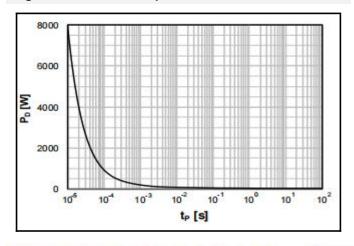


Figure 8: Maximum Drain Current

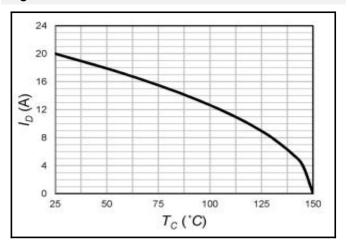


Figure 10: Body Diode Characteristics

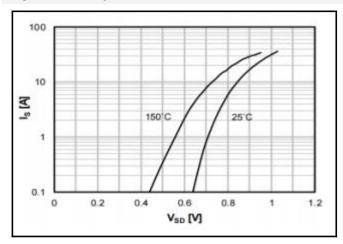
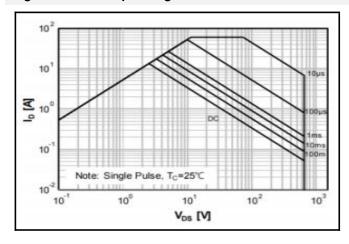


Figure 12: Safe Operating Area



BORN INTERTECHNOLOGY, INC. ALL RIGHTS RESERVED







N-channel Super Junction MOSFET

Test Circuit and Waveform

Figure 13: Resistive Switching Test Circuit and Waveforms

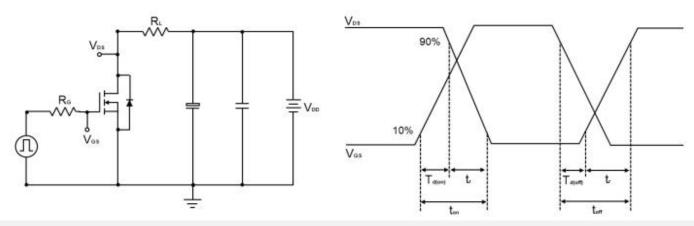


Figure 14: Diode Reverse Recovery Test Circuit and Waveforms

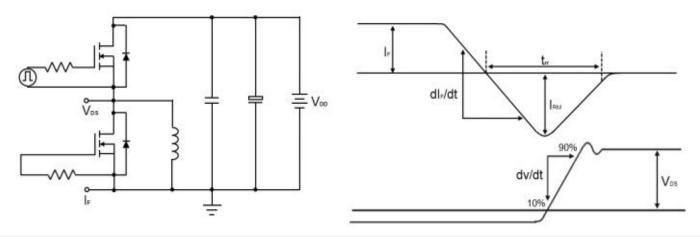
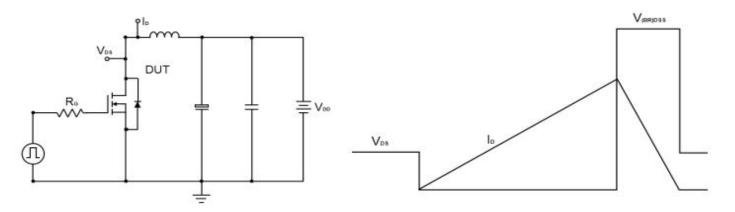


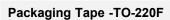
Figure 15: Unclamped Inductive Switching Test Circuit and Waveforms

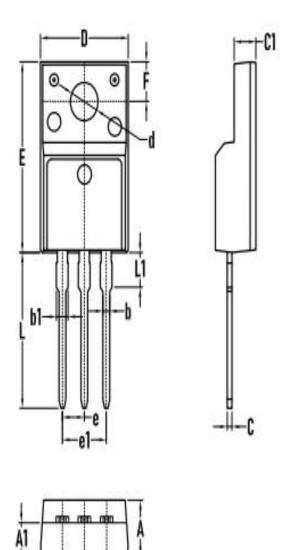


BORN INTERTECHNOLOGY, INC. ALL RIGHTS RESERVED









0	Millimeters			
Symbol	MIN.	TYP.	MAX.	
Α	4.30	4.60	4.80	
A1	2.70	2.80	2.90	
b	0.70	0.80	0.90	
b1	1.20	1.30	1.40	
С	0.40	0.50	0.60	
C1	2.40	2.60	2.80	
D	9.90	10.00	10.20	
E	15.20	15.60	16.00	
е	2.44	2.54	2.64	
e1	4.88	5.08	5.26	
F	3.00	3.30 3.60		
L	12.70	13.20	3.20 13.70	
L1	2.70	2.90	3.10	
d	3.10	3.20	3.30	



