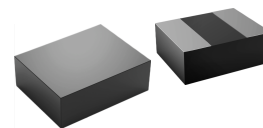


MTQH404030S Series

Molded Power Inductors

FEATURES

- Metal material for large current and low loss
- Vinyl thermal spray, better surface compactness
- Closed magnetic circuit design reduces leakage flux
- Operate temperature range $-55^{\circ}\text{C} \sim +125^{\circ}\text{C}$ (Including self temp. rise)
- RoHS compliant



APPLICATIONS

- Smart phone, pad
- Notebooks, VR, AR
- Portable gaming devices, Smart wear, Wi-Fi module
- Baseband power supply, Amplifier, Power management, Module power supply, Camera power manageme.

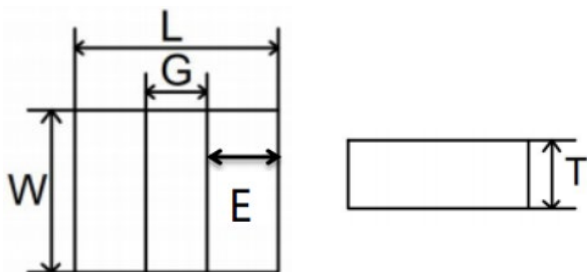
Explanation of Part Number

MTQH 404030 S 2R2 M B TA

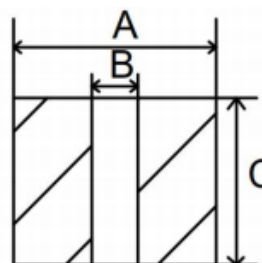
1 2 3 4 5 6 7 8

- ◆ 1:Series Name: Molding power inductors
- ◆ 2:Size Code: L*W*T
- ◆ 3: Material Code:S Type
- ◆ 4:Initial inductance value: 2R2 = 2.2uH
- ◆ 5:Tolerance of Inductance:M: $\pm 20\%$
- ◆ 6:Coating color:B=Black,G=Gray
- ◆ 7:Packing:Tape Carrier Package
- ◆ 8:Internal Code: A,B

Dimensions: [mm]



Land Pattern: [mm]



Series	L	G/Typ	W	E	T	A/Typ	B/Typ	C/Typ
MTQH404030S	4.1 ± 0.2	1.4	4.1 ± 0.2	1.35 ± 0.2	3.00Max.	4.10	1.30	4.10

Electrical Properties:

P/N	L0(μ H) @ (0A) 1MHz	Rdc(m Ω)		Heat rating current Irms(A)		Saturation current Isat(A)	
		Typical	Max	Typical	Max	Typical	Max
MTQH404030SR33M□T	0.33	4.5	6.0	16	15	25	23
MTQH404030SR47M□T	0.47	5.5	7.0	15	14	23	21
MTQH404030SR56M□T	0.56	6.0	7.5	15	14	22	20
MTQH404030SR68M□T	0.68	8.3	10	9.5	8.0	17	15
MTQH404030S1R0M□T	1.0	10	12	10	9.0	15.5	14
MTQH404030S1R5M□T	1.5	15	18	6.5	6.0	12.5	11
MTQH404030S2R2M□T	2.2	19	22	9.0	8.5	10.5	9.5
MTQH404030S2R2M□TA	2.2	22	25	9.2	8.6	11	10
MTQH404030S3R3M□T	3.3	30	35	5.3	4.8	8.5	7.5
MTQH404030S4R7M□T	4.7	41	46	4.3	4.0	7.0	6.0
MTQH404030S4R7M□TA	4.7	36	42	6.0	5.5	7.8	6.8
MTQH404030S5R6M□T	5.6	41	48	5.5	5.0	7.0	6.2
MTQH404030S6R8M□T	6.8	51	62	4.2	3.8	6.3	5.1
MTQH404030S8R2M□T	8.2	90	102	3.9	3.5	5.2	4.8
MTQH404030S100M□T	10.0	92	110	3.7	3.3	4.9	4.5
MTQH404030S220M□T	22.0	190	220	2.5	2.2	3.4	3.0

Test remarks

Note 1.: All test data is referenced to 25 °C ambient.

Note 2.: Test Condition: 1MHz, 1.0Vrms.

Note 3.: Irms:DC current (A) that will cause an approximate ΔT of 40 °C.

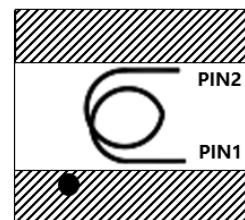
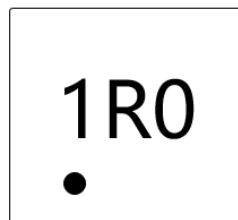
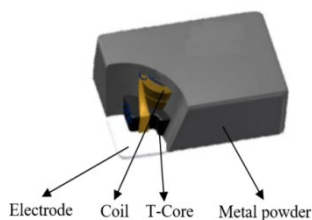
Note 4.: Isat:DC current (A) that will cause L0 to drop approximately 30%.

Note 5.: Operating Temperature Range -55°C to + 125°C.

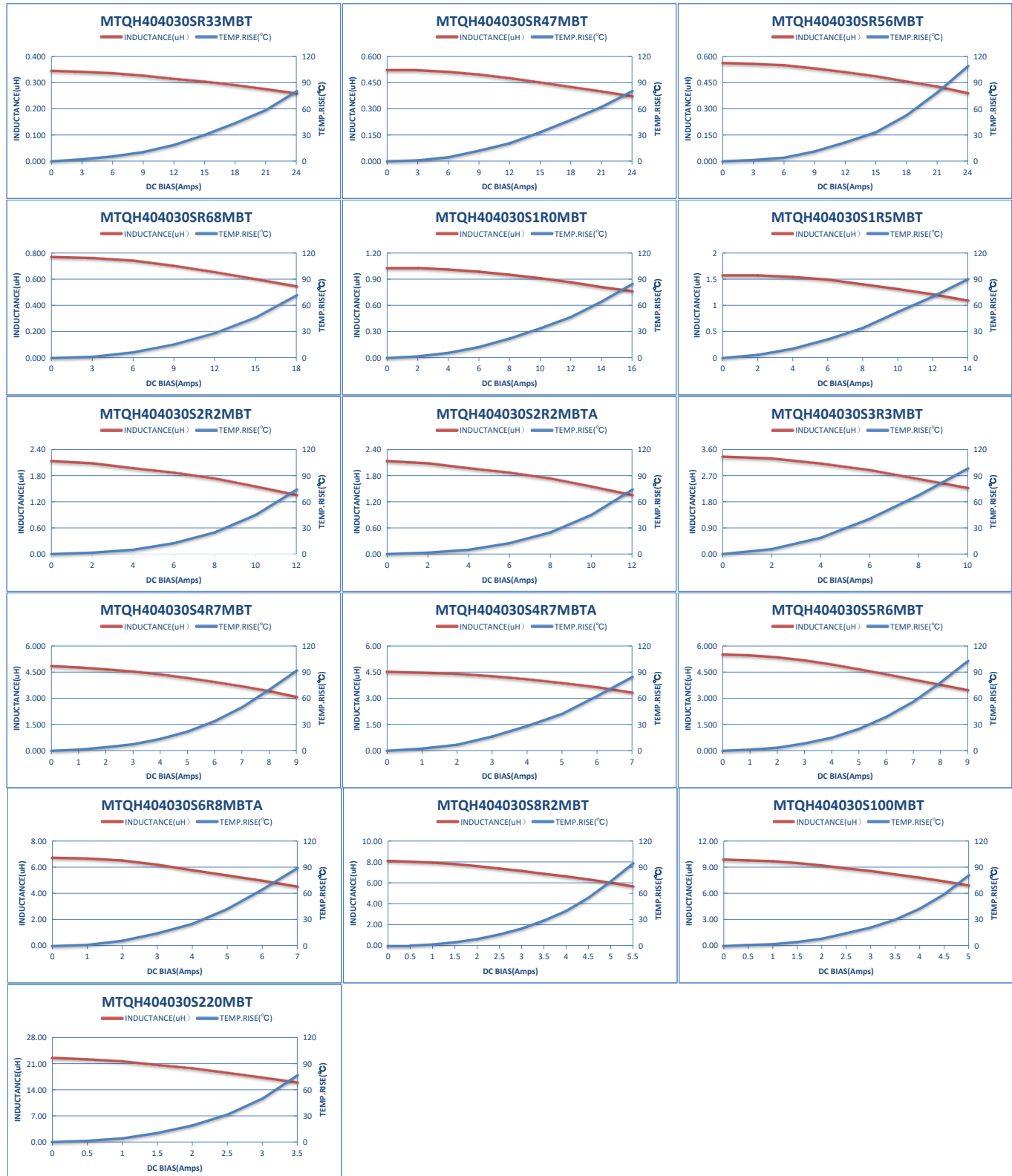
Note 6.: The part temperature (ambient + temp rise) should not exceed 125 under °C the worst case operating conditions. Circuit design, component placement, PCB trace size and thickness, airflow and other cooling provision all affect the part temperature. Part temperature should be verified in the end application.

Note 7.: The rated current as listed is either the saturation current or the heating current depending on which value is lower.

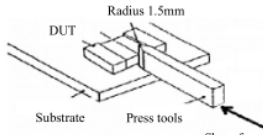
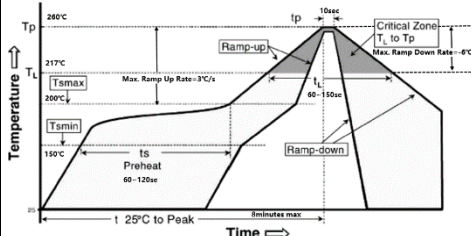
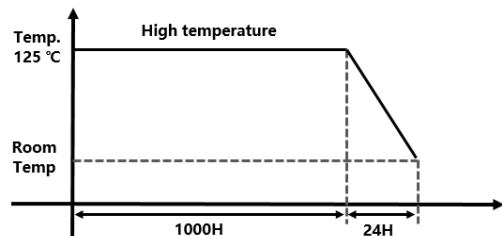
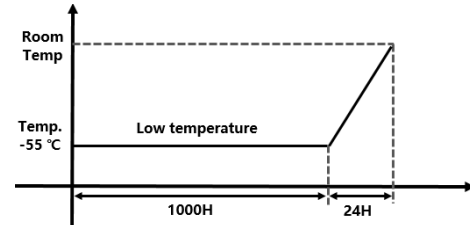
Structure

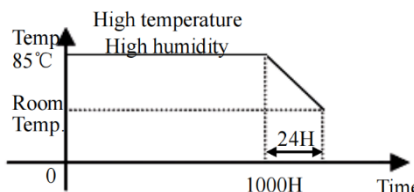
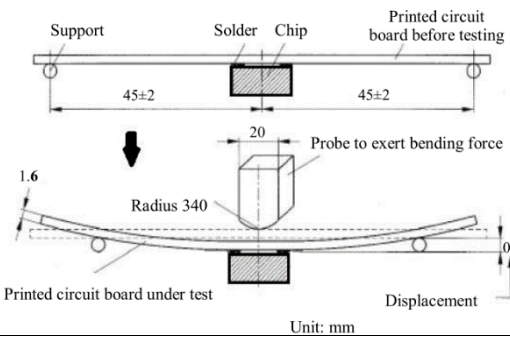


Current Characteristic



Reliability

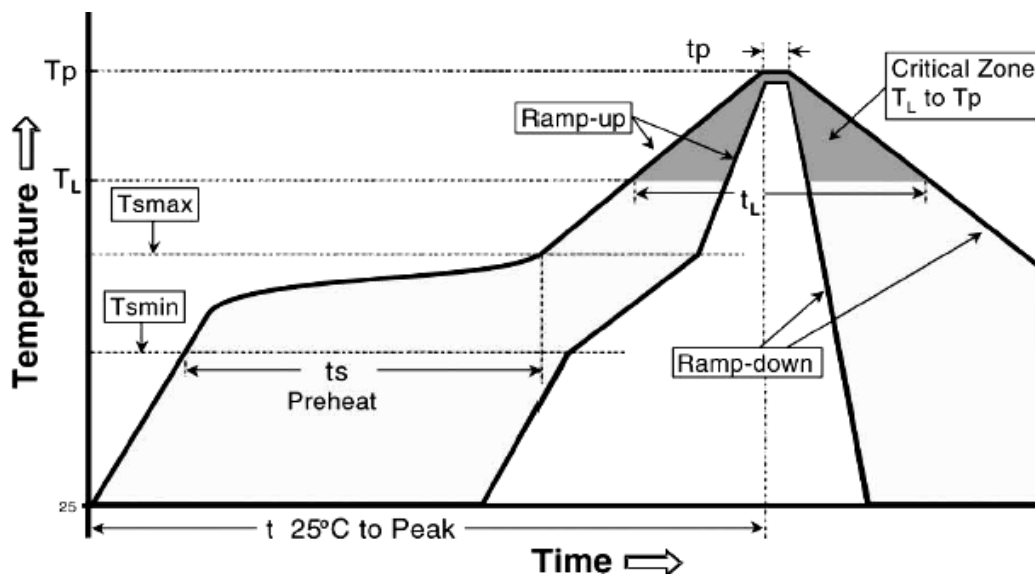
No.	Item	Requirements	Test Methods and Remarks	Reference	Sample Size								
1	Solderability	(1) No case deformation or change in appearance. (2) Terminal area must have 95% min. Solder coverage.	①Temperature:245± 5°C. ②Solder Composition: Sn/Ag3.0/Cu0.5(Pb-Free). ③Sample immersion tin furnace 5 ±0.5s.	AEC-Q200 (J-STD 002)	32								
2	Adhesion of teral electrode	(1)Strong bond between the pad and the core, without come off PCB.	①Preconditioning: 245°CReflow 3 times ②Inductors shall be subjected to (260+0/-5°C.)°C for (10±5)s Soldering in the base whit 0.3mm solder. ③Aplombelectrode way plus tax 12 N for (10±1) seconds. <table border="1"><tr><td>series</td><td>"X" N</td></tr><tr><td>1008</td><td>6</td></tr><tr><td>1210~1608</td><td>8</td></tr><tr><td>2012</td><td>12</td></tr></table> 	series	"X" N	1008	6	1210~1608	8	2012	12	AEC-Q200 (AEC-Q200-006)	32
series	"X" N												
1008	6												
1210~1608	8												
2012	12												
3	Reflow test	(1) No physical damage. (2) ΔL0/L0 ≤10%	① The peak temperature: 260+0/-5°C. ② Reflow:3times. ③ Temperature curve is as below 	AEC-Q200 (MIL-STD-202 Method 210)	32								
4	High temperature	(1) No physical damage. (2) ΔL0/L0 ≤10%	① Preconditioning: Bake at 125+5°C for 24±0.5H, 245°CReflow 3 times ②Temperature: 125±2°C. ③ Time : 1000 hours. ④Measurement at 24±4 hours after test conclusion 	AEC-Q200 (MIL-STD -202 Method 108)	77								
5	Low temperature	(1) No physical damage. (2) ΔL0/L0 ≤10%	①Preconditioning: Bake at 125+5°C for 24±0.5H, 245°CReflow 3 times ②Temperature: -55±2°C. ③Time : 1000 hours. ④ Measurement at 24±4 hours after test conclusion 	JESD22-A119A	77								
6	Thermal shock	(1) No physical damage. (2) ΔL0/L0 ≤10%	①Preconditioning: Bake at 125+5°C for 24±0.5H, 245°CReflow 3 times ②Repeat 500 cycle as follow : (-55±2 °C ,30±3minutes) 、(Room temperature, 5 minutes)、(+125±2 °C ,30±3minutes)、(Room temperature, 5 minutes) ③Measurement at 24±4 hours after test conclusion	MIL-STD -202 Method 107	77								

No.	Item	Requirements	Test Methods and Remarks	Reference	Sample Size
7	Resistance to Soldering Heat	(1) No physical damage. (2) $ \Delta L_0/L_0 \leq 10\%$	① Solder Composition: Sn/Ag3.0/Cu0.5(Pb-Free). ② Solder Temperature: 260±5°C. ③ Immersion Time: 10±1sec.	AEC-Q200 (MIL-STD-202 Method 210)	32
8	Static Humidity	(1) No physical damage. (2) $ \Delta L_0/L_0 \leq 10\%$	① Preconditioning: Bake at 125±5°C for 24±0.5H, 245°C Reflow 3 times ② 1000 hours, 85°C/85%RH. ③ Unpowered. ④ Measurement at 24±4 hours after test conclusion 	AEC-Q200 (MIL-STD-202 Method 103)	77
9	Board Flex	(1) No physical damage. (2) $ \Delta L_0/L_0 \leq 10\%$	① Preconditioning: 245°C Reflow 3 times ② Part mounted on a 100mm*40mm FR4 PCB board, which is 1.6±0.2 mm thick and as a Layer-thickness 35 μm ± 10 μm. ③ Bending speed is 1mm/s. ④ Keeping the P.C Board 2 mm minimum for 60 seconds. 	AEC-Q200 (AEC-Q200-005)	30
10	Vibration	(1) No physical damage. (2) $ \Delta L_0/L_0 \leq 10\%$	① Preconditioning: 245°C Reflow 3 times ② Frequency range : 10~2000Hz. ③ Amplitude: 1.5mm or 20g. ④ Sweep time and duration: 10~2000~10Hz for 20 minutes. ⑤ Each four hours in X,Y,Z direction, 12hours in total.	AEC-Q200 (MIL-STD-202 Method 204)	32
11	Mechanical Shock	(1) No physical damage. (2) $ \Delta L_0/L_0 \leq 10\%$	① Preconditioning: 245°C Reflow 3 times ② Peak acceleration: 100G/S ③ Duration of pulse: 6ms ④ 3times in each of 6(±X, ±Y, ±Z) axes.	AEC-Q200 (MIL-STD-202 Method 213)	32
12	Loading at High Temperature	(1) No physical damage. (2) $ \Delta L_0/L_0 \leq 10\%$	① Preconditioning: Bake at 125±5°C for 24±0.5H, 245°C Reflow 3 times ② Temperature: 85±2°C. ③ Time : 1000 hours. ④ Applied Current : Rated current. ⑤ Measurement at 24±4 hours after test conclusion	AEC-Q200 (MIL-STD-202 Method 108)	77

Soldering Condition

(This is for recommendation, please customer perform adjustment according to actual application)

Recommend Reflow Soldering Profile : (solder : Sn96.5 / Ag3 / Cu0.5)



Profile Feature	Lead (Pb)-Free solder
Preheat:	
Temperature Min (Ts _{min})	150°C
Temperature Max (Ts _{max})	200°C
Time (Ts _{min} to Ts _{max}) (ts)	60 -120 seconds
Average ramp-up rate: (Ts max to Tp)	3°C / second max.
Time maintained above :	
Temperature (TL)	217°C
Time (tL)	60-150 seconds
Peak Temperature (Tp)	260°C
Time within $+0^{\circ}\text{C}$ to -5°C of actual peak Temperature (tp) ²	10 seconds
Ramp-down Rate	6°C/second max.
Time 25°C to Peak Temperature	8minutes max.

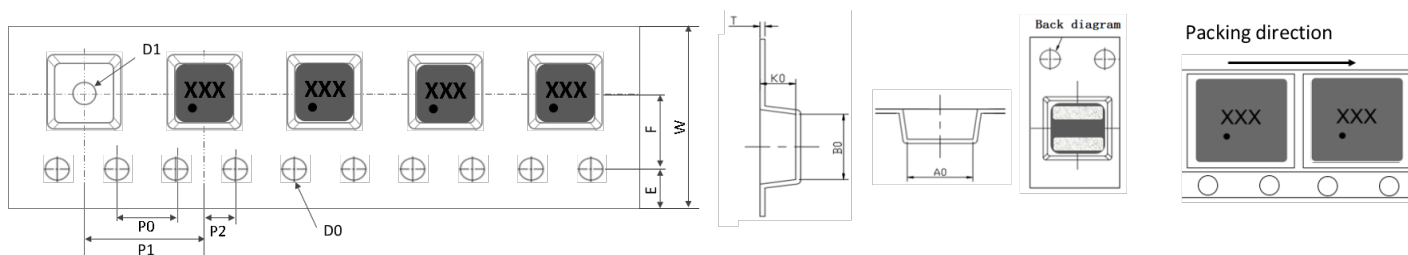
Allowed Re-flow times : 2 times

Remark : To avoid discoloration phenomena of chip on terminal electrodes, please use N₂ Re-flow furnace .

Packing

1. Dimension of plastic taping: (Unit: mm)

The following dimensions are related to the actual fit of the machine, for reference only.



Series	W	A0	B0	D0	D1	E
公差	±0.30	±0.10	±0.10	+0.1/-0	±0.20	±0.10
404030	12.0	4.40	4.40	1.5	1.5	1.75

Series	F	K0	P0	P2	P1	T	包装 数量
公差	±0.10	±0.10	±0.10	±0.10	±0.10	±0.05	
404030	5.5	3.10	4.0	2.0	8.0	0.30	2K

2. Dimension of Reel : (Unit: mm)

Type	A	B	C
	±2.0	±2.0	±2.0
All	330	100	13.0

