



2.2 Ω Low Voltage SPDT Analog Switch

1 Features

- ◆ bandwidth: 300 MHz
- ◆ Supply range: +1.65 V to +5.5 V
- ◆ Low on-state resistance: 2.2 Ω @ 4.5 V
- ◆ Break-before-make switching
- ◆ Rail-to-Rail signal range
- ◆ TTL/CMOS compatible
- ◆ Operation temperature range: -40°C to +85°C
- ◆ Micro size packages: SC70-6

2 Application

- ◆ Cell phones
- ◆ PADS
- ◆ Portable instrumentation
- ◆ Battery powered communications
- ◆ Computer peripherals

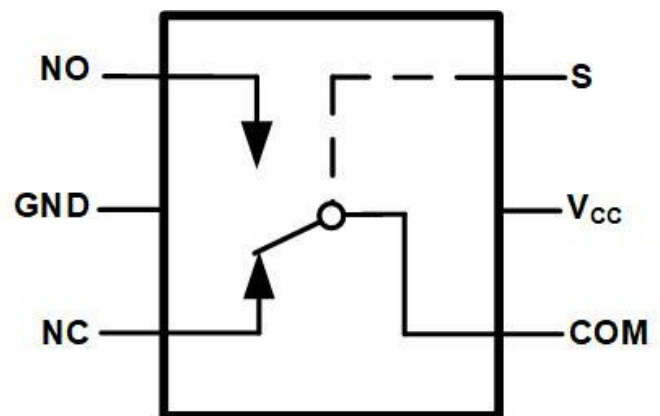
3 Description

The WB4157 is a single-pole double-throw (SPDT) analog switch that is designed to operate from 1.65V to 5.5V.

The WB4157 device can handle both analog and digital signals. It features high-bandwidth (300 MHz) and low on-resistance (2.2 Ω @ 4.5 V).

Applications: signal gating, chopping, modulation or demodulation (modem), and signal multiplexing for analog-to-digital and digital-to-analog conversion systems.

4 Circuit diagram





5 Device Pin and Packages

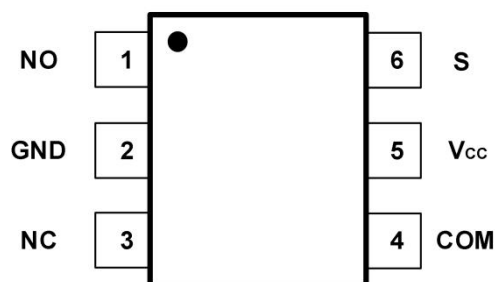


Table 5-1 Pin definition

Name	Pin	Description
NO	1	Data port
GND	2	Ground
NC	3	Data port (normally closed)
COM	4	Common output data port
VCC	5	Power input
S	6	Logic control

Table 5-2 Function Table

LOGIC	Function
0	NC connected to COM
1	NO connected to COM



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6 Voltage, Temperature, ESD and Thermal Ratings

6.1 Absolute Maximum Ratings⁽¹⁾

Parameters		Min.	Max.	Unit
V _{CC}	Supply voltage range	-0.3	6.0	V
V _{IN}	Input voltage	-0.3	6.0	V
	Analog,digital voltage range ⁽²⁾	-0.3	(V _{CC})+0.3	V
	Continuous current NO,NC or COM	-100	+100	mA
I _{PEAK}	Continuous channel current	-160	+160	mA
T _J	Junction temperature under bias	-65	150	°C
T _{stg}	Storage temperature range	-65	150	°C

(1)Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "Recommended Operating Conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

(2)The input and output negative-voltage ratings may be exceeded if the input and output current ratings are observed.

6.2 ESD Ratings

ESD			Value	Unit
V(ESD)	Electrostatic discharge	Human-Body Model (HBM) ⁽¹⁾	2.5K	V
		Charged-Device Model (CDM) ⁽²⁾	2K	V

(1) JEDEC document JEP155 states that 500-V HBM allows safe manufacturing with a standard ESD control process.

(2) JEDEC document JEP157 states that 250-V CDM allows safe manufacturing with a standard ESD control process.

6.3 Recommended Operating Conditions⁽¹⁾

Over operating free-air temperature range (unless otherwise noted)

Symbol	Parameter	Min	Max	Units
V _{CC}	Supply voltage	1.65	5.5	V
V _{IN}	Control input voltage	0	V _{CC}	V
	Switch input voltage	0	V _{CC}	V
V _{OUT}	Output voltage	0	V _{CC}	V
T _A	Ambient temperature	-40	85	°C

(1) All unused digital inputs of the device must be held at V_{CC} or GND to ensure proper device operation.

6.4 Thermal Information

Package Type	θ_{JA}	θ_{JC}	Unit
SC70-6	214.7	127.1	°C/W



7 Electrical Specifications

7.1 DC Electrical Characteristics

$V_{CC}=1.65V$ to $5.5V$, FULL= $-40^{\circ}C$ to $+125^{\circ}C$. Typical values are at $T_A=+25^{\circ}C$ (unless otherwise noted)

Parameter	Symbol	Conditions	VCC	Temp	Min	Typ	Max	Units
Analog Signal Range	V_{NO}, V_{NC}, V_{COM}				0		V_{CC}	V
On-Resistance	R_{ON}	V_{NO} or $V_{NC}=1.5V$ $I_{COM}=-100mA$	4.5V	$+25^{\circ}C$		2.2	4.5	Ω
			3V	$+25^{\circ}C$		3.8	7	
			2.3V	$+25^{\circ}C$		6.9	12	
			1.65V	$+25^{\circ}C$		12	20	
On-Resistance Match Between Channels	ΔR_{ON}	$0 \leq V_{NO}$ or $V_{NC} \leq V_{CC}$, $I_{COM}=-100mA$	4.5V	$+25^{\circ}C$		0.04		Ω
			3V	$+25^{\circ}C$		0.08		
			2.3V	$+25^{\circ}C$		0.01		
			1.65V	$+25^{\circ}C$		0.02		
On-Resistance Flatness	$R_{FLAT(ON)}$	$0 \leq V_{NO}$ or $V_{NC} \leq V_{CC}$, $I_{COM}=-100mA$	4.5V	$+25^{\circ}C$		0.6	1	Ω
			3V	$+25^{\circ}C$		1.7	3	
			2.3V	$+25^{\circ}C$		4.5	7	
			1.65V	$+25^{\circ}C$		8	15	
NC,NO Off Leakage Current	$I_{NC(OFF)}, I_{NO(OFF)}$	V_{NO} or $V_{NC}=1V, 4.5V$, $V_{COM}=4.5V, 1V$	5.5 V	Full			1	μA
NC,NO,COM On Leakage Current	$I_{NC(ON)}, I_{NO(ON)},$ $I_{COM(ON)}$	$V_{COM}=1V, 4.5V, V_{NO}$ or $V_{NC}=1V, 4.5V$, or floating	5.5 V	Full			1	μA
Input High Voltage	V_{INH}		3V	Full	1.7			V
Input Low Voltage	V_{INL}		3V	Full			0.5	V
Input Leakage Current	I_{IN}	$V_S=V_{CC}$ or 0	5.5 V	Full			1	μA
Power Supply Current	I_{CC}	$V_S=GND$ or V_{CC}	5.5 V	Full			1	μA



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7.2 Switch And AC Characteristics

$V_{CC}=1.65V$ to $5.5V$, FULL= $-40^{\circ}C$ to $+125^{\circ}C$. Typical values are at $T_A=+25^{\circ}C$ (unless otherwise noted)

Parameter	Symbol	Conditions	VCC	Temp	Min	Typ	Max	Units
Turn-On Time	t_{ON}	$V_{COM}=V_{CC}$, $R_L=300\Omega$, $C_L=35pF$	5V 3.3V	+25°C	6 11			ns
Turn-Off Time	t_{OFF}	$V_{COM}=V_{CC}$, $R_L=300\Omega$, $C_L=35pF$	5V 3.3V	+25°C	7 8			ns
Break-Before Make Time Delay	t_{BBM}	$V_{NO1}=V_{NC1}=V_{NO2}=V_{NC2}=3V$, $R_L=300\Omega$, $C_L=35pF$	5V 3.3V	+25°C	6 9			ns
Off Isolation	O_{ISO}	$R_L=50\Omega$, Switch OFF	5V	+25°C		-47		dB
		$f=10MHz$ $f=1MHz$				-66		
Crosstalk Isolation	X_{TALK}	$R_L=50\Omega$, Switch OFF, $f=10MHz$	5V	+25°C		-53		dB
-3dB Bandwidth	BW	$R_L=50\Omega$, Switch ON	5V	+25°C		300		MHz
Charge Injection	Q	$C_L=1nF$, $V_{GEN}=0V$, $R_{GEN}=0\Omega$	5V	+25°C		10		pC
NC,NO Off Capacitance	$C_{NC(OFF)}$, $C_{NO(OFF)}$	$f=1MHz$	5V	+25°C		7		pF
NC,NO,COM On Capacitance	$C_{NC(ON)}$, $C_{NO(ON)}$, $C_{COM(ON)}$	$f=1MHz$	5V	+25°C		20		pF



8 Typical Characteristics

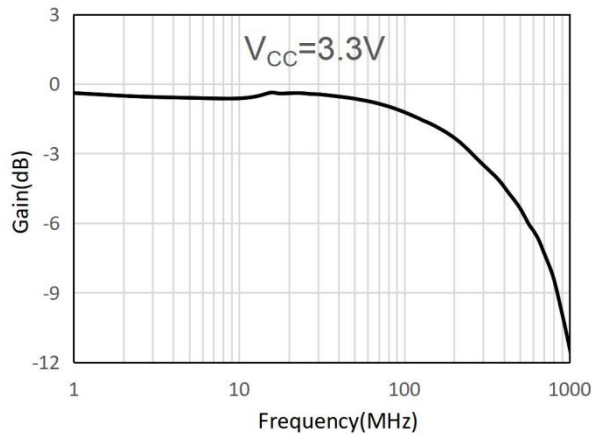


Fig.8-1. Bandwidth vs Frequency at 3.3V V_{CC}

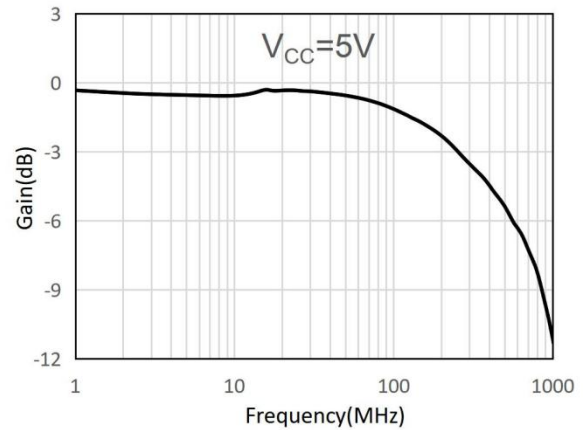


Fig.8-2. Bandwidth vs Frequency at 5V V_{CC}

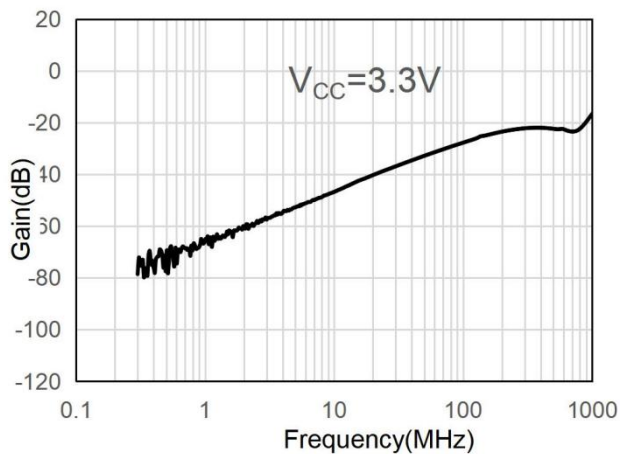


Fig.8-3. Off Isolation vs Frequency at 3.3V V_{CC}

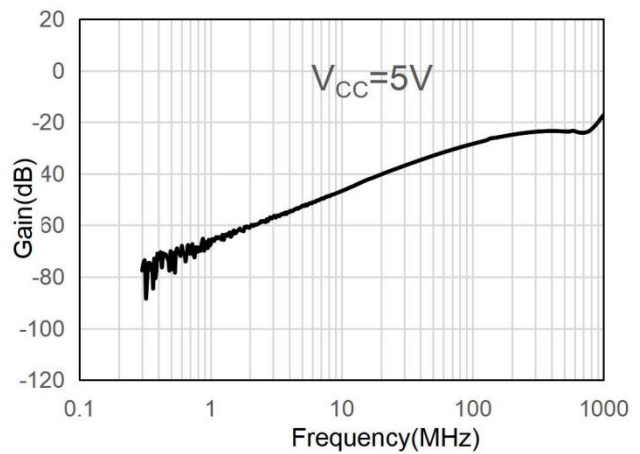


Fig.8-4. Off Isolation vs Frequency at 5V V_{CC}



9 Measurement Information

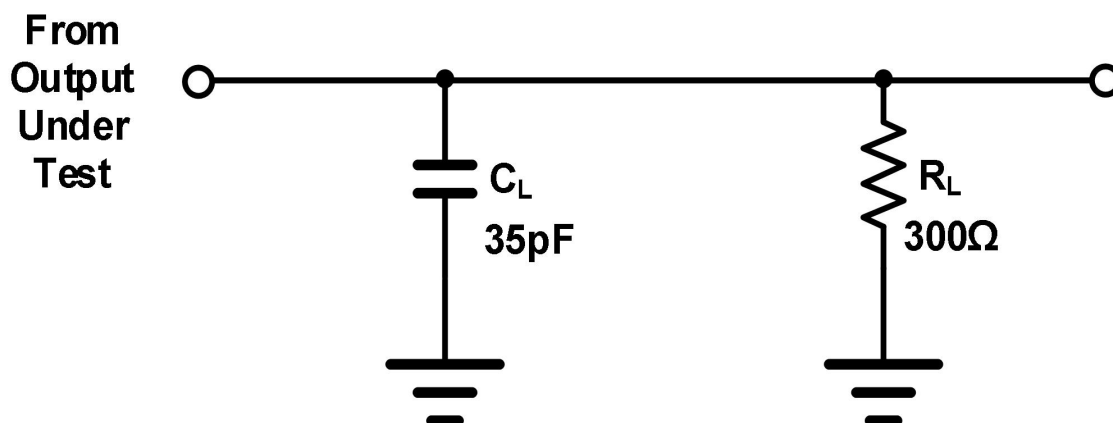


Fig.9-1. AC Test Circuit

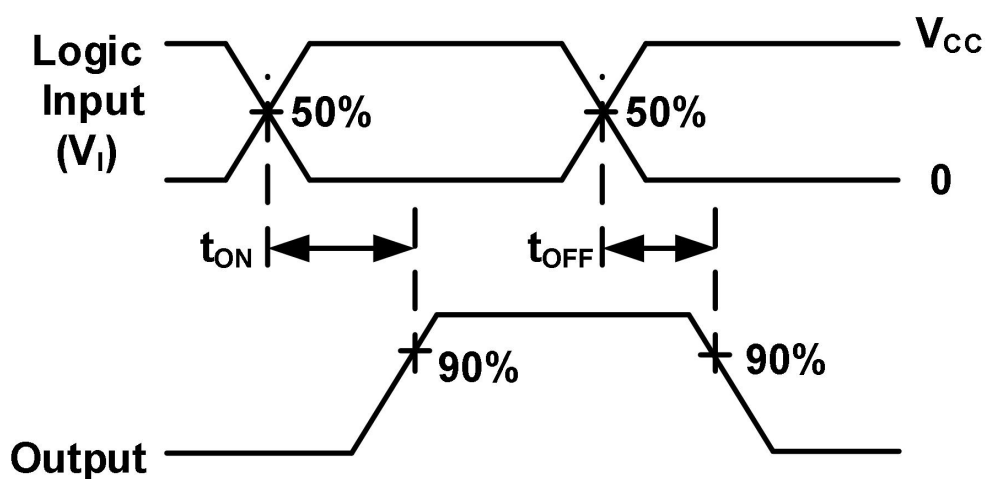


Fig.9-2. Switching Time

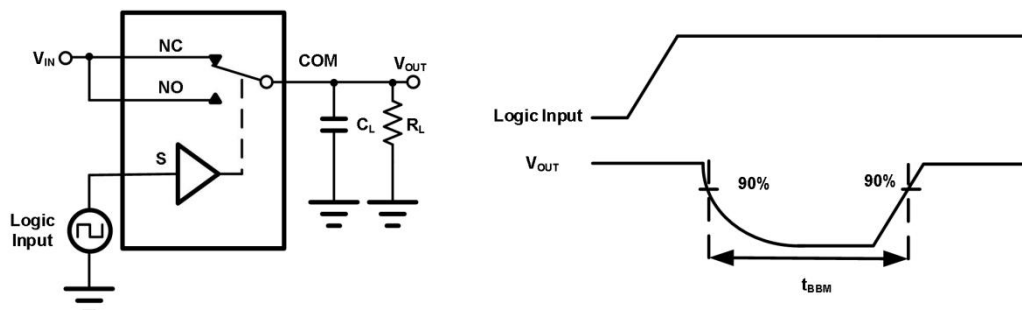


Fig.9-3. Break-Before-Make Time(t_{BBM})



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9 Measurement Information(Continued)

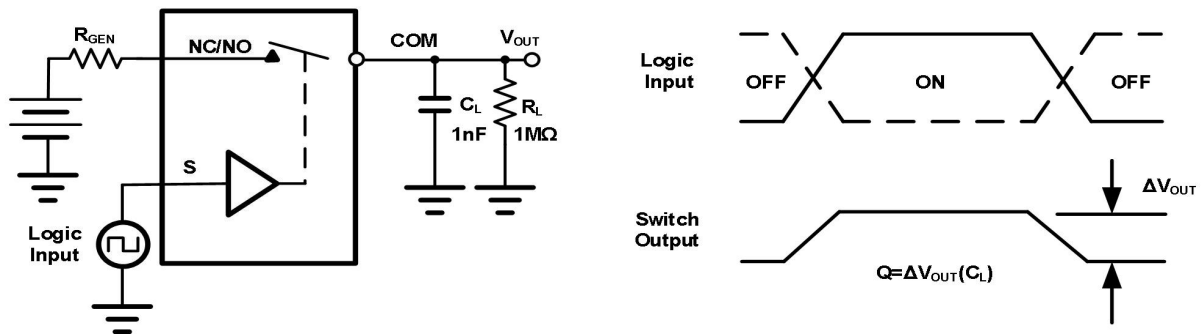


Fig.9-4. Charge Injection Test

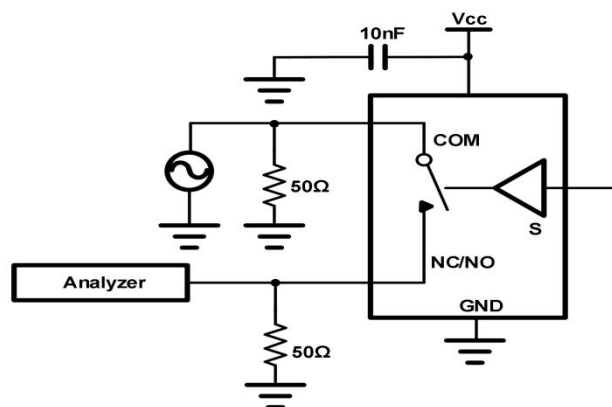


Fig.9-5. OFF Isolation (O_{iso})

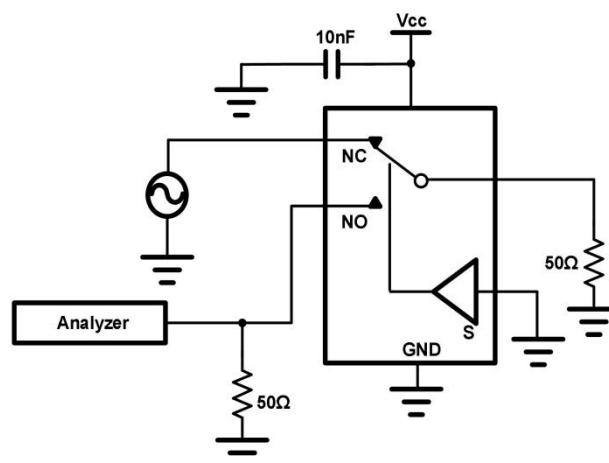


Fig.9-6. Crosstalk(X_{TALK})



9 Measurement Information(Continued)

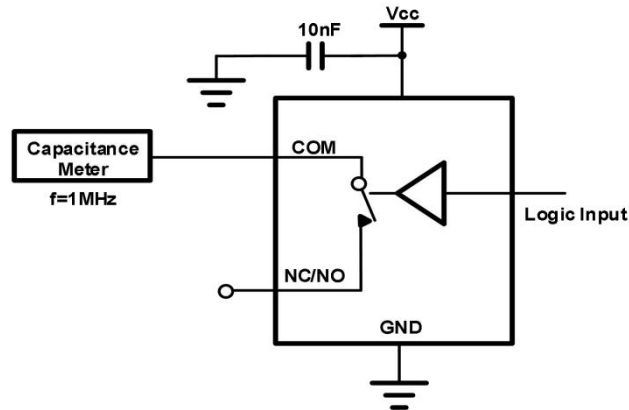


Fig.9-7. Channel Off Capacitance

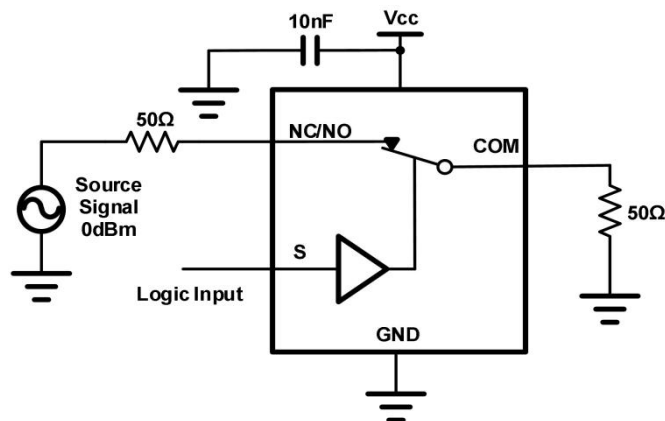


Fig.9-8. Channel On Capacitance

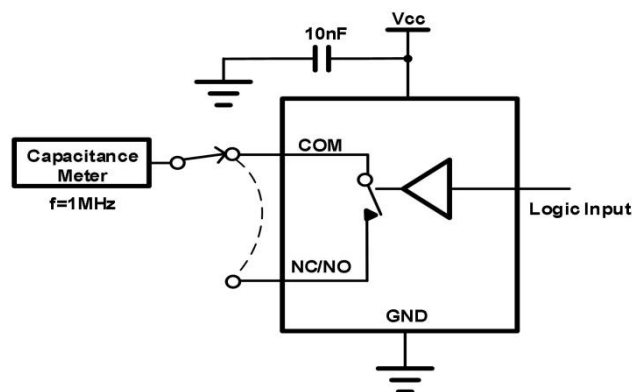


Fig.9-9. Bandwidth



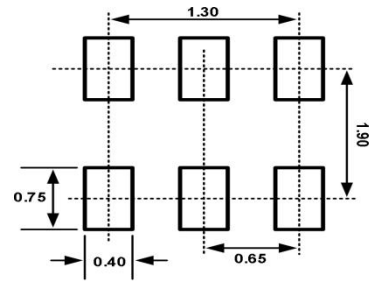
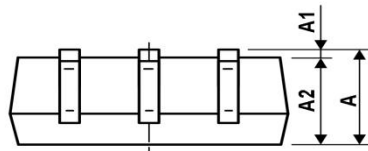
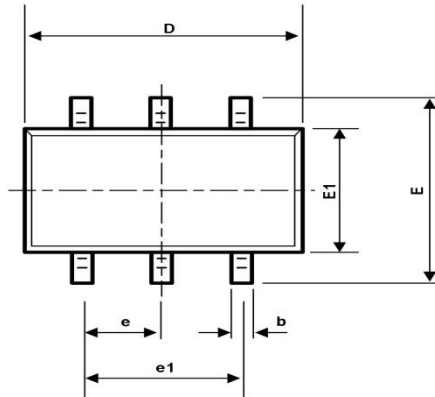
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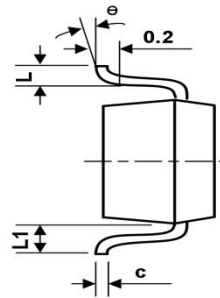
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10 Package Outline Dimension

SC70-6



Recommended Land Pattern (Unit: mm)



symbol	Dimension In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.900	1.100	0.035	0.043
A1	0.000	0.100	0.000	0.004
A2	0.900	1.000	0.035	0.039
b	0.150	0.350	0.006	0.014
c	0.110	0.175	0.004	0.007
D	2.000	2.200	0.079	0.087
E	2.150	2.450	0.085	0.096
E1	1.150	1.350	0.045	0.053
e	0.650TYP		0.026TYP	
e1	1.200	1.400	0.047	0.055
L	0.260	0.460	0.010	0.018
L1	0.525REF		0.021REF	
θ	0°	8°	0°	8°



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Notes For Attention

- When making the purchase, please ensure you recognize the company's trademark. If you have any questions, please contact the company's headquarters.
- When designing the circuit, please do not exceed the absolute maximum ratings of the components; otherwise, it will affect the reliability of the entire machine.
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