

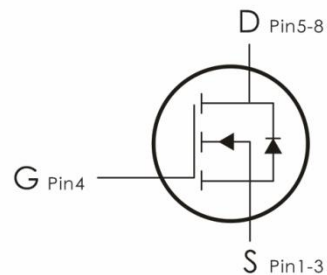
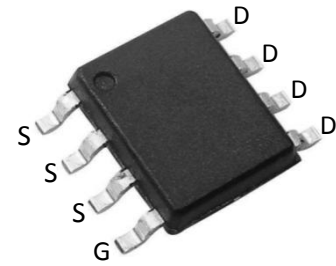
Description:

This N-Channel MOSFET uses advanced trench technology and design to provide excellent $R_{DS(on)}$ with low gate charge.

It can be used in a wide variety of applications.

Features:

- 1) $V_{DS}=30V, I_D=9A, R_{DS(ON)} < 20m\ \Omega @ V_{GS}=10V$ (typ: $16m\ \Omega$)
- 2) Low gate charge.
- 3) Green device available.
- 4) Advanced high cell density trench technology for ultra low $R_{DS(ON)}$.
- 5) Excellent package for good heat dissipation.



Package Marking and Ordering Information:

Part NO.	Marking	Package	Packing
DO4404	4404	SOP-8	3000 pcs/Reel

Absolute Maximum Ratings: ($T_A=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Ratings	Units
V_{DS}	Drain-Source Voltage	30	V
V_{GS}	Gate-Source Voltage	± 20	V
I_D	Continuous Drain Current	9	A
	Continuous Drain Current- $T_A=100^\circ\text{C}$	5.4	
I_{DM}	Pulsed Drain Current ¹	30	
P_D	Power Dissipation	3.1	W
T_J, T_{STG}	Operating and Storage Junction Temperature Range	-55-+150	$^\circ\text{C}$

Thermal Characteristics:

Symbol	Parameter	Max	Units
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient ³	113	$^\circ\text{C}/\text{W}$

Electrical Characteristics: ($T_A=25^{\circ}\text{C}$ unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
Off Characteristics						
BV _{DSS}	Drain-Sourctce Breakdown Voltage	V _{GS} =0V, I _D =250 μ A	30	---	---	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{GS} =0V, V _{DS} =30V	---	---	1	μ A
I _{GSS}	Gate-Source Leakage Current	V _{GS} =± 20V, V _{DS} =0A	---	---	± 100	nA
On Characteristics						
V _{GS(th)}	GATE-Source Threshold Voltage	V _{GS} =V _{DS} , I _D =250 μ A	1.2	1.7	2.2	V
R _{DS(ON)}	Drain-Source On Resistance ⁴	V _{GS} =10V, I _D =5.5A	---	16	20	m Ω
		V _{GS} =4.5V, I _D =4.5A	---	24	30	m Ω
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{DS} =15V, V _{GS} =0V, f=1MHz	---	484	---	pF
C _{oss}	Output Capacitance		---	68	--	
C _{rss}	Reverse Transfer Capacitance		---	52	---	
Switching Characteristics						
t _{d(on)}	Turn-On Delay Time	V _{DS} =15V, I _D =5A, R _{ENG} =3 Ω ,V _{GS} =10V	---	3	---	ns
t _r	Rise Time		---	10	---	ns
t _{d(off)}	Turn-Off Delay Time		---	13	---	ns
t _f	Fall Time		---	1	---	ns
Q _g	Total Gate Charge	V _{GS} =10V, V _{DS} =15V, I _D =5A	---	9	---	nc
Q _{gs}	Gate-Source Charge		---	1	---	nc
Q _{gd}	Gate-Drain “Miller” Charge		---	1	---	nc
Drain-Source Diode Characteristics						
V _{SD}	Diode Forward Voltage	V _{GS} =0V, I _{SD} =5.8A	---	---	1.2	V
I _S	Continuous Drain Curren	VD=VG=0V	---	---	9	A
I _{SM}	Pulsed Drain Current		---	---	30	A
Trr	Reverse Recovery Time	I _F =5A, T _J =25 °C	---	7.5	---	ns
Qrr	Reverse Recovery Charge	dl/dt=100A/us	---	2	---	nc

Notes:

1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.
2. $R_{\theta JA}$ is measured with the device mounted on a 1inch² pad of 2oz copper FR4 PCB
3. Pulse Test: Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 0.5\%$.

Typical Characteristics: ($T_A = 25^\circ C$ unless otherwise noted)

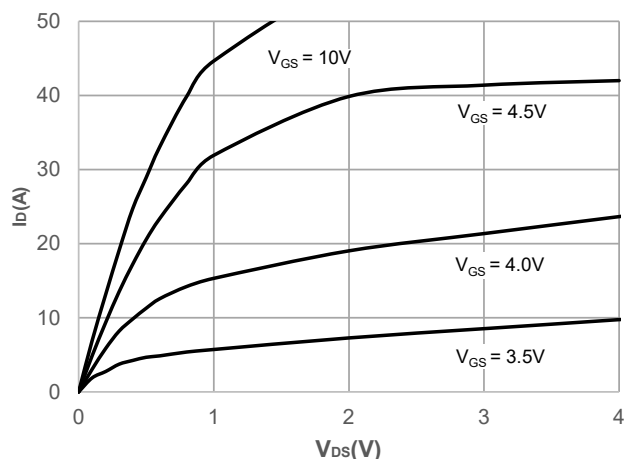


Figure 1: Output Characteristics

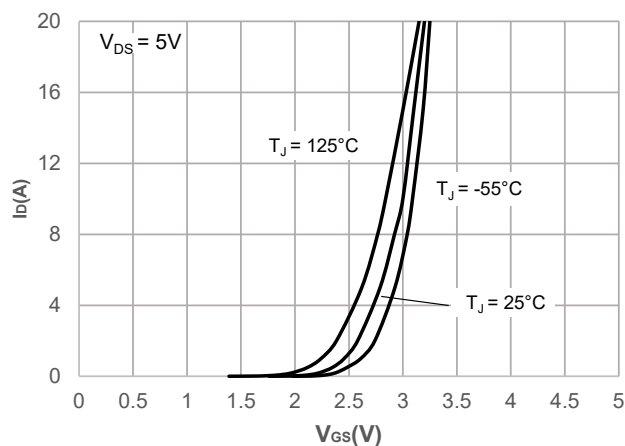


Figure 2: Typical Transfer Characteristics

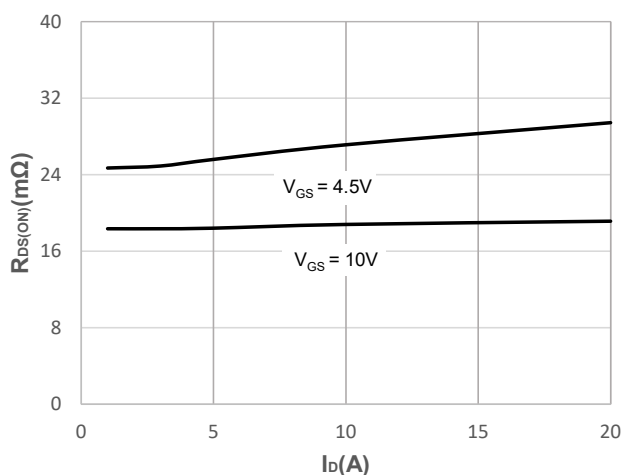


Figure 3: On-resistance vs. Drain Current

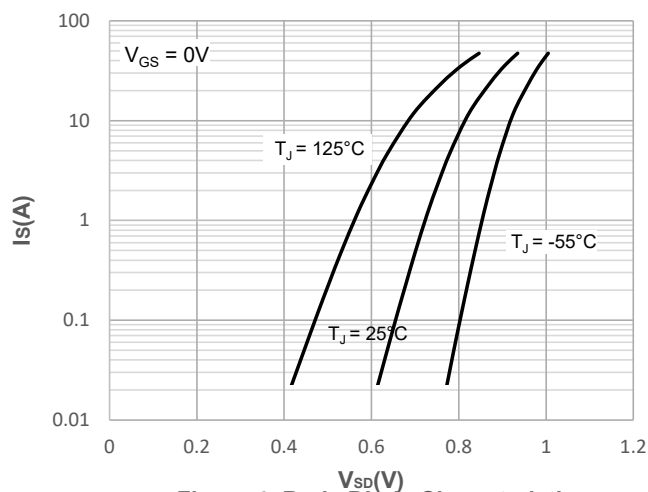


Figure 4: Body Diode Characteristics

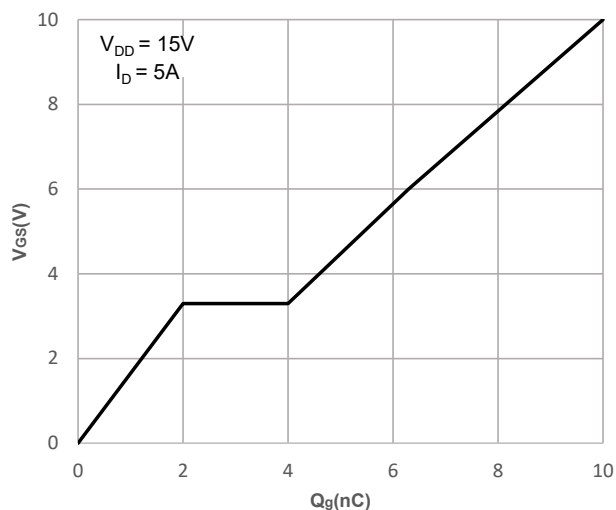


Figure 5: Gate Charge Characteristics

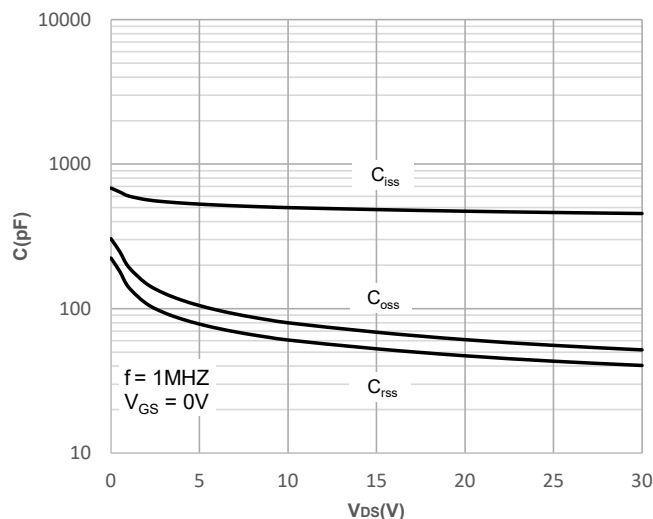


Figure 6: Capacitance Characteristics

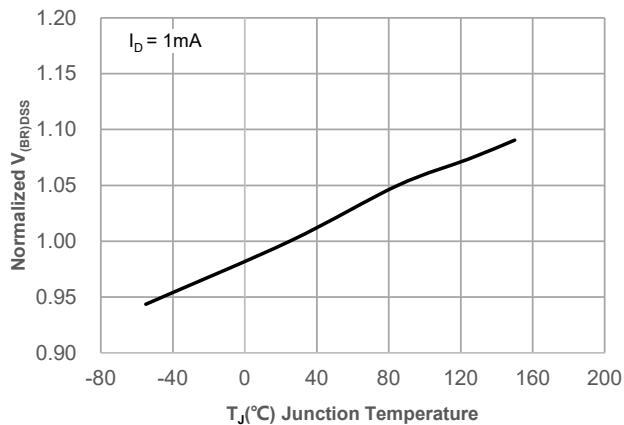


Figure 7: Normalized Breakdown voltage vs. Junction Temperature

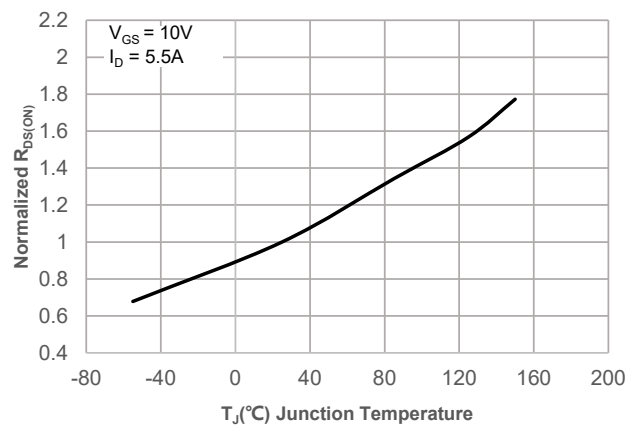


Figure 8: Normalized on Resistance vs. Junction Temperature

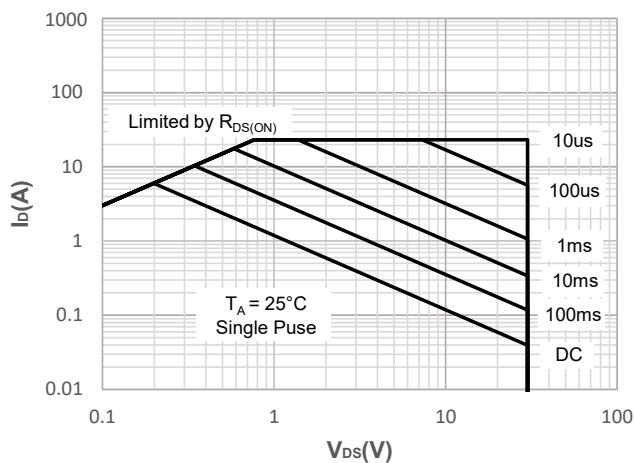


Figure 9: Maximum Safe Operating Area

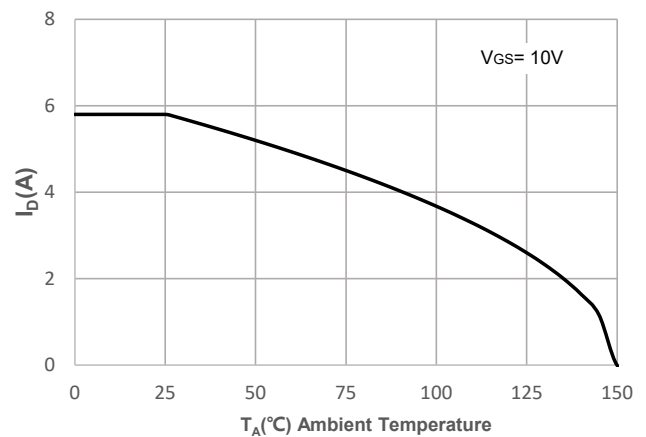


Figure 10: Maximum Continuous Drain Current vs. Ambient Temperature

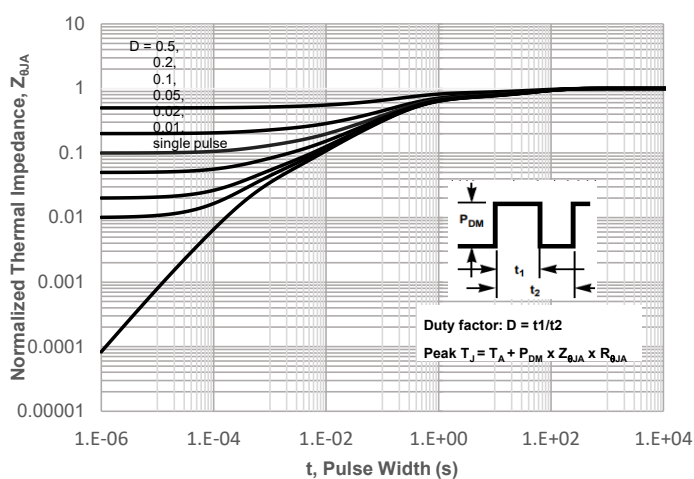


Figure 11: Normalized Maximum Transient Thermal Impedance

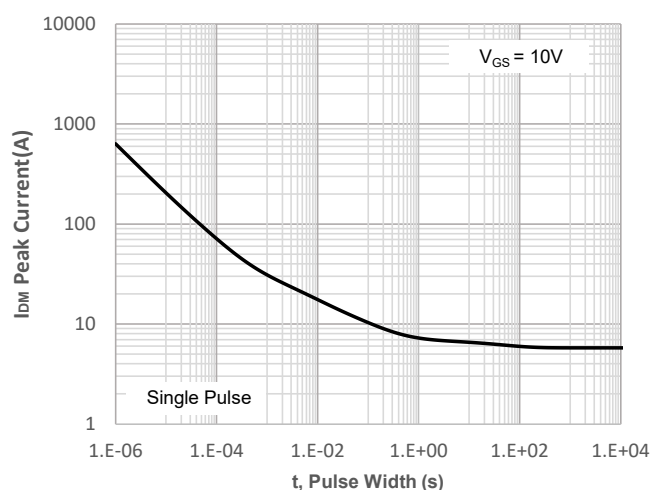
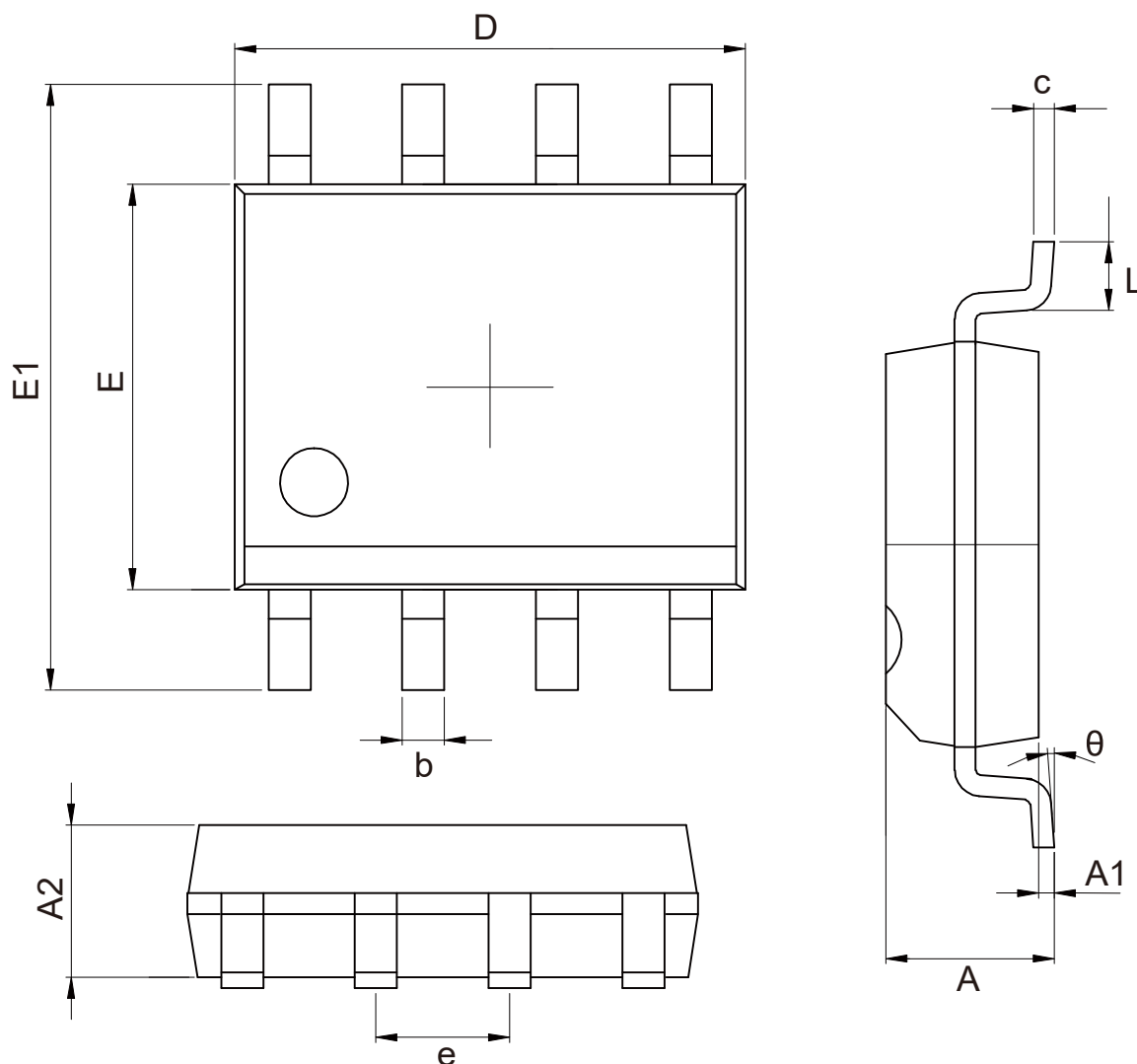


Figure 12: Peak Current Capacity

SOP-8 Package Information:



COMMON DIMENSIONS			
CUNITS MEASURE=MILLIMETER			
SYMBOL	MIN	NOM	MAX
A	1.350	—	1.750
A1	0.100	—	0.250
A2	1.350	—	1.550
b	0.330	—	0.510
c	0.170	—	0.250
D	4.700	—	5.100
E	3.800	3.900	4.000
E1	5.800	—	6.200
e	1.270BSC		
L	0.400	—	1.270
θ	0°	--	8°

Unit:mm

Marking Information:

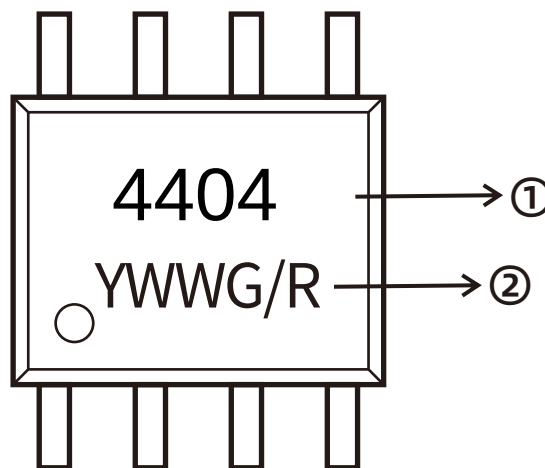
①. Part NO.

②. Date Code(YWWG / R)

Y : Year Code , last digit of the year

WW : Week Code(01-53)

G/R : G(Green) /R(Lead Free)



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