

MBR20100CT

Schottky Barrier Rectifiers



Voltage: 100 Volts

Current: 20 A

Package: TO-220C

Features

- NH'S Plane Schottky Barrier Chip Technology
- Low Forward Voltage Drop For High Efficiency
- Low Power Loss For High Reliability
- High Frequency Switching Speed

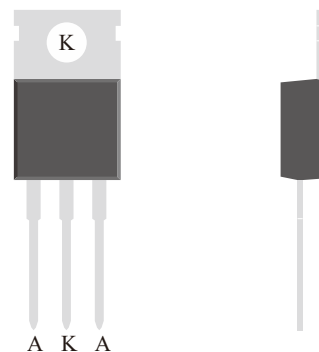
Mechanical Data

- **Case:** Molded With UL-94 ClassV-0 Recognized, RoHS-Compliant
- **Polarity:** Look At The Diagram And Polarity On The Right
- **Terminals:** Tin Plated Leads,Solderable Per J-STD-002 And JESD22-B102

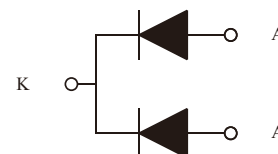
Typical Applications

- Switch Mode Power Supplies (SMPS)
- Fast Chargers
- LED Driver And Monitor Lighting
- Automotive Electronics And Charging Posts

Diagram:



Polarity:



Single Phase,Half Wave,60Hz,Resistive Or Inductive Load.For Capacitive Load,Derate Current By 20%

Maximum Ratings (Ta=25°C Unless Otherwise Specified)

Parameter	Test Conditions	Symbol	Ratings	Unit
Maximum Repetitive Peak Reverse Voltage		V_{RRM}	100	V
Maximum RMS Voltag		V_{RMS}	70	V
Maximum DC Blocking Voltage		V_{DC}	100	V
Maximum Average Forward Rectified Current	Per Diode Total device	$I_{F(AV)}$	10 20	A
Peak Forward Surge Current Per Diode	8.3ms Single Half Sine-wave Superimposed On Rate Load	I_{FSM}	175	A
Current Squared Time Per Diode	$t < 8.3ms$	I^2t	127.1	A ² sec
Maximum Mounting Torque	M3 screw	T_{MM}	1.1	N.m

Electrical Characteristics (Ta=25°C Unless Otherwise Specified)

Parameter	Test Conditions	Symbol	Ratings			Unit
			Min.	Typ.	Max.	
Instaneous Forward Voltage Per Diode	Ta=25°C Ta=110°C I _F = 10.0 A	V_F	-- --	0.82 0.77	0.89 0.84	V
Maximum DC Reverse Current at Rated DC Blocking Voltage	Ta=25°C ,V _R =VRRM Ta=110°C ,V _R =VRRM*80%	I_{RRM}	-- --	1 1	10 3	uA mA
Typical Junction Capacitance Per Diode	4 V,1MHz	C_J	--	240	--	pF

Thermal Characteristics (Ta=25°C Unless Otherwise Specified)

Parameter	Test Conditions	Symbol	Ratings	Unit
Operating Junction Temperature Range		T_J	-55 to 150	°C
Storage Temperature Range		T_{STD}	-55 to 150	
Thermal Resistance Junction To Ambient With Steady-State	Still Air Environment With Ta=25°C	$R_{\theta JA}$	58.0	°C/W
Thermal Resistance Junction-Case With Steady-State	Device Mounted On 75mm x 45mm x 2.5mm Alu. Heat.	$R_{\theta JC}$	2.0	

Notes: 1.Pulse Test: 300 Us Pulse Width,1% Duty Cycle

MBR20100CT

Schottky Barrier Rectifiers



Typical Characteristics Curves

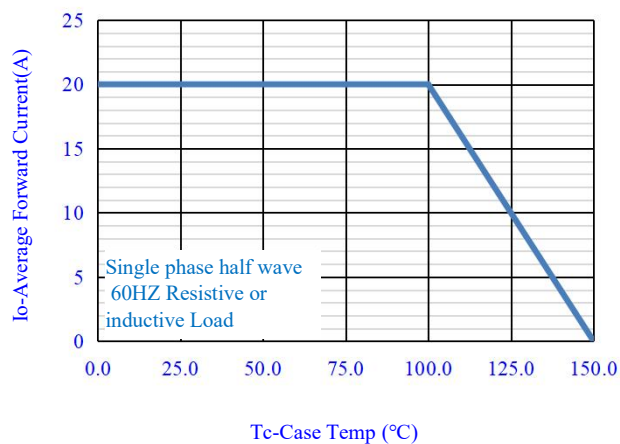


Fig.1-Forward Current Derating Curve

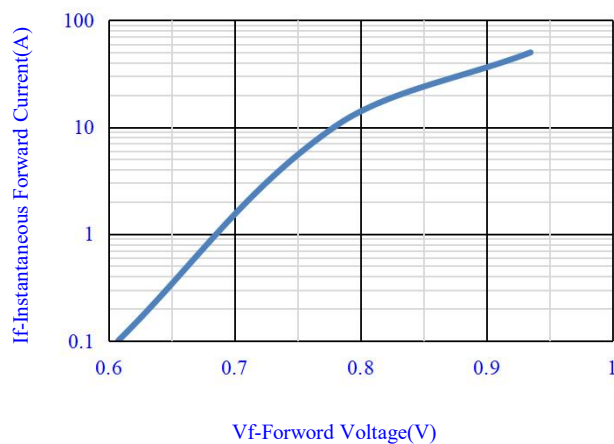


Fig.2-Typical Instantaneous Forward

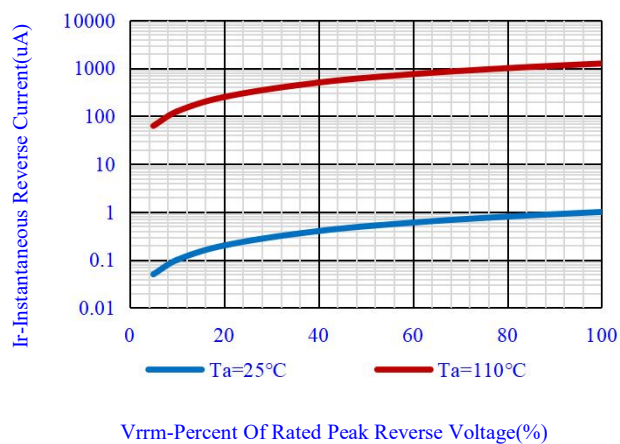


Fig.3-Typical Reverse Characteristics

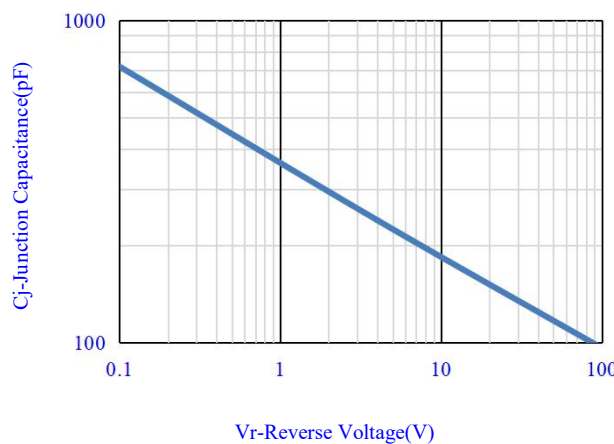


Fig.4-Typical Junction Capacitance

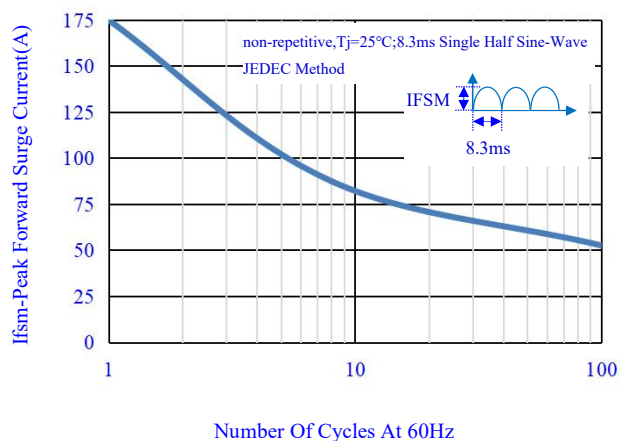


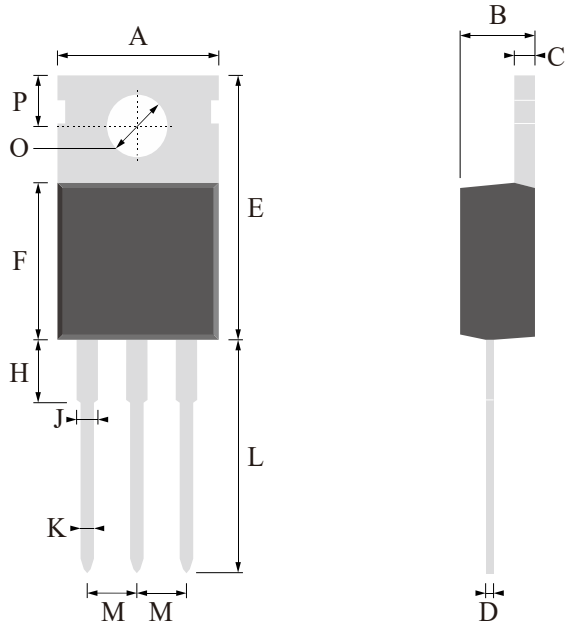
Fig.5-Max. Non-Repetitive Surge Current

MBR20100CT

Schottky Barrier Rectifiers



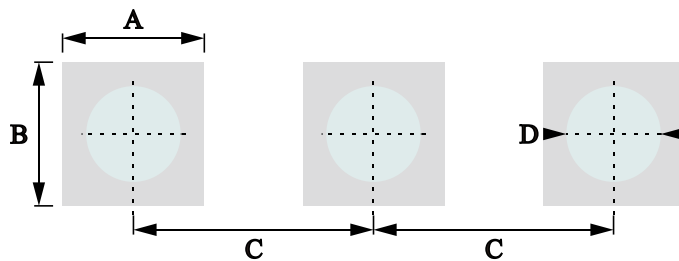
OUTLINE DRAWINGS



TO-220C

OUTLINE DIMENSIONS						
Dim.	Milimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	9.25	-	10.75	0.3642	-	0.4232
B	1.10	-	1.65	0.0433	-	0.0650
C	4.15	-	4.95	0.1634	-	0.1949
D	0.25	-	0.65	0.0098	-	0.0256
E	14.50	-	16.70	0.5709	-	0.6575
F	8.40	-	9.95	0.3307	-	0.3917
L	12.15	-	14.30	0.4783	-	0.5630
H	3.00	-	3.80	0.1181	-	0.1496
J	1.05	-	1.60	0.0413	-	0.0630
K	0.65	-	0.95	0.0256	-	0.0374
M	2.10	-	2.90	0.0827	-	0.1142
O	3.20	-	4.10	0.1260	-	0.1614
P	2.45	-	3.10	0.0965	-	0.1220

RECOMMENDED LAYOUT DRAWINGS



TO-220C

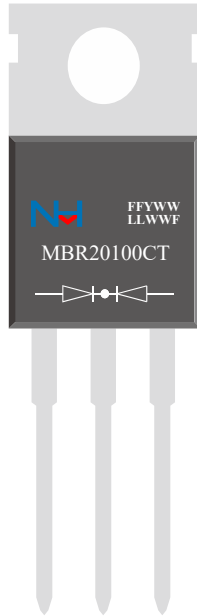
OUTLINE DIMENSIONS						
Dim.	Milimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	-	1.60	-	-	0.0630	-
B	-	1.60	-	-	0.0630	-
C	-	2.54	-	-	0.1000	-
D	-	1.00	-	-	0.0394	-

MBR20100CT

Schottky Barrier Rectifiers



MARKING



MARKING INSTRUCTION

NH=Niuhan Trademark

FF=Product Line Code,According To Actual Changes

YWW=Date Code,According To Actual Changes

LLWWF=Inernal Code,According To Actual Changes

MBR20100CT=Model

PACKING INFORMATION

Package Type	Package Code	Product Weight Approx(g/Pcs)	Package Method	Quantity (Pcs/Min. Pack.)	Quantity (Pcs/Inner Box)	Quantity (Pcs/Carton)
TO-220C	P1	2.057	Tube	50	1000	5000

MBR20100CT

Schottky Barrier Rectifiers



Recommended wave soldering condition

Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

Recommended temperature profile for IR reflow



Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
Average ramp-up rate (T _{smax} to T _p)	3°C/second max.	3°C/second max.
Preheat -Temperature Min(TS min) -Temperature Max(TS max) -Time(ts min to ts max)	100°C 150°C 60-120 seconds	150°C 200°C 60-180 seconds
Time maintained above: -Temperature (TL) - Time (tL)	183°C 60-150 seconds	217°C 60-150 seconds
Peak Temperature(TP)	240 +0/-5 °C	260 +0/-5 °C
Time within 5°C of actual peak temperature(tp)	10-30 seconds	20-40 seconds
Ramp down rate	6°C/second max.	6°C/second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

Note : All temperatures refer to topside of the package, measured on the package body surface.

MBR20100CT

Schottky Barrier Rectifiers



Disclaimer

- Reproducing and modifying information of the document is prohibited without permission from Niuhan Electronics Technology Co., LTD
- Niuhan Electronics Technology Co., LTD. reserves the rights to make changes of the content herein the document anytime without notification.
- Niuhan Electronics Technology Co., LTD. disclaims any and all liability arising out of the application or use of any product including damages incidentally and consequentially occurred.
- Niuhan Electronics Technology Co., LTD. does not assume any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.
- Applications shown on the herein document are examples of standard use and operation. Customers are responsible in comprehending the suitable use in particular applications. Niuhan Electronics Technology Co., LTD. makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.
- The products shown herein are not designed and authorized for equipments requiring high level of reliability or relating to human life and for any applications concerning life-saving or life-sustaining, such as medical instruments, transportation equipment, aerospace machinery et cetera. Customers using or selling these products for use in such applications do so at their own risk and agree to fully indemnify Niuhan Electronics Technology Co., LTD. for any damages resulting from such improper use or sale.
- When the appearance of the product and chip size does not change, in order to product the customer. quality, change the internal structure and the production process Niuhan can not notify