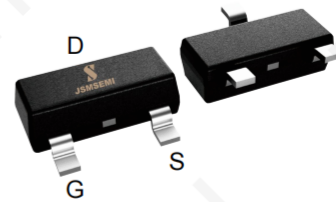


Features

- ◆ Low $R_{DS(on)}$ @ $V_{GS}=-10V$
- ◆ -5V Logic Level Control
- ◆ P Channel SOT23 Package
- ◆ Pb-Free,RoHS Compliant

Applications

- ◆ Load Switch
- ◆ Switching circuits
- ◆ High-speed line driver
- ◆ Power Management Functions



SOT23

Absolute Maximum Ratings

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

Symbol	Parameter	Rating	Unit	
Common Ratings (T _A =25°C Unless Otherwise Noted)				
V _{GS}	Gate-Source Voltage	±12	V	
V _{(BR)DSS}	Drain-Source Breakdown Voltage	-30	V	
T _J	Maximum Junction Temperature	150	°C	
T _{STG}	Storage Temperature Range	-50 to 150	°C	
Mounted on Large Heat Sink				
I _{DM}	Pulse Drain Current Tested①	T _A =25°C	-16	A
I _D	Continuous Drain Current	T _A =25°C	-4.5	A
		T _A =70°C	-3.2	
P _D	Maximum Power Dissipation	T _A =25°C	1.2	W
		T _A =70°C	0.9	
R _{JA}	Thermal Resistance Junction-Ambient	80	°C/W	

Symbol	Parameter	Condition	Min	Typ	Max	Unit
Static Electrical Characteristics @ T _J = 25°C (unless otherwise stated)						
V _{(BR)DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V I _D =-250μA	-30	--	--	V
I _{DSS}	Zero Gate Voltage Drain Current(T _A =25°C)	V _{DS} =-30V, V _{GS} =0V	--	--	-1	μA
	Zero Gate Voltage Drain Current(T _A =125°C)	V _{DS} =-24V, V _{GS} =0V	--	--	-100	uA
I _{GSS}	Gate-Body Leakage Current	V _{GS} =±16V, V _{DS} =0V	--	--	±100	nA
V _{GS(TH)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =-250μA	-0.5	-0.8	-1.2	V
R _{DS(ON)}	Drain-Source On-State Resistance②	V _{GS} =-10V, I _D =-4A	--	40	48	mΩ
R _{DS(ON)}	Drain-Source On-State Resistance②	V _{GS} =-4.5V, I _D =-3A	--	44	55	mΩ
R _{DS(ON)}	Drain-Source On-State Resistance②	V _{GS} =-3.3V, I _D =-3A	--	56	65	mΩ
Dynamic Electrical Characteristics @ T _J = 25°C (unless otherwise stated)						
C _{iss}	Input Capacitance	V _{DS} =-15V, V _{GS} =0V, f=1MHz	--	655	--	pF
C _{oss}	Output Capacitance		--	65	--	pF
C _{rss}	Reverse Transfer Capacitance		--	53	--	pF
Q _g	Total Gate Charge	V _{DS} =-15V I _D =-4A, V _{GS} =-4.5V	--	7.2	--	nC
Q _{gs}	Gate Source Charge		--	1.5	--	nC
Q _{gd}	Gate Drain Charge		--	2.6	--	nC
Switching Characteristics @ T _J = 25°C (unless otherwise stated)						
t _{d(on)}	Turn on Delay Time	V _{DD} =-15V, I _D =-2A, R _G =3.3Ω, V _{GS} =-10V	--	7	--	ns
t _r	Turn on Rise Time		--	3.8	--	ns
t _{d(off)}	Turn Off Delay Time		-	35	--	ns
t _f	Turn Off Fall Time		--	10.5	--	ns
Source Drain Diode Characteristics						
I _{SD}	Source drain current(Body Diode)	T _A =25°C	--	--	-2	A
V _{SD}	Forward on voltage②	T _J =25°C, I _{SD} =-4A, V _{GS} =0V	--	-0.88	-1.2	V

Notes:

① Pulse width limited by maximum allowable junction temperature

②Pulse test ; Pulse width 300 s, duty cycle 2%.

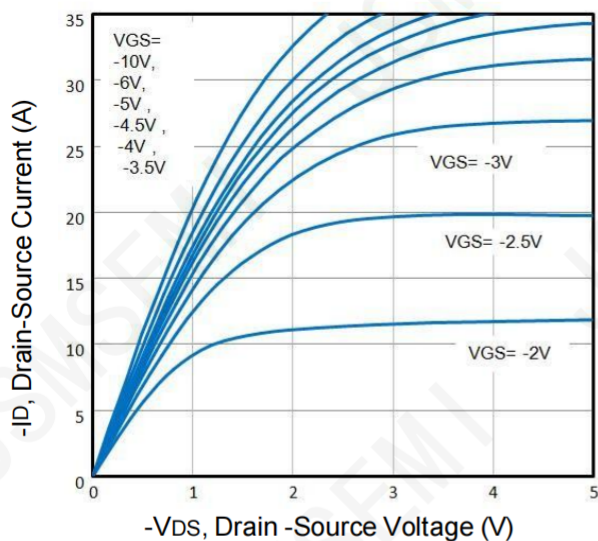


Fig1. Typical Output Characteristics

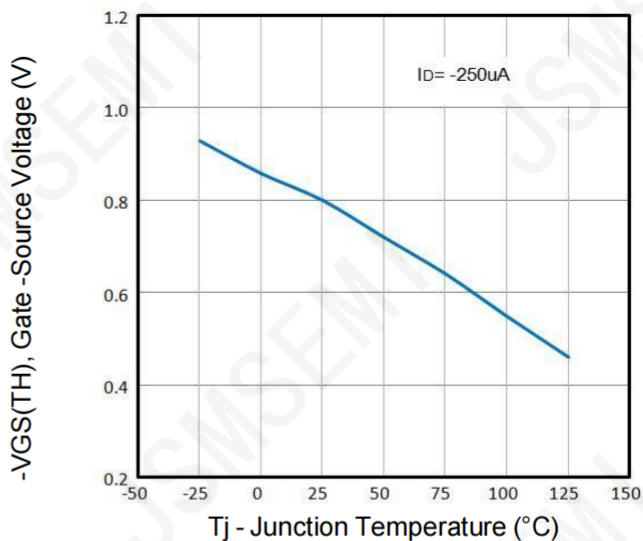


Fig2. Normalized Threshold Voltage Vs. Temperature

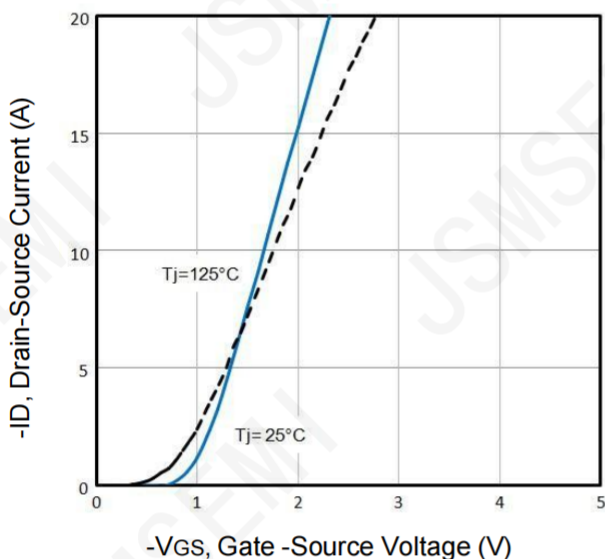


Fig3. Typical Transfer Characteristics

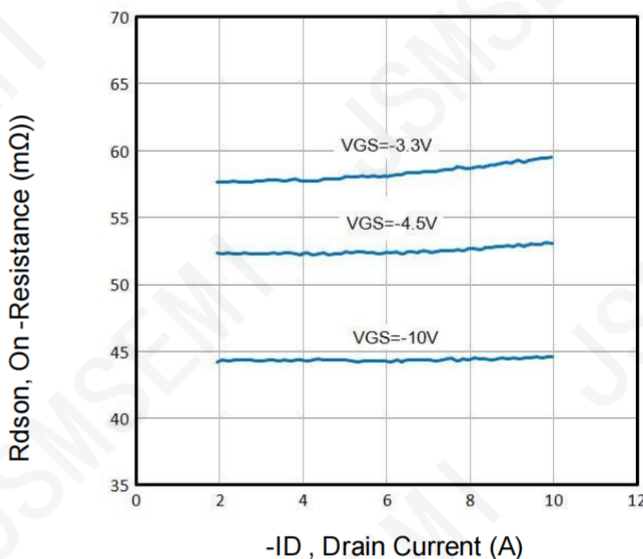


Fig4. On-Resistance vs. Drain Current and Gate

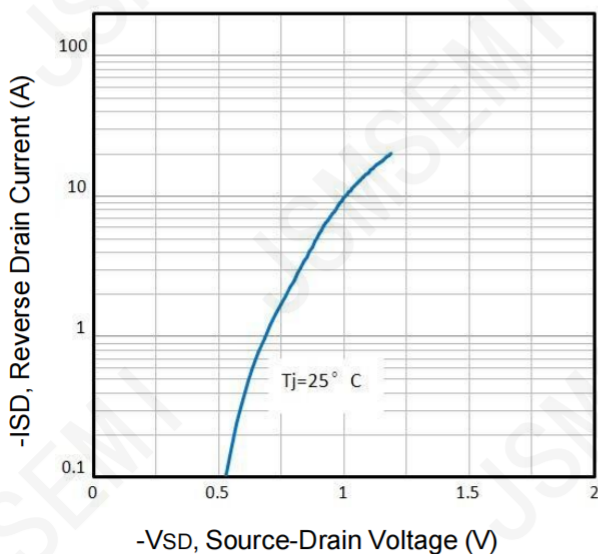


Fig5. Typical Source-Drain Diode Forward Voltage

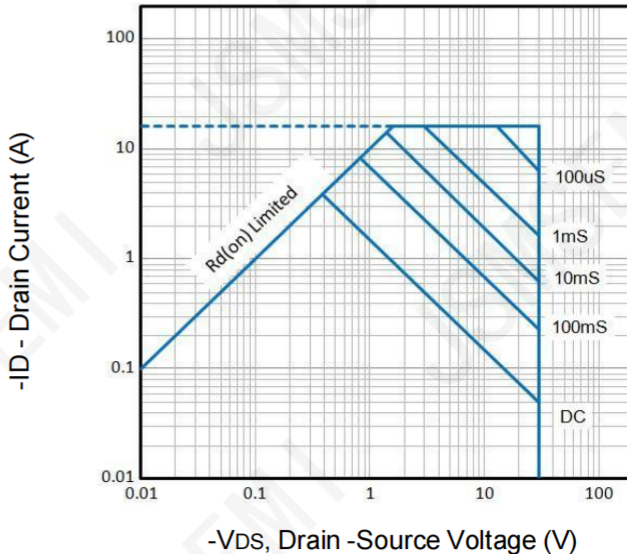


Fig6. Maximum Safe Operating Area

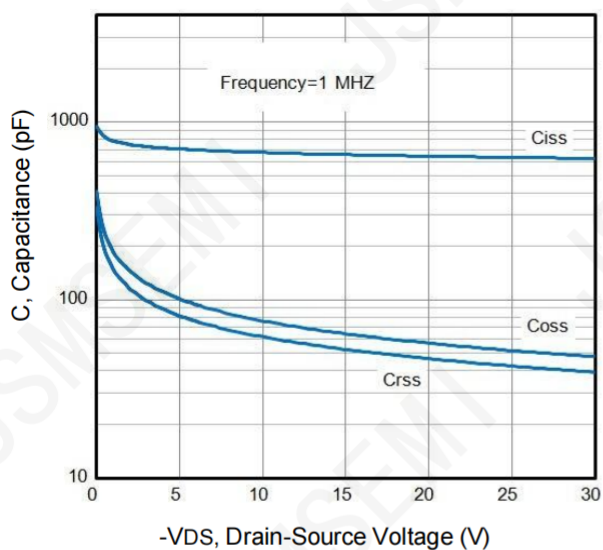


Fig7. Typical Capacitance Vs. Drain-Source Voltage

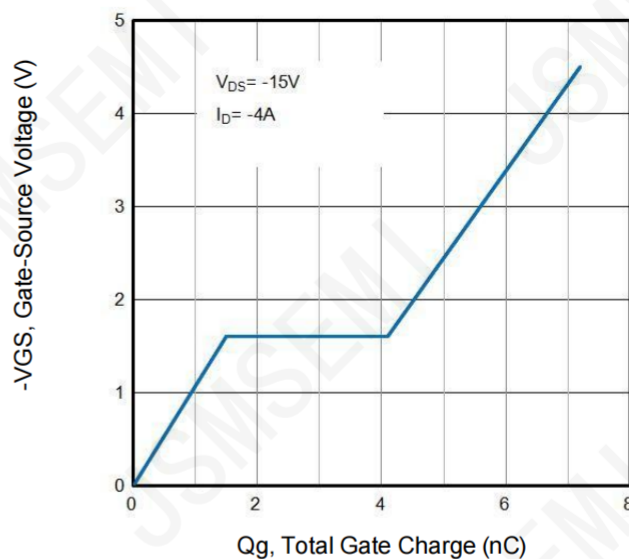


Fig8. Typical Gate Charge Vs. Gate-Source Voltage

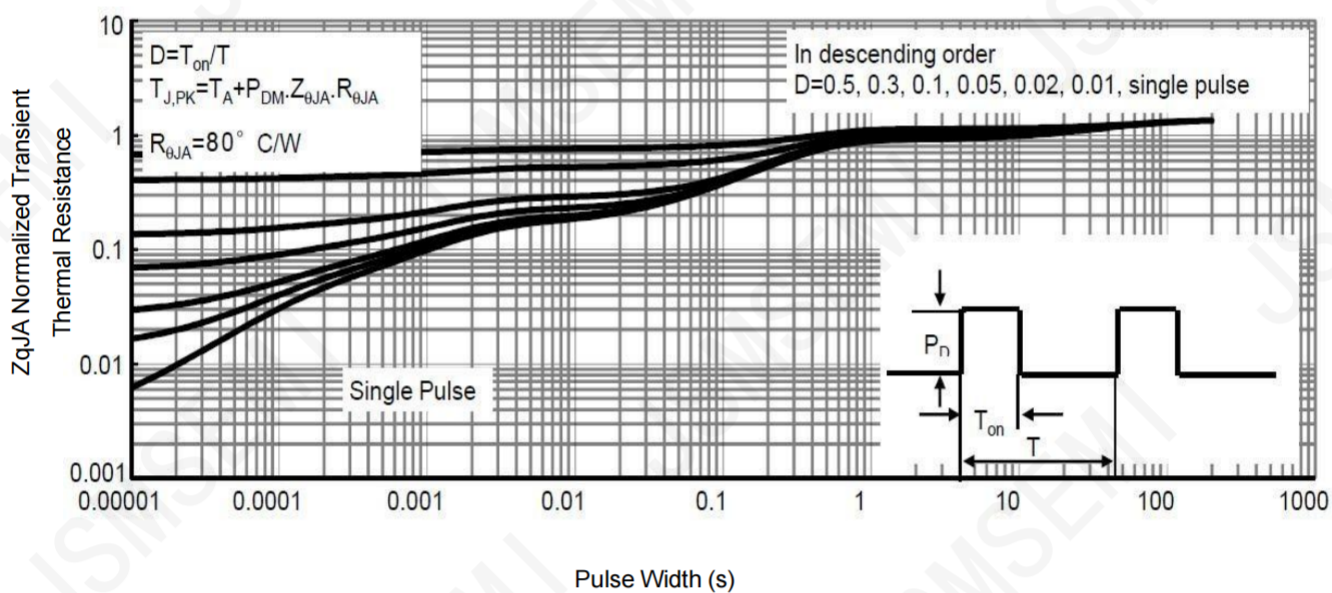


Fig9. Normalized Maximum Transient Thermal Impedance

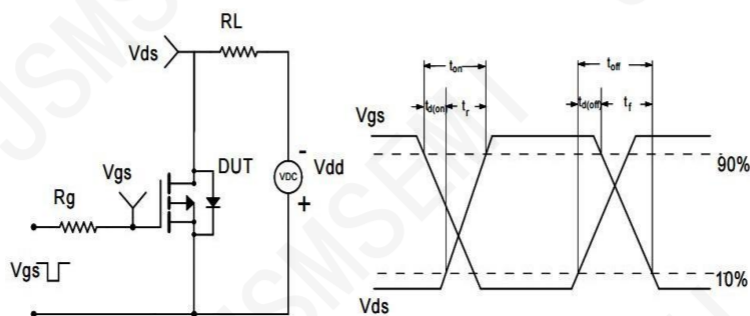
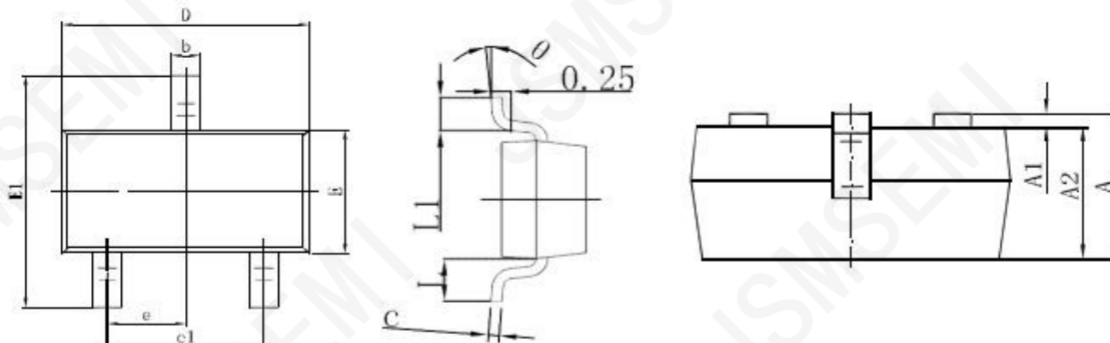


Fig10. Switching Time Test Circuit and waveforms

LOW VOLTAGE MOSFET (P-CHANNEL)

SOT-23 Package Outline Dimensions



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.900	1.150	0.035	0.045
A1	0.000	0.100	0.000	0.004
A2	0.900	1.050	0.035	0.041
b	0.300	0.500	0.012	0.020
c	0.080	0.150	0.003	0.006
D	2.800	3.000	0.110	0.118
E	1.200	1.400	0.047	0.055
E1	2.250	2.550	0.089	0.100
e	0.950 TYP		0.037 TYP	
e1	1.800	2.000	0.071	0.079
L	0.550 REF		0.022 REF	
L1	0.300	0.500	0.012	0.020
θ	0°	8°	0°	8°