HUASHUO SEMICONDUCTOR

HSBA3103

P-Ch 30V Fast Switching MOSFETs

Description

The HSBA3103 is the high cell density trenched P-ch MOSFETs, which provide excellent RDSON and gate charge for most of the synchronous buck converter applications.

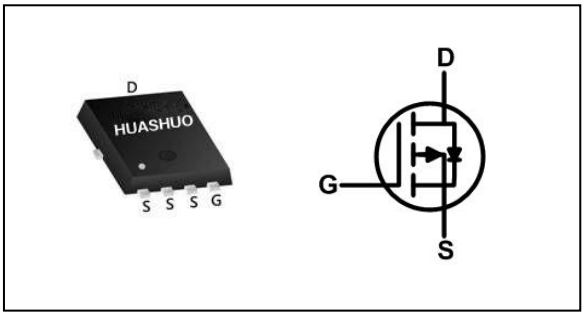
The HSBA3103 meet the RoHS and Green Product requirement , 100% EAS guaranteed with full function reliability approved.

Product Summary

V_{DS}	-30	V
$R_{DS(ON),typ}$	14	mΩ
I_D	-35	A

- 100% EAS Guaranteed
- Green Device Available
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- Advanced high cell density Trench technology

PRPAK5X6 Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter	Rating		Units
		10s	Steady State	
V_{DS}	Drain-Source Voltage	-30		V
V_{GS}	Gate-Source Voltage	±20		V
I_D@T_C=25℃	Continuous Drain Current, V_{GS} @ -10V[1]	-35		A
I_D@T_C=100℃	Continuous Drain Current, V_{GS} @ -10V[1]	-22		A
I_D@T_A=25℃	Continuous Drain Current, V_{GS} @ -10V[1]	-13.4	-8.5	A
I_D@T_A=70℃	Continuous Drain Current, V_{GS} @ -10V[1]	-10.7	-6.8	A
I_{DM}	Pulsed Drain Current[2]	-95		A
EAS	Single Pulse Avalanche Energy[3]	72.2		mJ
I_{AS}	Avalanche Current	-18		A
P_D@T_C=25℃	Total Power Dissipation[4]	34.7		W
P_D@T_A=25℃	Total Power Dissipation[4]	5	2	W
T_{STG}	Storage Temperature Range	-55 to 150		℃
T_J	Operating Junction Temperature Range	-55 to 150		℃

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-Ambient [1]	---	62	℃/W
$R_{\theta JA}$	Thermal Resistance Junction-Ambient [1] (t ≤10s)	---	25	℃/W
$R_{\theta JC}$	Thermal Resistance Junction-Case[1]	---	3.6	℃/W

Electrical Characteristics (T_J=25 ℃, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS}=0V , I_D=-250uA	-30	---	---	V
$R_{DS(ON)}$	Static Drain-Source On-Resistance[2]	V_{GS}=-10V , I_D=-15A	---	14	17	mΩ
		V_{GS}=-4.5V , I_D=-10A	---	19	24	
$V_{GS(th)}$	Gate Threshold Voltage	V_{GS}=V_{DS} , I_D =-250uA	-1.0	-1.6	-2.5	V
I_{DSS}	Drain-Source Leakage Current	V_{DS}=-24V , V_{GS}=0V , T_J=25℃	---	---	-1	uA
		V_{DS}=-24V , V_{GS}=0V , T_J=55℃	---	---	-5	
I_{GSS}	Gate-Source Leakage Current	V_{GS}=±20V , V_{DS}=0V	---	---	±100	nA
gfs	Forward Transconductance	V_{DS}=-5V , I_D=-10A	---	5	---	S
R_g	Gate Resistance	V_{DS}=0V , V_{GS}=0V , f=1MHz	---	13	---	Ω
Q_g	Total Gate Charge (-4.5V)	V_{DS}=-15V , V_{GS}=-4.5V , I_D=-15A	---	12.5	---	nC
Q_{gs}	Gate-Source Charge		---	5.4	---	
Q_{gd}	Gate-Drain Charge		---	5	---	
$T_{d(on)}$	Turn-On Delay Time	V_{DD}=-15V , V_{GS}=-10V , R_G=3.3Ω, I_D=-15A	---	4.4	---	ns
T_r	Rise Time		---	11.2	---	
$T_{d(off)}$	Turn-Off Delay Time		---	34	---	
T_f	Fall Time		---	18	---	
C_{iss}	Input Capacitance	V_{DS}=-15V , V_{GS}=0V , f=1MHz	---	1345	---	pF
C_{oss}	Output Capacitance		---	194	---	
C_{rss}	Reverse Transfer Capacitance		---	158	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_S	Continuous Source Current[1,5]	V_G=V_D=0V , Force Current	---	---	-35	A
V_{SD}	Diode Forward Voltage[2]	V_{GS}=0V , I_S=-1A , T_J=25℃	---	---	-1.2	V
t_{rr}	Reverse Recovery Time	IF=-15A , dI/dt=100A/μs , T_J=25℃	---	12.4	---	nS
Q_{rr}	Reverse Recovery Charge		---	5	---	nC

Note :
1.The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
2.The data tested by pulsed , pulse width ≦ 300us , duty cycle ≦ 2%
3.The EAS data shows Max. rating . The test condition is V_{DD}=-20V,V_{GS}=-10V,L=0.5mH,I_{AS}=-18A
4.The power dissipation is limited by 150℃ junction temperature
5.The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.

Typical Characteristics

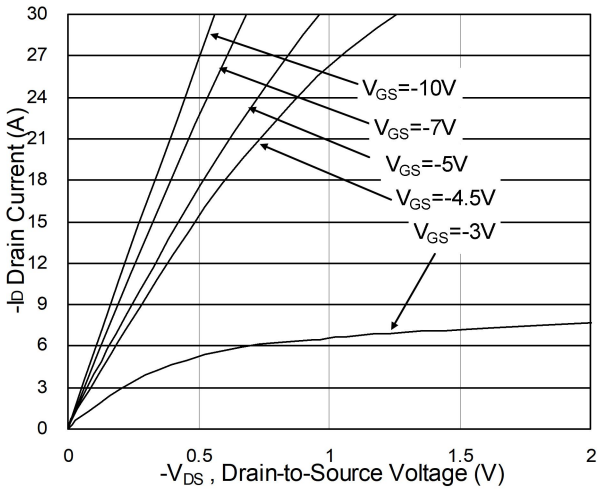


Fig.1 Typical Output Characteristics

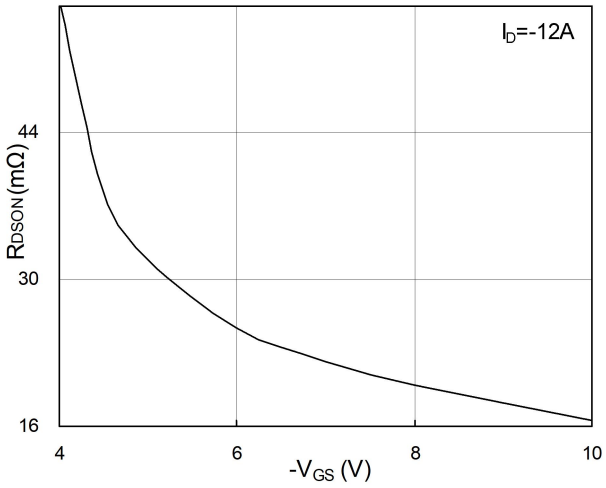


Fig.2 On-Resistance v.s Gate-Source

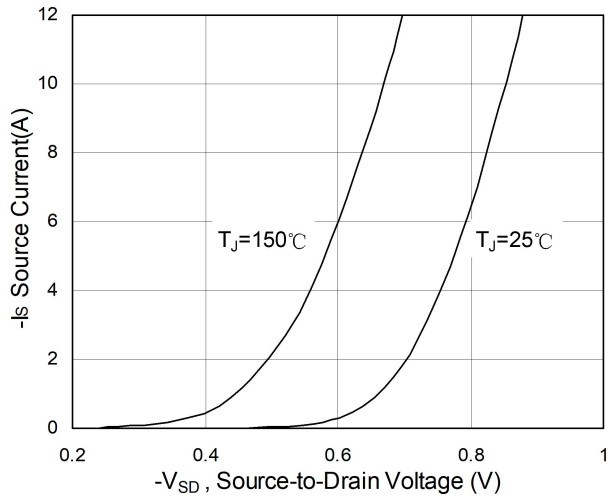


Fig.3 Forward Characteristics of Reverse

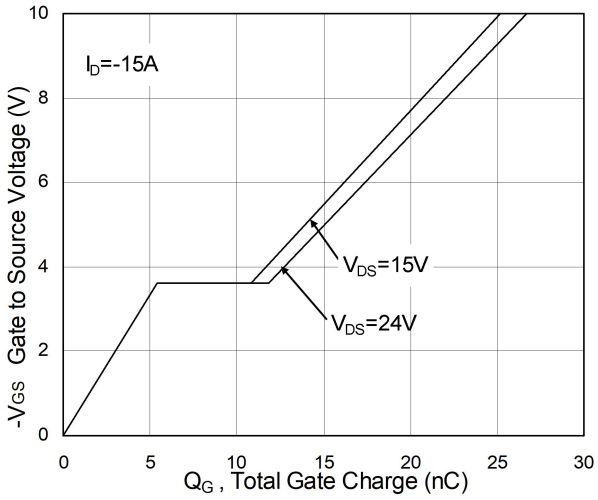


Fig.4 Gate-Charge Characteristics

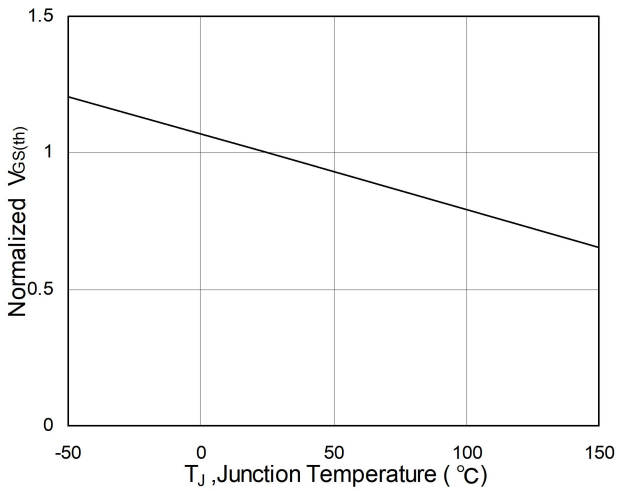


Fig.5 Normalized $V_{GS(th)}$ v.s T_J

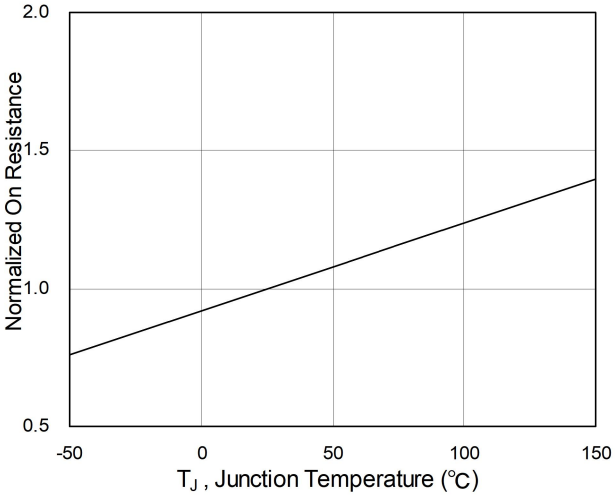


Fig.6 Normalized R_{DSON} v.s T_J

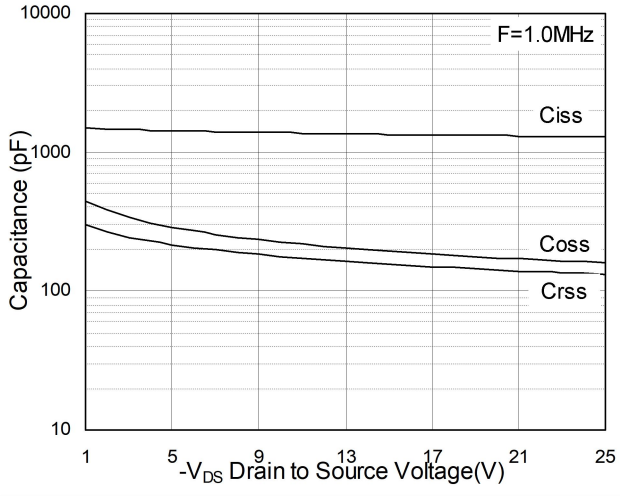


Fig.7 Capacitance

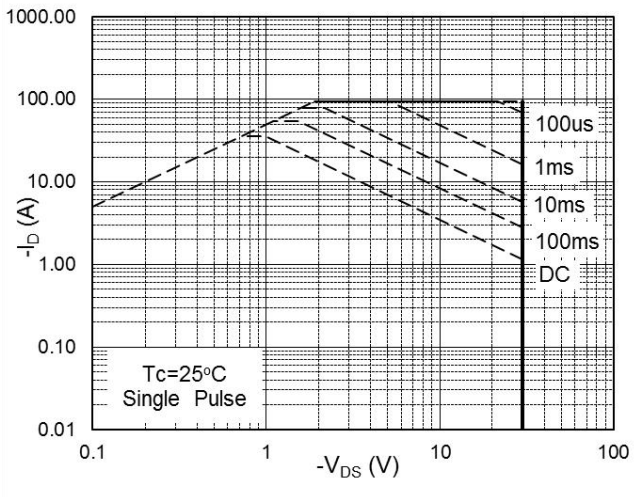


Fig.8 Safe Operating Area

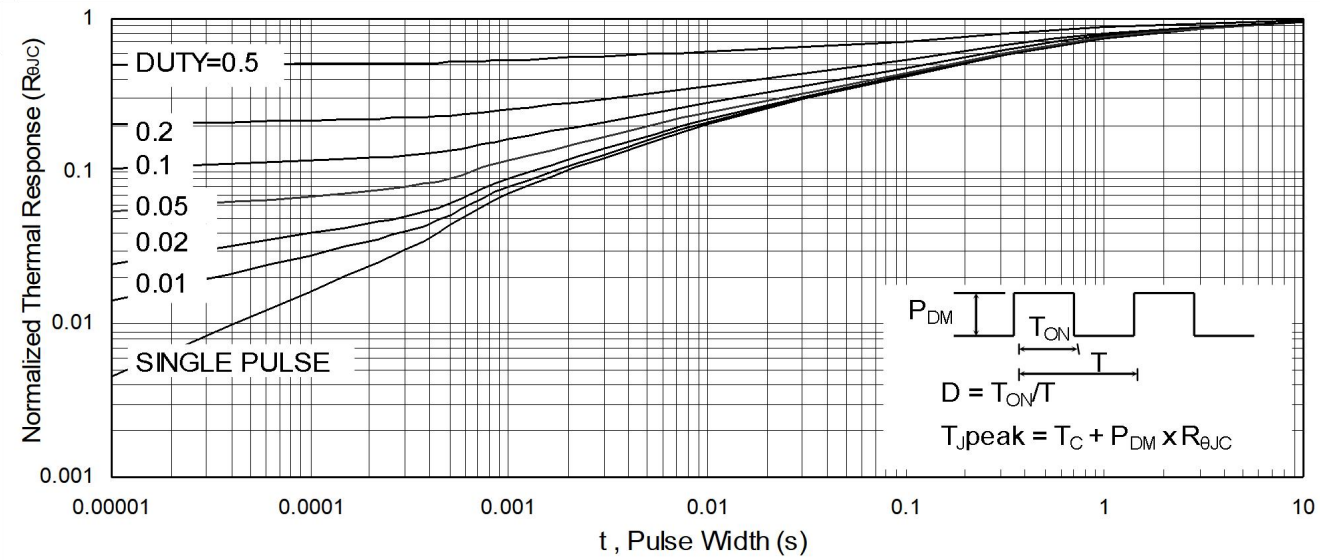


Fig.9 Normalized Maximum Transient Thermal Impedance

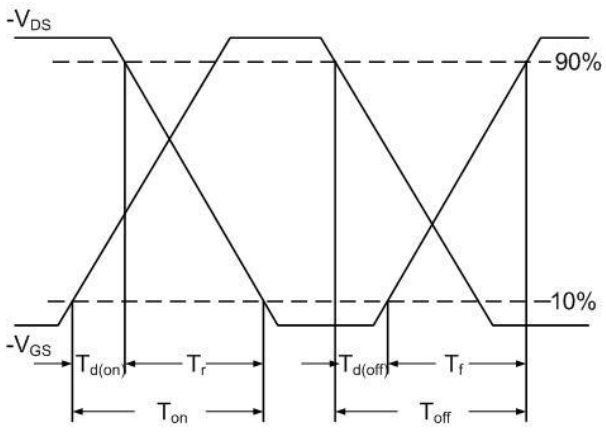


Fig.10 Switching Time Waveform

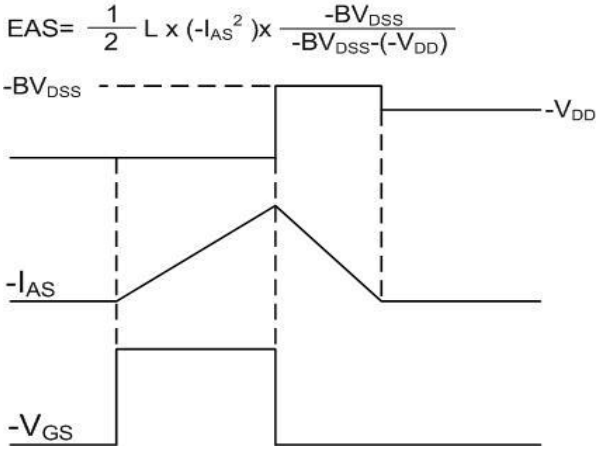
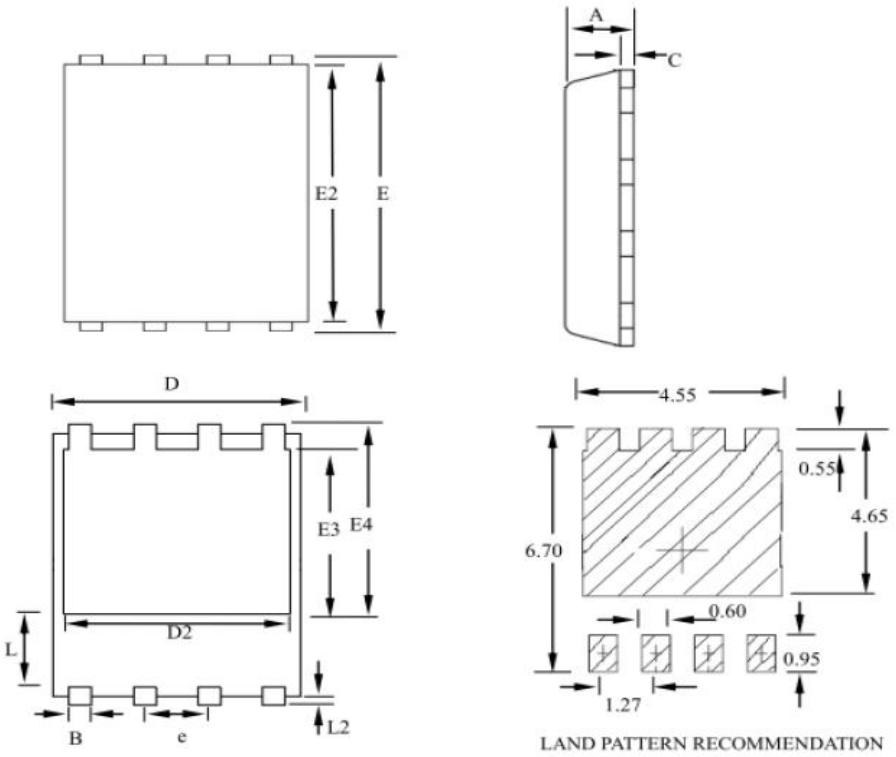


Fig.11 Unclamped Inductive Switching Waveform

Ordering Information

Part Number	Package code	Packaging
HSBA3103	PRPAK5*6	3000/Tape&Reel



SYMBOLS	MILLIMETERS			INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	0.80	--	1.20	0.031	--	0.047
B	0.30	--	0.51	0.012	--	0.020
C	0.15	--	0.35	0.006	--	0.014
D	4.80	--	5.30	0.189	--	0.209
D2	3.61	--	4.35	0.142	--	0.171
E	5.90	--	6.35	0.232	--	0.250
E2	5.42	--	5.90	0.213	--	0.232
E3	3.23	--	3.90	0.127	--	0.154
E4	3.69	--	4.55	0.145		0.179
L	0.61	--	1.80	0.024	--	0.071
L2	0.05	--	0.36	0.002	--	0.014
e	--	1.27	--	--	0.050	--