

PDFN5060 Plastic-Encapsulate MOSFETS

Features

- $V_{DS}=100V$
- $I_D=94A$
- $R_{DS(on)}@V_{GS}=10V < 7.5m\Omega$
- $R_{DS(on)}@V_{GS}=4.5V < 9.5m\Omega$
- High density cell design for ultra low Rdson
- Voltage controlled small signal switch
- Fast Switching Speedze

Drain-source Voltage

100 V

Drain Current

94 Ampere

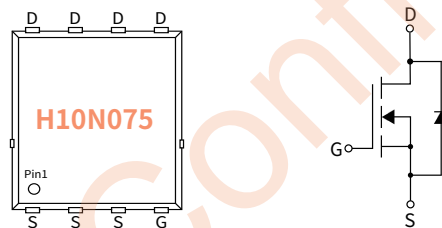
Applications

- Battery protection
- Load switch
- Power management

Mechanical Data

- Case: PDFN5060
Molding compound meets UL 94V-0 flammability rating, RoHS-compliant,halogen-free
- Terminals: Solder plated, solderable per MIL-STD-750,Method 2026

Function Diagram



Ordering Information

PACKAGE	PACKAGE CODE	UNIT WEIGHT(g)	REEL(pcs)	BOX(pcs)	CARTON(pcs)	DELIVERY MODE
PDFN5060	R3	0.09	3000	6000	60000	13"

Maximum Ratings (Ta=25°C Unless otherwise specified)

PARAMETER	SYMBOL	UNIT	VALUE
Drain-source Voltage	V_{DS}	V	100
Gate-source Voltage	V_{GS}	V	± 20
Drain Current	I_D	A	94
Pulsed Drain Current ⁽¹⁾	I_{DM}	A	390
Total Power Dissipation	P_D	W	114
Single pulse avalanche energy ⁽²⁾	EAS	mJ	306
Junction temperature	T_J	°C	-55 ~+150
Storage temperature	T_{stg}	°C	-55 ~+150
Thermal Resistance Junction-to-Case	$R_{\theta JC}$	°C / W	1.1

● Static Parameter Characteristics (T_j=25°C Unless otherwise specified)

PARAMETER	SYMBOL	Condition	UNIT	Min	Typ	Max
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} = 0V, I _D =250μA	V	100	—	—
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =100V, V _{GS} =0V	μA	—	—	1.0
Gate-Body Leakage Current	I _{GSS}	V _{GS} = ±20V, V _{DS} =0V	nA	—	—	±100
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D =250μA	V	1.3	1.8	2.4
Static Drain-Source On-Resistance ⁽³⁾	R _{DS(on)}	V _{GS} = 10V, I _D =40A	mΩ	—	5.4	7.5
		V _{GS} = 4.5V, I _D =30A		—	7	9.5
Forward Transconductance	g _{FS}	V _{DS} =5.0V, I _D =40A	S	—	45	—

● Dynamic Parameters

PARAMETER	SYMBOL	Condition	UNIT	Min	Typ	Max
Input Capacitance	C _{iss}	V _{DS} =50V, V _{GS} =0V, f=1MHZ	pF	—	2800	—
Output Capacitance	C _{oss}			—	600	—
Reverse Transfer Capacitance	C _{rss}			—	20	—

● Switching Parameters

PARAMETER	SYMBOL	Condition	UNIT	Min	Typ	Max
Turn-on Delay Time	t _{D(on)}	V _{GS} =10V, V _{DD} =50V, I _D =40A, R _{GEN} =3Ω	nS	—	10	—
Turn-on Rise Time	t _r		nS	—	36	—
Turn-off Delay Time	t _{D(off)}		nS	—	33	—
Turn-off fall Time	t _f		nS	—	32	—
Total Gate Charge	Q _g	V _{DS} =50V, I _D =40A V _{GS} =10V	nC	—	41	—
Gate-Source Charge	Q _{gs}		nC	—	8.3	—
Gate-Drain Charge	Q _{gd}		nC	—	3.5	—

● Drian-Source Diode Characteristics

PARAMETER	SYMBOL	Condition	UNIT	Min	Typ	Max
Diode Forward Voltage	V _{SD}	I _S =40A, V _{GS} =0V	V	—	—	1
Maximum Body-Diode Continuous Current	I _S	—	A	—	—	94
Reverse Recovery time	t _{rr}	I _{SD} =40A di/dt=100A/us	nS	—	32	—
Reverse Recovery Charge	Q _{rr}		nC	—	23	—

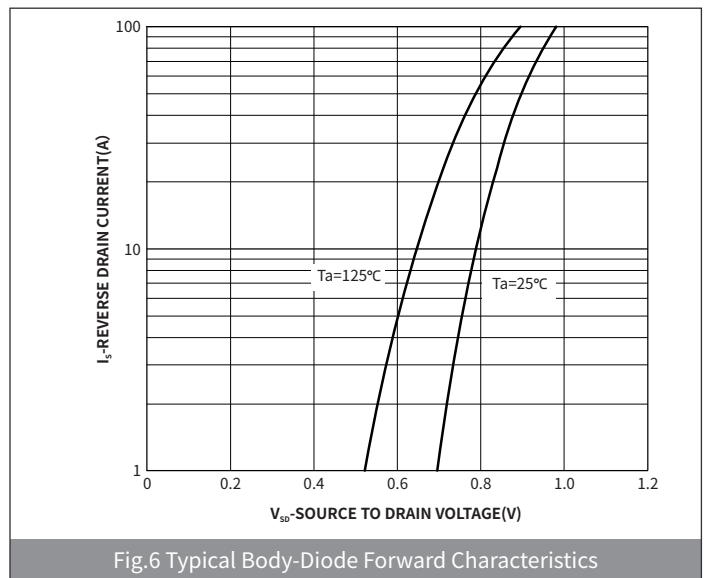
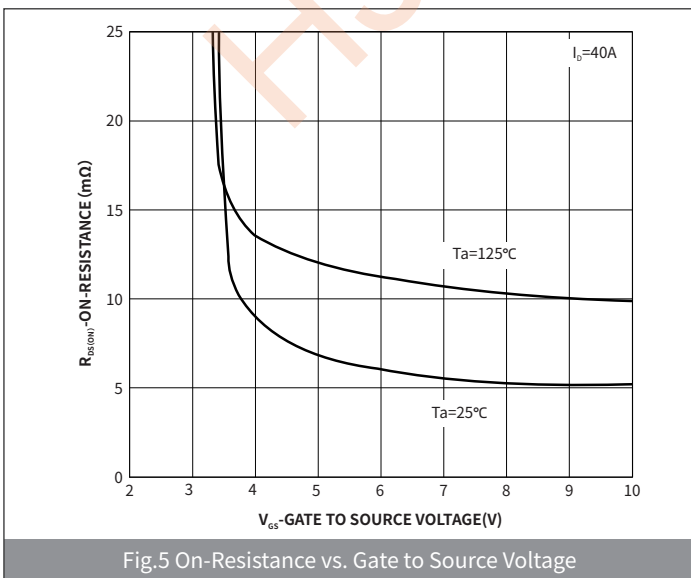
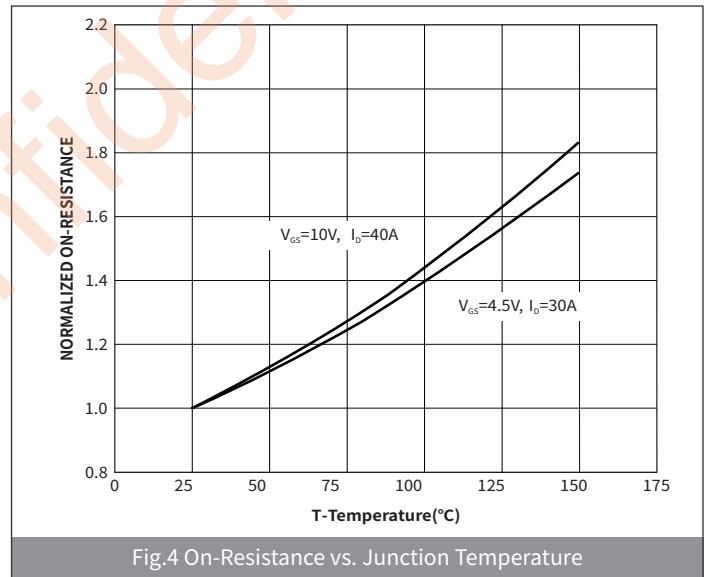
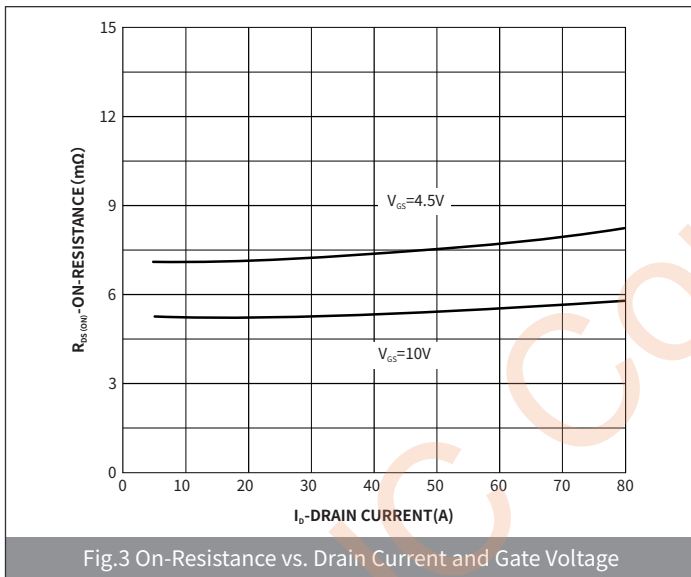
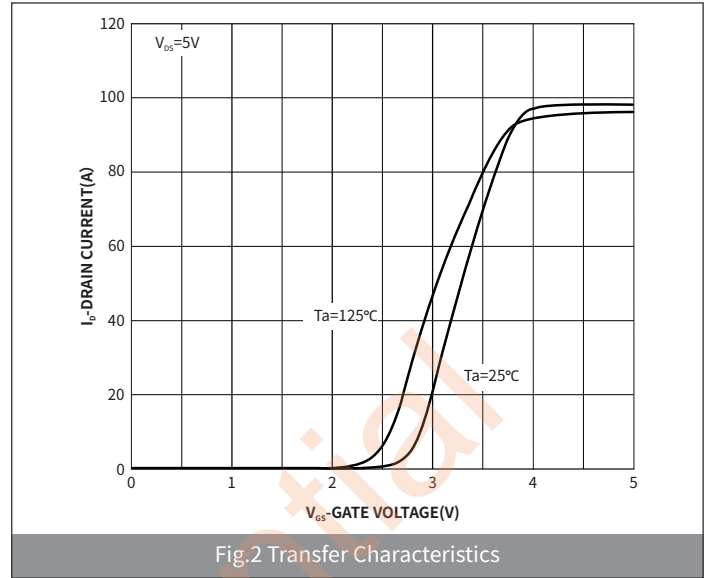
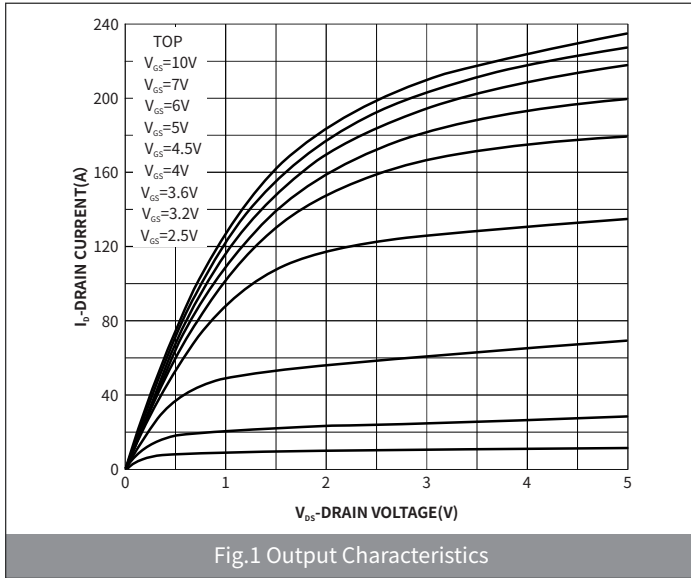
Note :

(1)Repetitive Rating: Pulse width limited by maximum junction temperature.

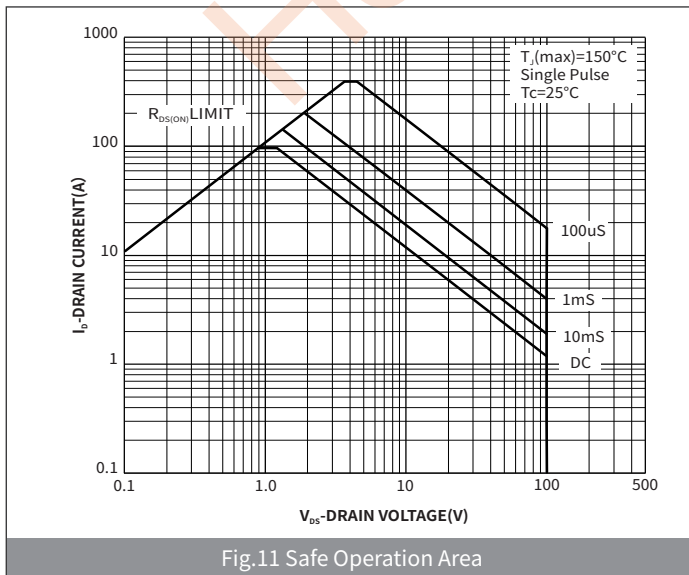
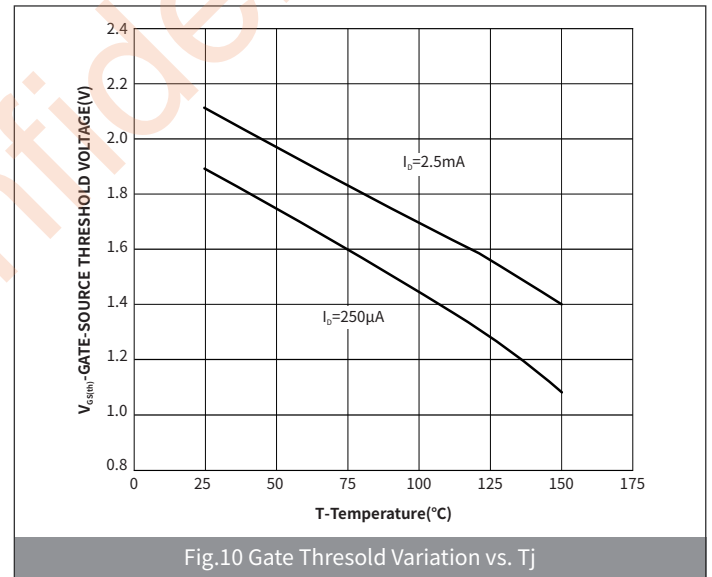
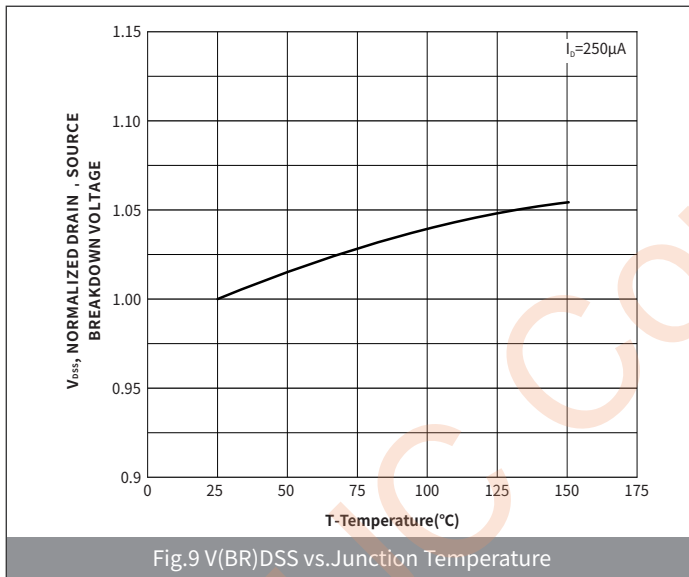
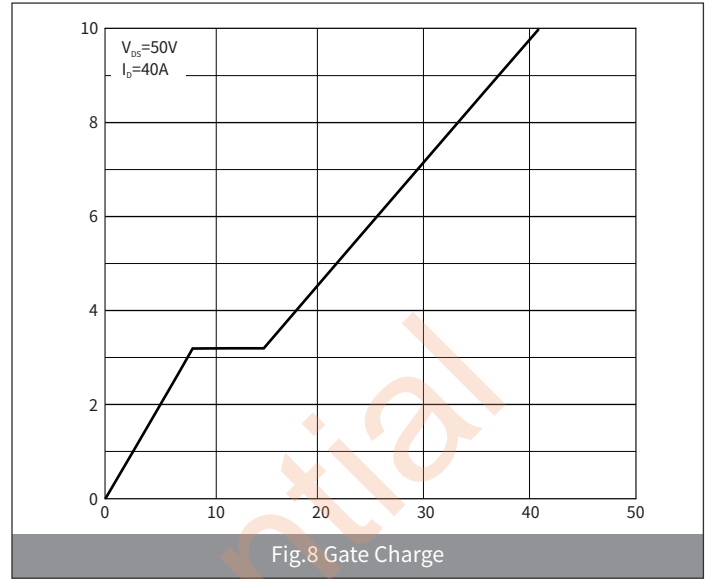
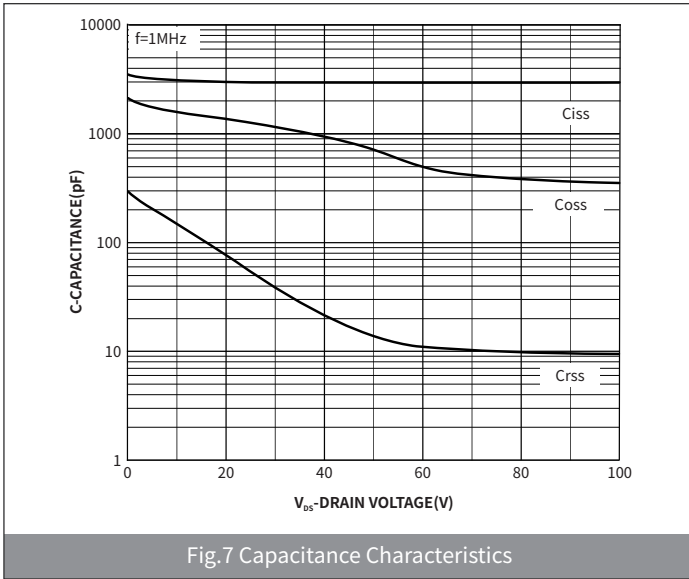
(2)EAS condition : T_j=25°C ,V_{DD}=50V,V_G=10V,L=0.5mH,I_{AS}=35A,R_G=25Ω.

(3)Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 2%.

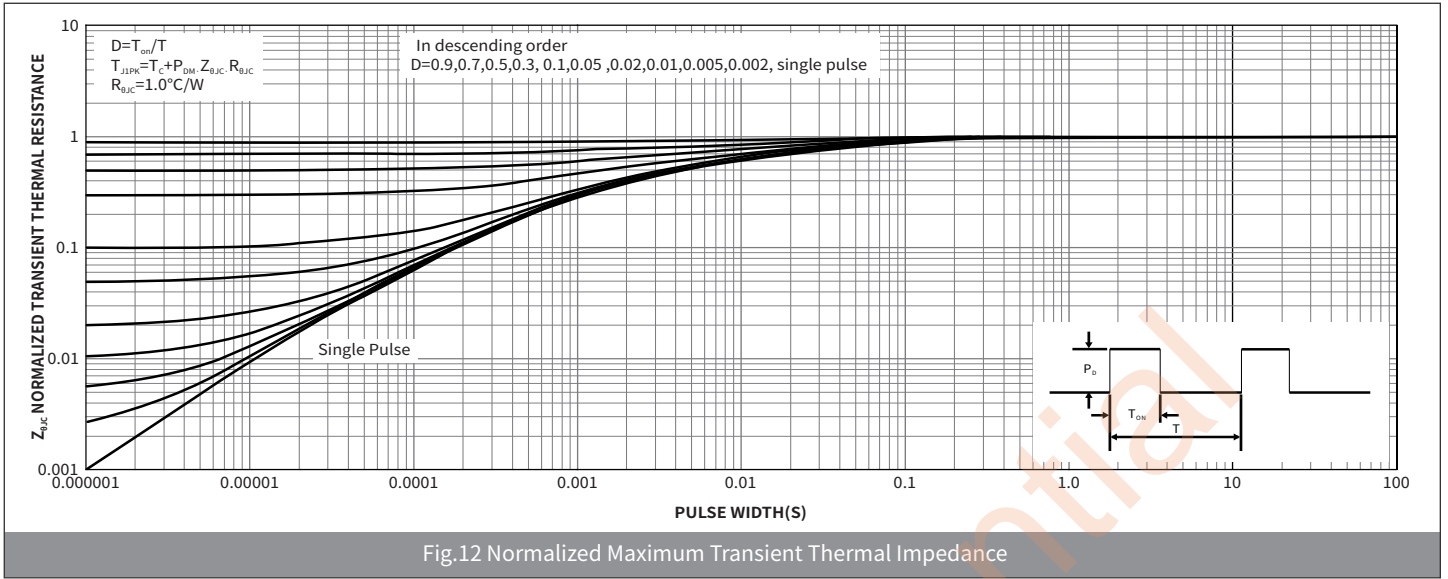
● Ratings And Characteristics Curves (Ta=25°C Unless otherwise specified)



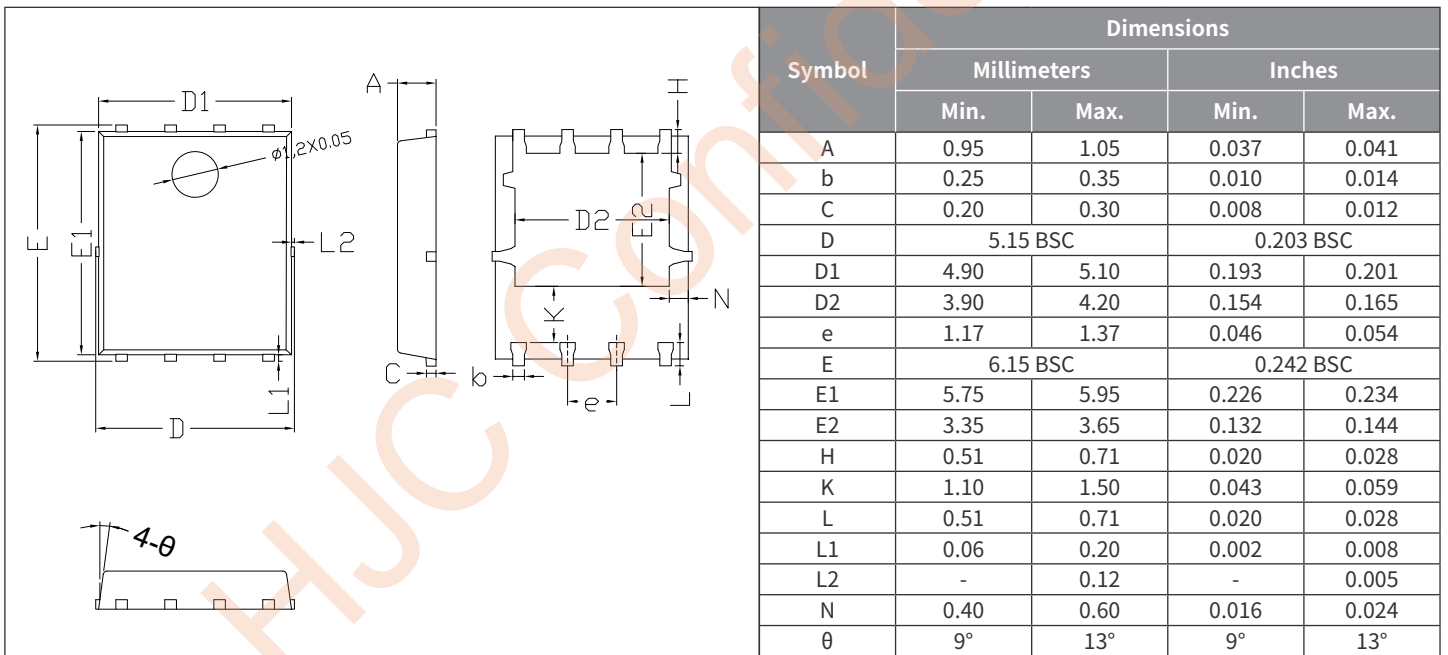
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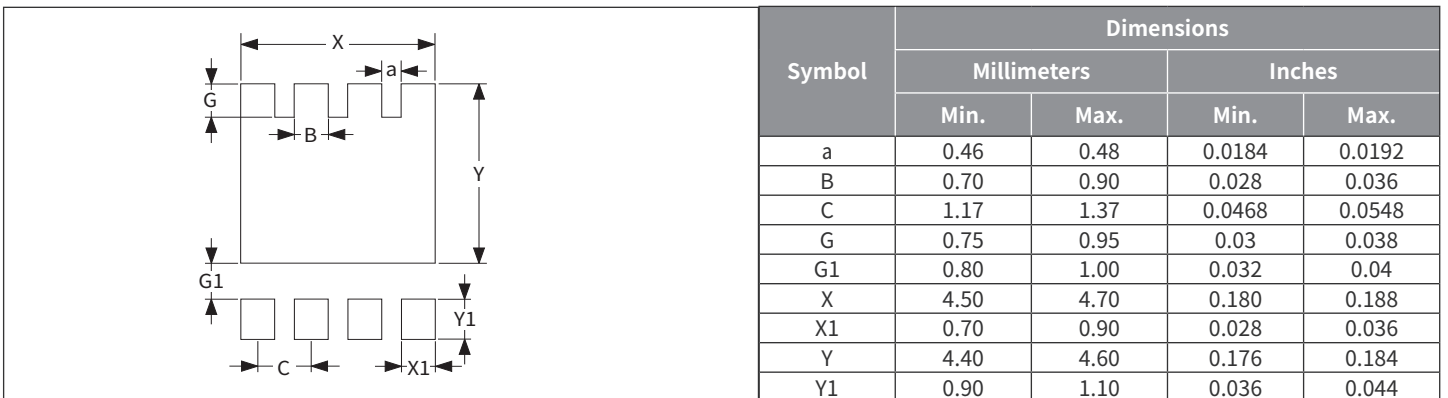
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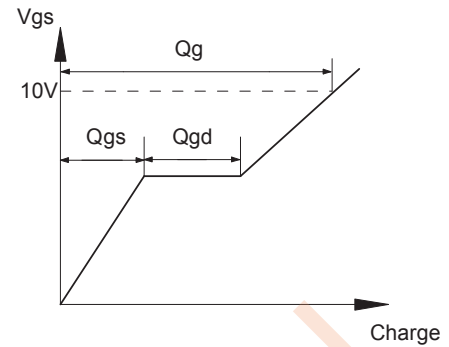
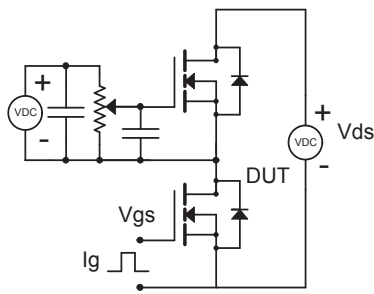
● Package Outline Dimensions (PDFN5060)



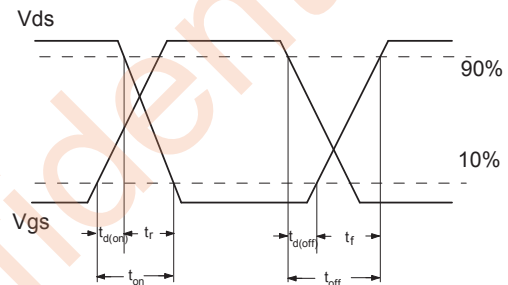
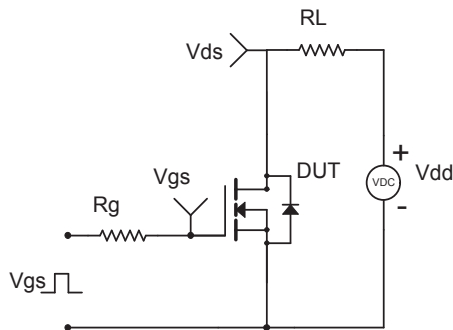
● Suggested Pad Layout



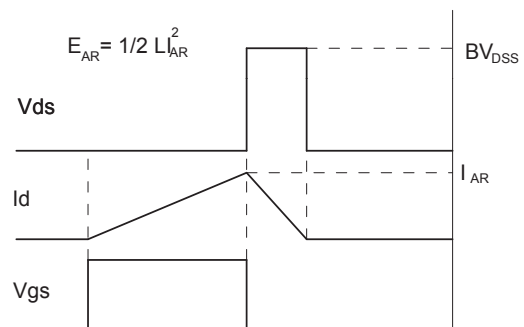
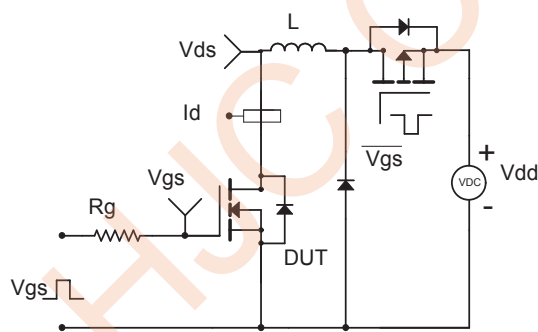
1. Gate Charge Test Circuit & Waveforms



2. Resistive Switching Test Circuit & Waveforms



3. Unclamped Inductive Switching (UIS) Test Circuit & Waveforms



4. Diode Recovery Test Circuit & Waveforms

