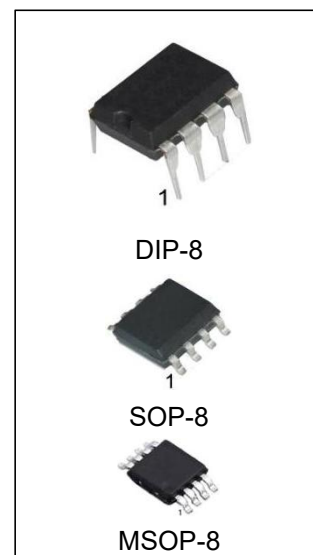


CMOS Voltage Converters

Features

- Simple Conversion of +5V Logic Supply to $\pm 5V$ Supplies
- Simple Voltage Multiplication ($V_{OUT} = (-) nV_{IN}$)
- Typical Open Circuit Voltage Conversion Efficiency 99.9%
- Typical Power Efficiency 98%
- Wide Operating Voltage Range 1.5V to 10V
- Easy to Use - Requires Only 2 External Non-Critical Passive Components



Ordering Information

DEVICE	Package Type	MARKING	Packing	Packing Qty
LMC7660N	DIP-8	LMC7660	TUBE	2000pcs/Box
LMC7660M/TR	SOP-8	LMC7660	REEL	2500pcs/Reel
LMC7660MM/TR	MSOP-8	7660	REEL	3000pcs/Reel

CMOS Voltage Converters

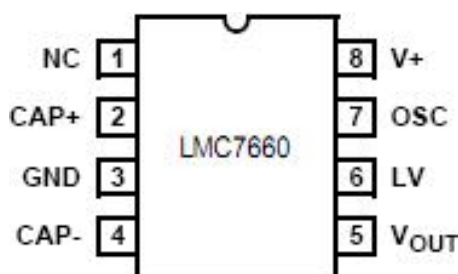
The LMC7660 are monolithic CMOS power supply circuits which offer unique performance advantages over previously available devices. The LMC7660 performs supply voltage conversions from positive to negative for an input range of +1.5V to +10V resulting in complementary output voltages of -1.5V to -10V. Only 2 noncritical external capacitors are needed for the charge pump and charge reservoir functions. The LMC7660 can also be connected to function as voltage doublers and will generate output voltages up to +18.6V with a +10V input.

Contained on the chip are a series DC supply regulator, RC oscillator, voltage level translator, and four output power MOS switches. A unique logic element senses the most negative voltage in the device and ensures that the output N-Channel switch source-substrate junctions are not forward biased. This assures latchup free operation. The oscillator, when unloaded, oscillates at a nominal frequency of 10kHz for an input supply voltage of 5.0V. This frequency can be lowered by the addition of an external capacitor to the "OSC" terminal, or the oscillator may be over driven by an external clock. The "LV" terminal may be tied to GROUND to bypass the internal series regulator and improve low voltage (LV) operation. At medium to high voltages (+3.5V to +10V), the LV pin is left floating to prevent device latchup.

Applications

- On Board Negative Supply for Dynamic RAMs
- Localized μ Processor (8080 Type) Negative Supplies
- Inexpensive Negative Supplies
- Data Acquisition Systems

Pinouts



Pin Description

Name	Pin#	Function
NC	1	
CAP+	2	"+" Capacitor Plate
GND	3	Ground
CAP-	4	"-" Capacitor Plate
VOUT	5	Output Voltage
LV	6	Low Supply Voltage
OSC	7	Oscillator
V+	8	Supply Voltage

Absolute Maximum Ratings

Condition	Min	Max
Supply Voltage LMC7660	-	+10.5V
LV and OSC Input Voltage	-0.3V to (V ₊ +0.3V) for V ₊ <5.5V	
	(V ₊ -5.5V) to (V ₊ +0.3V) for V ₊ >5.5V	
Current into LV	20μA for V ₊ >3.5V	
Temperature Range	0°C	70°C
Thermal Resistance, θ _{JA} (°C/W)	-	150
Maximum Storage Temperature Range	-65°C	150°C
Lead Temperature (Soldering, 10 seconds)	-	260°C

Note: Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is intended to be functional, but specific performance is not ensured.

Electrical Characteristics (V₊ = 5V, T_A = 25°C, C_{OSC} = 0, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNITS
Supply Current	I ₊	R _L =∞	-	170	500	μA
Supply Voltage Range - Lo	V _{L+}	MIN ≤ T _A ≤ MAX, R _L = 10kΩ, LV to GND	1.5	-	3.5	V
Supply Voltage Range - Hi	V _{L+}	MIN ≤ T _A ≤ MAX, R _L = 10kΩ, LV to Open	3.0	-	10	V
Output Source Resistans	R _{OUT}	I _{OUT} = 20mA, T _A = 25°C	-	55	100	Ω
		I _{OUT} = 20mA, 0°C ≤ T _A ≤ 70°C	-	-	120	
		V ₊ = 2V, I _{OUT} = 3mA, LV to GND, 0°C ≤ T _A ≤ 70°C	-	-	300	
Oscillator Frequency	f _{OSC}		8	-	18	kHz
Power Efficiency	P _{EF}	R _L = 5kΩ	95	98	-	%
Voltage Conversion Efficiency	V _{OUTEF}	R _L = ∞	97	99.9	-	%
Oscillator Impedance	Z _{OSC}	V ₊ = 2V	-	1.0	-	MΩ
		V ₊ = 5V	-	100	-	kΩ

TYPICAL APPLICATION CIRCUITS

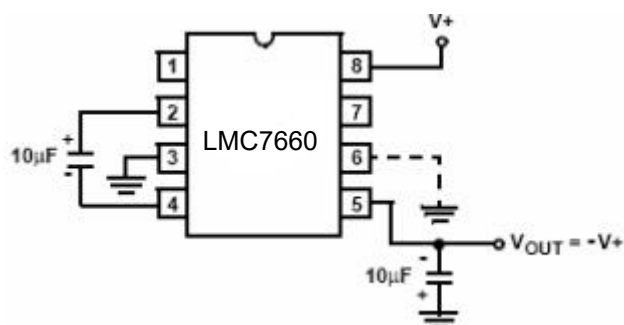


FIGURE 1. SIMPLE NEGATIVE CONVERTER

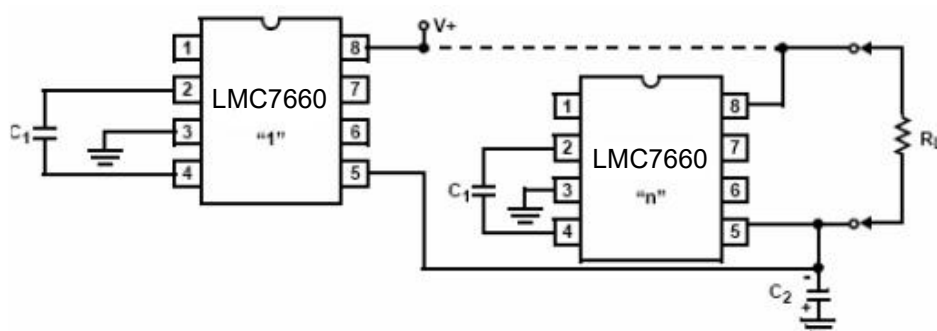


FIGURE 2. PARALLELING DEVICES

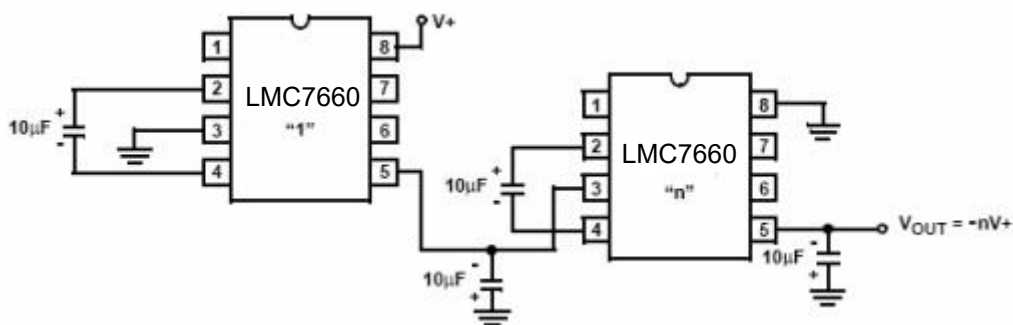


FIGURE 3. CASCADING DEVICES FOR INCREASED OUTPUT VOLTAGE

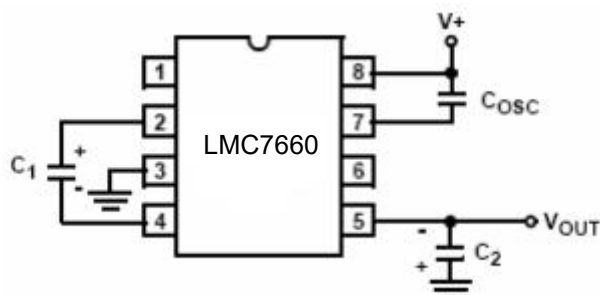


FIGURE 4. LOWERING OSCILLATOR FREQUENCY

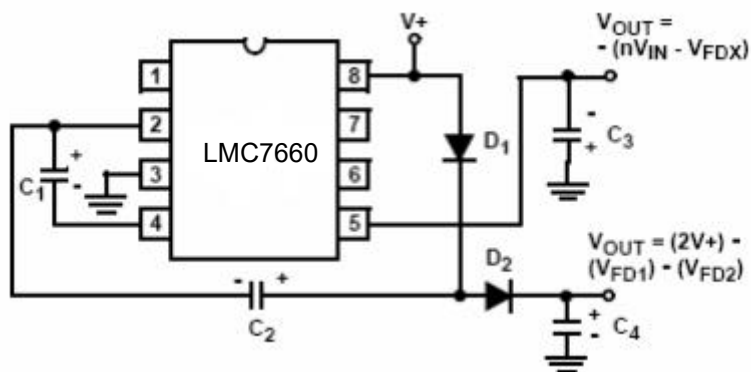


FIGURE 5. COMBINED NEGATIVE VOLTAGE CONVERTER AND POSITIVE DOUBLER

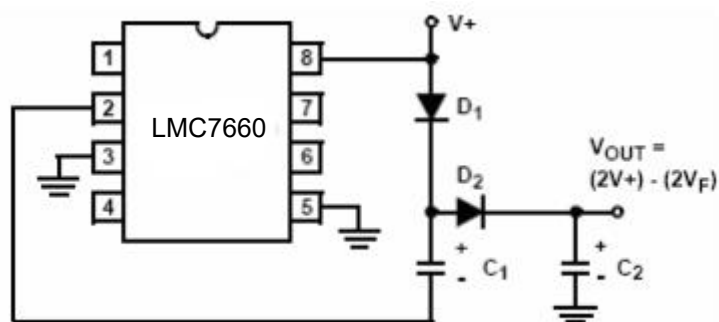


FIGURE 6. POSITIVE VOLT DOUBLER

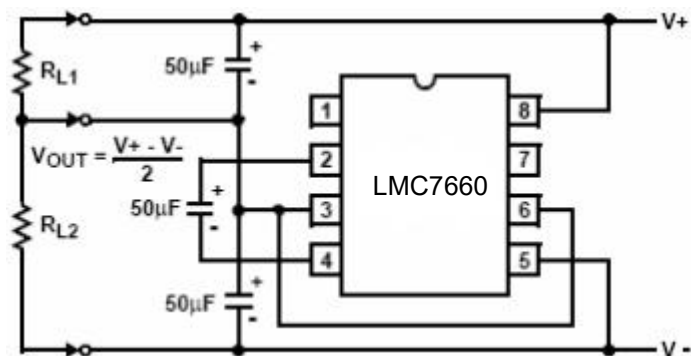
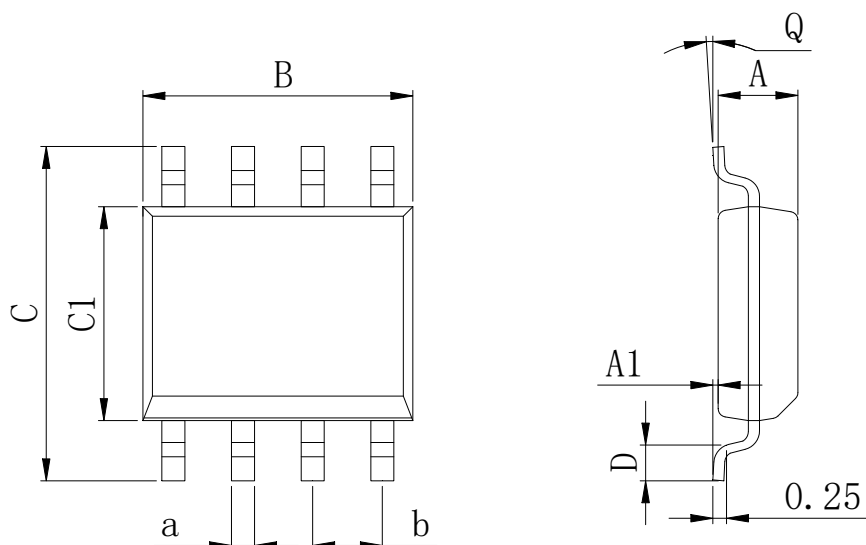


FIGURE 7. SPLITTING A SUPPLY IN HALF

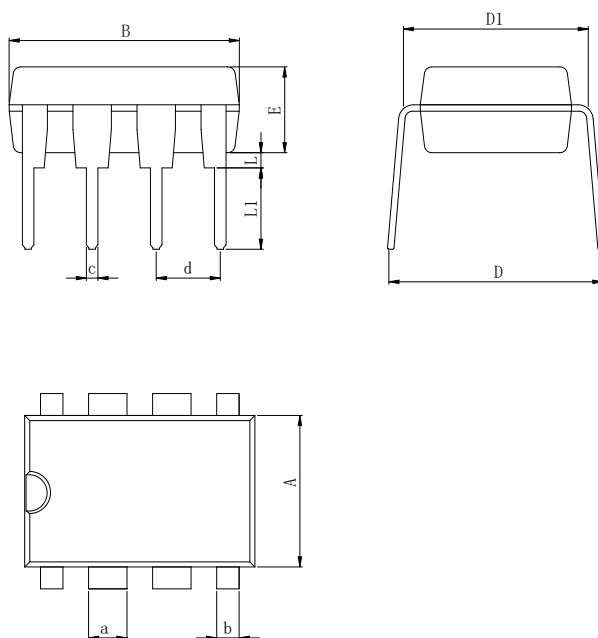
Physical Dimensions

SOP-8



Dimensions In Millimeters(SOP-8)									
Symbol:	A	A1	B	C	C1	D	Q	a	b
Min:	1.35	0.05	4.90	5.80	3.80	0.40	0°	0.35	1.27 BSC
Max:	1.55	0.20	5.10	6.20	4.00	0.80	8°	0.45	

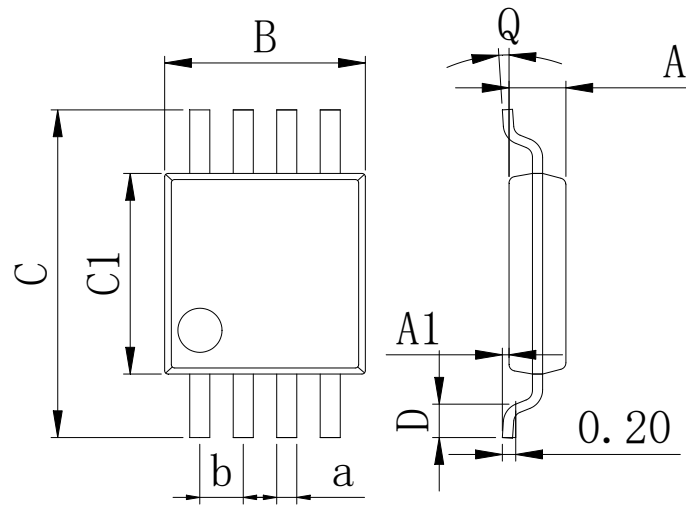
DIP-8



Dimensions In Millimeters(DIP-8)											
Symbol:	A	B	D	D1	E	L	L1	a	b	c	d
Min:	6.10	9.00	8.10	7.42	3.10	0.50	3.00	1.50	0.85	0.40	2.54 BSC
Max:	6.68	9.50	10.9	7.82	3.55	0.70	3.60	1.55	0.90	0.50	

Physical Dimensions

MSOP-8



Dimensions In Millimeters(MSOP-8)

Symbol:	A	A1	B	C	C1	D	Q	a	b
Min:	0.80	0.05	2.90	4.75	2.90	0.35	0°	0.25	0.65 BSC
Max:	0.90	0.20	3.10	5.05	3.10	0.75	8°	0.35	

Revision History

REVISION NUMBER	DATE	REVISION	PAGE
V1.0	2014-6	New	1-9
V1.1	2018-9	Update encapsulation type、Updated DIP-8 dimension	1、 6
V1.2	2025-4	Update input voltage range 10V	3

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