# MSKSEMI 美森科













ESD

S

TSS

MOV

GDT

PLED

BT138-800x-MS(252)

Product specification





#### **FEATURES**

- High current 12 A RMS current Triac
- Low thermal resistance
- High commutation or very high commutation capability

#### **APPLICATIONS**

- General purpose motor control circuits
- Phase control operations in light dimmers and motor speed controllers
- Home appliances

#### **Reference News**

TO-252	Schematic Symbol	MARKING		
1 3	T2(2) O T1(1) G (3)	MSKSEMI BT138-800* MS***		

### **ABSOLUTE MAXIMUM RATINGS**

Parameter		Symbol	Value	Unit		
Repetitive peak off-state voltage (T <sub>j</sub> =25℃)		$V_{DRM}$	800	V		
Repetitive peak reverse voltage (T <sub>j</sub> =	Repetitive peak reverse voltage (T <sub>j</sub> =25℃)		Repetitive peak reverse voltage (T <sub>j</sub> =25°C)		800	V
RMS on-state current (T <sub>c</sub> =95℃)		I <sub>T(RMS)</sub>	12			
Non repetitive surge peak on-state (full cycle, F=50Hz)	Non repetitive surge peak on-state current (full cycle, F=50Hz)		95	A		
Pt value for fusing (tp=10ms)	Pt value for fusing (tp=10ms)		45	A <sup>2</sup> S		
Critical rate of rise of on-state	I - II-III	-11/-14	50	.,		
current (I <sub>G</sub> =2*I <sub>GT</sub> )	IV	dl/dt	10	- A/μs		
Peak gate current		I <sub>GM</sub>	2	A		
Average gate power dissipation		$P_{G(AV)}$	0.5	W		
Peak gate power		$P_{GM}$	5	W		
Operating junction temperature range		T <sub>j</sub>	-40~+125	10		
Storage junction temperature range	T <sub>STG</sub>	-40~+150	${\mathbb C}$			



# **ELECTRICAL CHARACTERISTICS** ( $T_i$ =25 $^{\circ}$ C unless otherwise specified)

Or was least	Total Constitution	0 - 1 1	Value				
Symbol	Test Condition	Quadrant	D	E	F	G	Unit
		I - II-III	≤5	≤10	≤25	≤50	
l <sub>GT</sub>	$V_D = 12V, R_L = 33\Omega$	IV	≤10	≤25	≤70	≤100	mA
V <sub>GT</sub>		ALL		≤1.5			٧
$V_{\sf GD}$	$V_D = V_{DRM,} R_L = 3.3 K\Omega, T_j = 125 ^{\circ}C$	ALL	≥0.2			٧	
I <sub>H</sub>	l <sub>⊤</sub> =100mA		≤10	≤25	≤30	≤60	
	l <sub>G</sub> =1.2l <sub>GT</sub>	I - III- IV	≤15	≤30	≤40	≤60	mA
l,	G GI	II	≤20	≤40	≤80	≤90	
dV <sub>D</sub> /dt	V <sub>D</sub> =67%V <sub>DRM</sub> ,T <sub>j</sub> =125℃		≥20	≥50	≥50	≥200	V/µs
V <sub>TM</sub>	I <sub>τм</sub> =15A,tp=380μs		≤1.6				V
I <sub>DRM</sub>	\/ -\/ \/ \/ -\/	T <sub>j</sub> =25℃	≤5			uA	
I <sub>RRM</sub>	$V_D = V_{DRM}$ , $V_R = V_{RRM}$	T <sub>j</sub> =125℃		:	≤1		mA

# THERMAL RESISTANCES

Symbol	Parameter	Value	Unit
R <sub>th(j-c)</sub>	Junction to case(AC)	1.7	°C/W
R <sub>th(j-a)</sub>	Junction to ambient	70	°C/W



## PARAMETER CHARACTERISTIC CURVE

FIG.1 Maximum power dissipation versus RMS on-state current

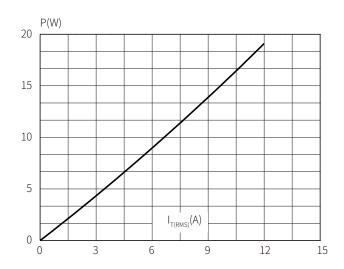


FIG.2: RMS on-state current versus case temperature

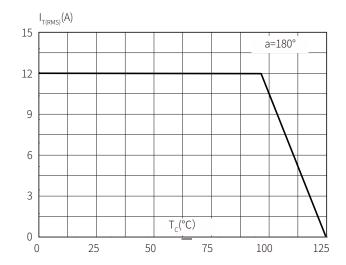


FIG.3: Surge peak on-state current versus number of cycles

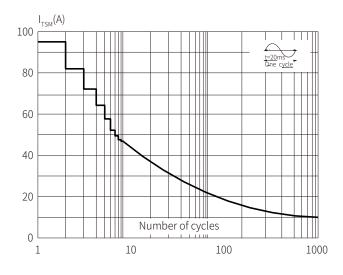


FIG.4 On-state characteristics (maximum values)

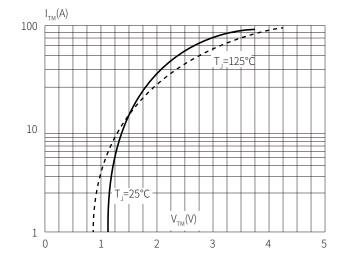




FIG.5: Non-repetitive surge peak on-state currentfor a sinusoidal pulse with width tp<20ms and corresponding value of I²t ( I - II - III:dI/dt<50A/ $\mu$ s; IV:dI/dt<10A/ $\mu$ s)

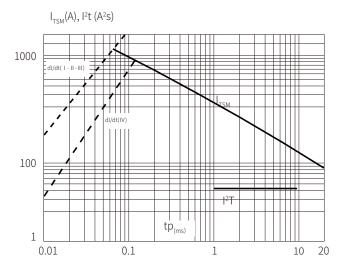


FIG.6 Relative variations of gate trigger current versus junction temperature

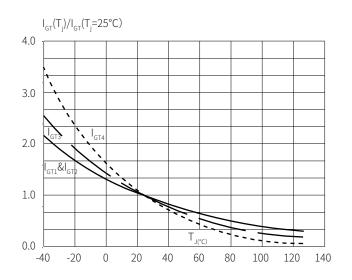


FIG.7 Relative variations of holding current versus junction temperature

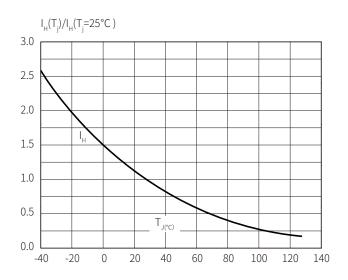
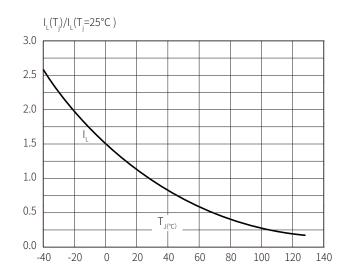
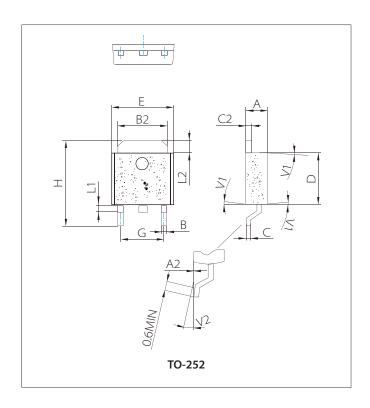


FIG.8 Relative variations of atching current versus junction temperature





## PACKAGE MECHANICAL DATA



	Dimensions					
Ref.	Millimeters			Inches		
	Min.	Тур.	Max.	Min.	Тур.	Max.
Α	2.20		2.40	0.086		0.095
A2	0.03		0.23	0.001		0.009
В	0.55		0.65	0.022		0.026
B2	5.10		5.40	0.200		0.213
С	0.45		0.62	0.018		0.024
C2	0.48		0.62	0.019		0.024
D	6.00		6.20	0.236		0.244
Е	6.40		6.70	0.252		0.264
G	4.40		4.70	0.173	0.1	0.185
Н	9.35		10.6	0.368		0.417
L1	1.30		1.70	0.051	0.143	0.067
L2	1.37		1.50	0.054		0.059
L1		4°			0.130	
V2	0°		8°	0°		8°

# **Order information**

P/N	PKG	QTY
BT138-800D-MS(252)	TO-252	2500PCS
BT138-800E-MS(252)	TO-252	2500PCS
BT138-800F-MS(252)	TO-252	2500PCS
BT138-800G-MS(252)	TO-252	2500PCS



#### **Attention**

- Any and all MSKSEMI Semiconductor products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your MSKSEMI Semiconductor representative nearest you before using any MSKSEMI Semiconductor products described or contained herein in such applications.
- MSKSEMI Semiconductor assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all MSKSEMI Semiconductor products described or contained herein.
- Specifications of any and all MSKSEMI Semiconductor products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer'sproducts or equipment.
- MSKSEMI Semiconductor. strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with someprobability. It is possiblethat these probabilistic failures could give rise to accidents or events that could endanger human lives, that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits anderror prevention circuitsfor safedesign, redundant design, and structural design.
- In the event that any or all MSKSEMI Semiconductor products (including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from theauthorities concerned in accordance with the above law.
- No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of MSKSEMI Semiconductor.
- Information (including circuit diagrams and circuit parameters) herein is for example only; it is not guaranteed for volume production. MSKSEMI Semiconductor believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.
- Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. Whendesigning equipment, referto the "Delivery Specification" for the MSKSEMI Semiconductor productthat you intend to use.