TAI-TECH

SMD Power Inductor

TMPC0603H-R15YG-D

1. Features

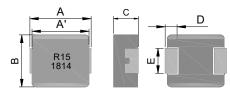
- 1. Carbonyl Powder.
- 2. Compact design.
- 3. High current , low DCR , high efficiency.
- 4. Very low acoustic noise and very low leakage flux noise.
- 5. High reliability.
- 6. 100% Lead(Pb)-Free and RoHS compliant.



2. Applications

Note PC power system , incl. IMVP-6 DC/DC converter .

3. Dimensions





Recommend PC Board Pattern

Series	A(mm)	A'(mm)	B(mm)	C(mm)	D(mm)	E(mm)
TMPC0603H	7.3±0.3	6.7±0.3	6.6±0.3	2.8±0.2	1.8±0.3	3.0±0.3

L(mm) G(mm) H(mm) 8.4

Note: 1. The above PCB layout reference only. 2. Recommend solder paste thickness at 0.15mm and above.

4. Part Numbering



A: Series

B: Dimension

C: Type Carbonyl Powder. D: Inductance R15=0.15uH

F: 印 D/C

Y=±30% E: Inductance Tolerance

印字:黑色.R15 及 D/C 1814(18 年,14 週期)(依實際生產日期而定)

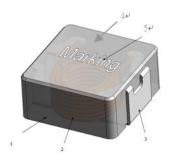
5. Specification

Part Number	Inductance I rm		s (A)	I sa	DCR (mΩ) Typ.	DCR (mΩ) Max.	
r art Humber	@ 0 A	Тур	Max	Тур	Max	@25°C	@25°C
TMPC0603H-R15YG-D	0.15	27.0	23.0	45.0	40.0	1.5	1.9

Note:

- 1. Test frequency: Ls: 100KHz /1.0V.
- $3. \ \ Testing\ Instrument (or\ equ): L:\ HP4284A, CH11025, CH3302, CH1320S, LCR\ METER\ /\ Rdc: CH16502, Agilent (33420A\ MICRO\ OHMMETER.)$
- 4. Heat Rated Current (Irms) will cause the coil temperature rise approximately $\,\Delta T$ of 40 $^{\circ}{\rm C}$
- 5. Saturation Current (Isat) will cause L0 to drop approximately 30%.
- 6. The part temperature (ambient + temp rise) should not exceed 125°C under worst case operating conditions. Circuit design, component, PCB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application.
- 7. Special inquiries besides the above common used types can be met on your requirement.

6. Material List



NO	Items	Materials
1	Core	Carbonyl Powder.
2	Wire	Polyester Wire or equivalent.
3	Clip	100% Pb free solder(Ni+SnPlating)
4	paint	Epoxy resin
5	Ink	Halogen-free ketone

7. Reliability and Test Condition

Item	Performance	Test Condition
Operating temperature	-40~+125℃ (Including self - temperature rise)	
Storage temperature	110~+40°ℂ,50~60%RH (Product with taping) 240~+125°ℂ (on board)	
Electrical Performance	Test	
Inductance	Refer to standard electrical characteristics list.	HP4284A,CH11025,CH3302,CH1320,CH1320S LCR Meter.
DCR	Note: to standard declinear characteristics list.	CH16502,Agilent33420A Micro-Ohm Meter.
Saturation Current (Isat)	Approximately △L30%.	Saturation DC Current (Isat) will cause L0 to drop $\triangle L(\%)$
Heat Rated Current (Irms)	Approximately △T40°C	Heat Rated Current (Irms) will cause the coil temperature rise \triangle T($\mathbb C$). 1.Applied the allowed DC current 2.Temperature measured by digital surface thermometer
Reliability Test		
Life Test		Preconditioning: Run through IR reflow for 2 times.(IPC/JEDECJ-STD-020DClassification Reflow Profiles) Temperature: 125±2°C(Inductor) Applied current: rated current Duration: 1000±12hrs Measured at room temperature after placing for 24±2 hrs.
Load Humidity	d Humidity	Preconditioning: Run through IR reflow for 2 times.(IPC/JEDECJ-STD-020DClassification Reflow Profiles) Humidity: 85±2% R.H, Temperature: 85°C±2°C Duration: 1000hrs Min. with 100% rated current Measured at room temperature after placing for 24±2 hrs.
Moisture Resistance	Appearance: No damage. Impedance: within±15% of initial value Inductance: within±10% of initial value Q: Shall not exceed the specification value. RDC: within ±15% of initial value and shall not exceed the specification value	Preconditioning: Run through IR reflow for 2 times.(IPC/JEDECJ-STD-020DClassification Reflow Profiles) 1. Baked at50°C for 25hrs, measured at room temperature after placing for 4 hrs. 2. Raise temperature to 65±2°C 90-100%RH in 2.5hrs, and keep 3 hours, cool down to 25°C in 2.5hrs. 3. Raise temperature to 65±2°C 90-100%RH in 2.5hrs, and keep 3 hours, cool down to 25°C in 2.5hrs,keep at 25°C for 2 hrs then keep at -10°C for 3 hrs 4. Keep at 25°C 80-100%RH for 15min and vibrate at the frequency of 10 to 55 Hz to 10 Hz, measure at room temperature after placing for 1~2 hrs.
Thermal shock		Preconditioning: Run through IR reflow for 2 times.(IPC/JEDECJ-STD-020DClassification Reflow Profiles) Condition for 1 cycle Step1: -40±2°C 30±5min Step2: 25±2°C ≤0.5min Step3: 125±2°C 30±5minNumber of cycles: 500
Vibration		Measured at room fempraturc after placing for 24±2 hrs. Preconditioning: Run through IR reflow for 2 times. (IPC/JEDECJ-STD-020DClassification Reflow Profiles) Oscillation Frequency: 10~2K~10Hz for 20 minutes Equipment: Vibration checker Total Amplitude:1.52mm±10% Testing Time: 12 hours(20 minutes, 12 cycles each of 3 orientations) ∘

Item	Performance	Test Condition			
Bending	Appearance : No damage.	Shall be mounted on a FR4 substrate of the following dimensions: >=0805 inch(2012mm):40x100x1.2mm <0805 inch(2012mm):40x100x0.8mm Bending depth: >=0805 inch(2012mm):1.2mm <0805 inch(2012mm):0.8mm duration of 10 sec.			
Shock	Inductance: within±10% of initial value Q: Shall not exceed the specification value. RDC: within ±15% of initial value and shall not	Type Peak value duration (D) Wave form (Vi)ft/sec			
Onder	exceed the specification value	SMD 50 11 Half-sine 11.3 Lead 50 11 Half-sine 11.3			
Solder ability	More than 95% of the terminal electrode should be covered with solder °	Preheat: 150°C,60sec. Solder: Sn96.5% Ag3% Cu0.5% Temperature: 245±5°C Flux for lead free: Rosin. 9.5% Dip time: 4±1sec Depth: completely cover the termination			
Resistance to Soldering Heat		Depth: completely cover the termination Temperature (°C) Time(s) Temperature ramp/immersion and emersion rate lead to ycles (solder temp) 10 ±1 25mm/s ±6 mm/s 1			
Terminal Strength	Appearance: No damage. Impedance: within±15% of initial value Inductance: within±10% of initial value Q: Shall not exceed the specification value. RDC: within ±15% of initial value and shall not exceed the specification value e	Preconditioning: Run through IR reflow for 2 times.(IPC/JEDEC J-STD-020DClassification Reflow Profiles With the component mounted on a PCB with the device to be tested, apply a force(-0805:1kg, <=0805:0.5kg)to the side of a device being tested. This force shall be applied for 60 + seconds. Also the force shall be applied gradually as not to apply a shock to the component being tested.			

Note: When there are questions concerning measurement result: measurement shall be made after 48 ± 2 hours of recovery under the standard condition.

8. Soldering and Mounting

(1) Soldering

Mildly activated rosin fluxes are preferred. The minimum amount of solder can lead to damage from the stresses caused by the difference in coefficients of expansion between solder, chip and substrate. TAI-TECH terminations are suitable for re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.

(2) Solder re-flow:

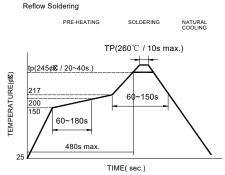
Recommended temperature profiles for re-flow soldering in Figure 1.

(3) Soldering Iron:

Products attachment with a soldering iron is discouraged due to the inherent process control limitations. In the event that a soldering iron must be employed the following precautions are recommended.

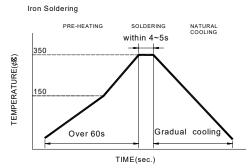
- Never contact the ceramic with the iron tip
- Use a 20 watt soldering iron with tip diameter of 1.0mm

- 355℃ tip temperature (max)
- 1.0mm tip diameter (max)
- Limit soldering time to 4~5sec.



Reflow times: 3 times max.

Fig.1

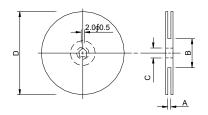


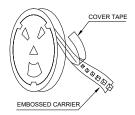
Iron Soldering times: 1 times max.

Fig.2

9. Packaging Information

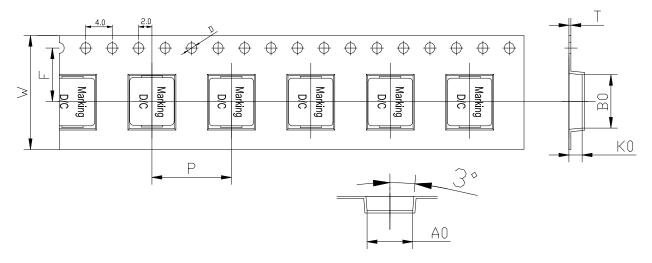
(1) Reel Dimension





Туре	A(mm)	B(mm)	C(mm)	D(mm)
13"x16mm	16.4+2/-0	100±2	13+0.5/-0.2	330

(2) Tape Dimension

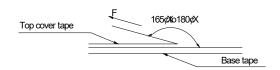


Series	Size	Bo(mm)	Ao(mm)	Ko(mm)	P(mm)	W(mm)	F(mm)	t(mm)	D(mm)
ТМРС	0603	7.7±0.1	7.0±0.1	3.3±0.1	12.0±0.1	16±0.3	7.5±0.1	0.35±0.05	1.5±0.1

(3) Packaging Quantity

ТМРС	0603
Chip / Reel	1000
Inner box	2000
Carton	8000

(4) Tearing Off Force



The force for tearing off cover tape is 10 to 130 grams in the arrow direction under the following conditions(referenced ANSI/EIA-481-D-2008 of 4.11 stadnard).

Room Temp.	Room Humidity	Room atm	Tearing Speed	
(℃)	(%)	(hPa)	mm/min	
5~35	45~85	860~1060		

Application Notice

- Storage Conditions
- To maintain the solderability of terminal electrodes:
- 1. TAI-TECH products meet IPC/JEDEC J-STD-020D standard-MSL, level 1.
- 3. Recommended products should be used within 12 months form the time of delivery.
- 4. The packaging material should be kept where no chlorine or sulfur exists in the air.
- Transportation
- 1. Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
- 2. The use of tweezers or vacuum pick up is strongly recommended for individual components.
- 3. Bulk handling should ensure that abrasion and mechanical shock are minimized.

10. Typical Performance Curves

