



REV.	ECN NO.	DESCRIPTION	DATE	DESIGN

NOTES:

1. MATERIAL:  
 HOUSING: THERMOPLASTIC, HIGH TEMP, UL94V-0, BLACK COLOR,  
 CONTACTS: COPPER ALLOY.  
 PEGS: COPPER ALLOY.

2. FINISH:  
 CONTACTS: SELECTED GOLD PLATING ON THE CONTACT AREA,  
 1u" MIN. GOLD PLATING ON THE SOLDER TAILS,  
 50 u" MIN. NICKEL UNDERPLATING OVER ALL.  
 PEGS: 120u" MIN. MATTE-TIN OVER 50u" MIN. NICKEL.

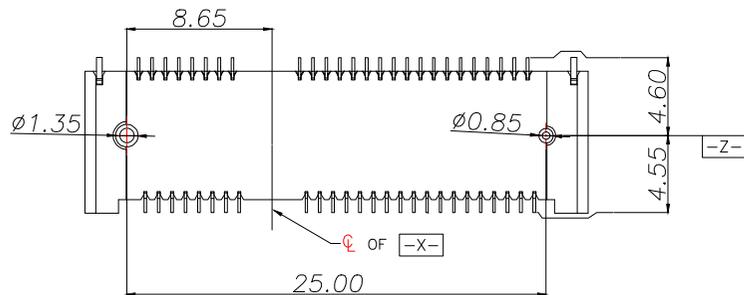
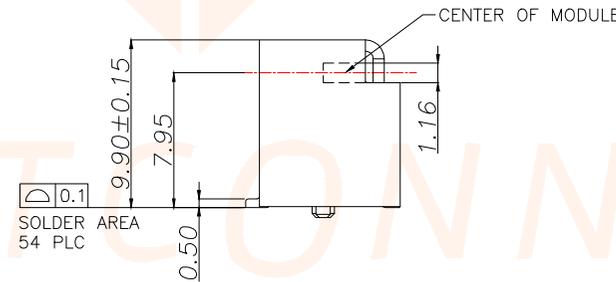
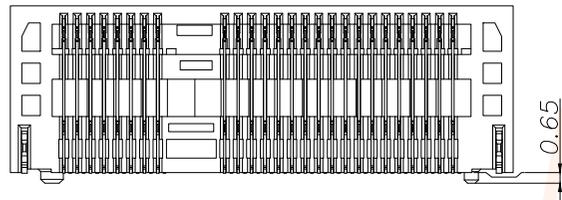
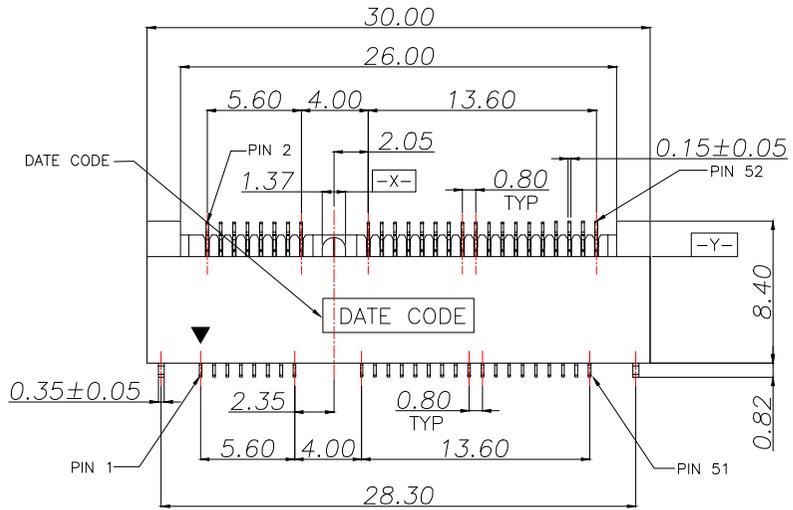
3. DATE CODE.

YYWWDD

YY: YEAR (19: 2019)

WW: WEEK (25: THE 25TH WEEK)

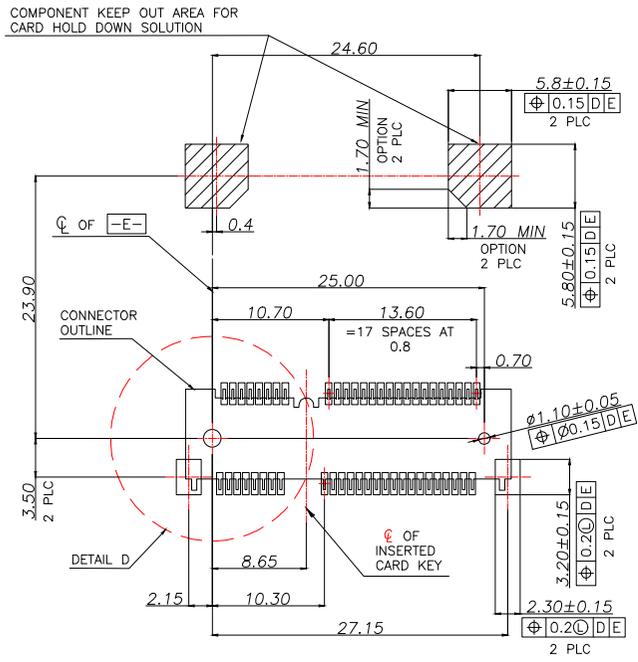
DD: (D1~D7: SUNDAY~SATURDAY)



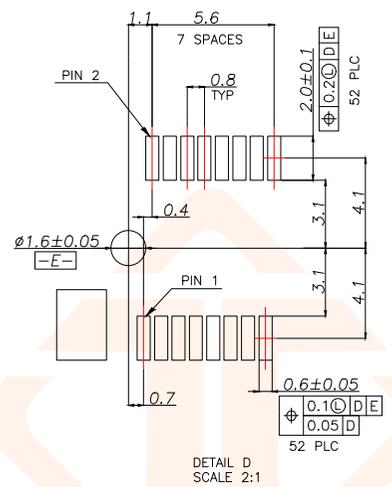
TOLERANCE		深圳聚兴泰电子科技有限公司 Shenzhen Juxingtai Electronic Technology Co., Ltd	
X.:±0.40	x.XXX:±0.15	PART NO.	TITLE:
X.X:±0.30		A052R2-99H	MINI PCI EXPRESS 9.9MM HEIGHT
X.XX:±0.20		DESIGN: Ethan	MANUFACTURE DWG
UNITS	mm	CHED: Olvia	
MATERILA	---	APPD: Ava	
FINISH	---		
			SCALE N:A SHEET 1/1



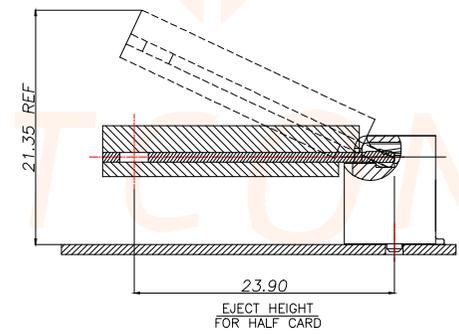
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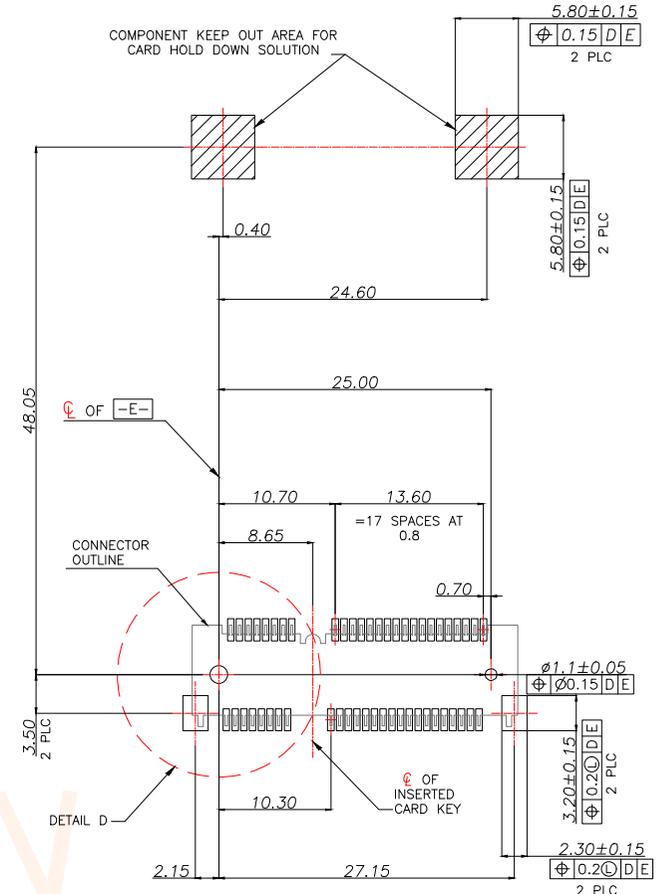
-D- IS THE TOP SURFACE OF PCB.  
 RECOMMENDED P.C. BOARD PATTERN LAYOUT  
 FOR HALF CARD CONNECTOR  
 (TOP VIEW)



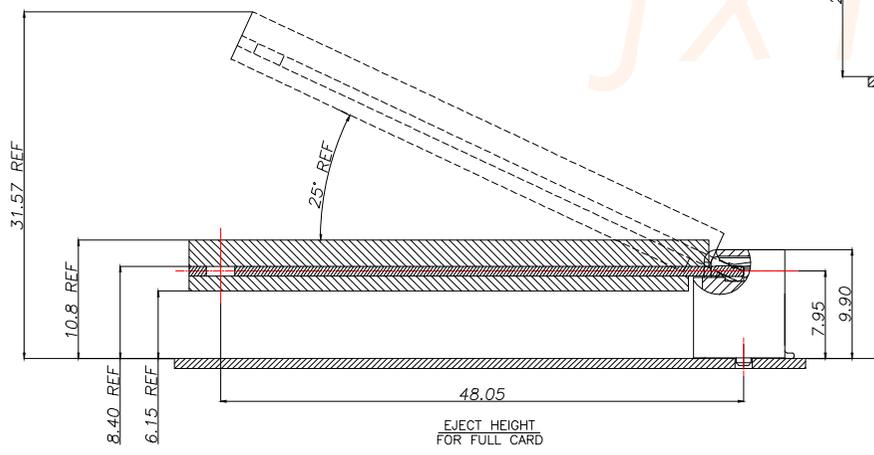
DETAIL D  
 SCALE 2:1



EJECT HEIGHT  
 FOR HALF CARD



-D- IS THE TOP SURFACE OF PCB.  
 RECOMMENDED P.C. BOARD PATTERN LAYOUT  
 FOR FULL CARD CONNECTOR  
 (TOP VIEW)

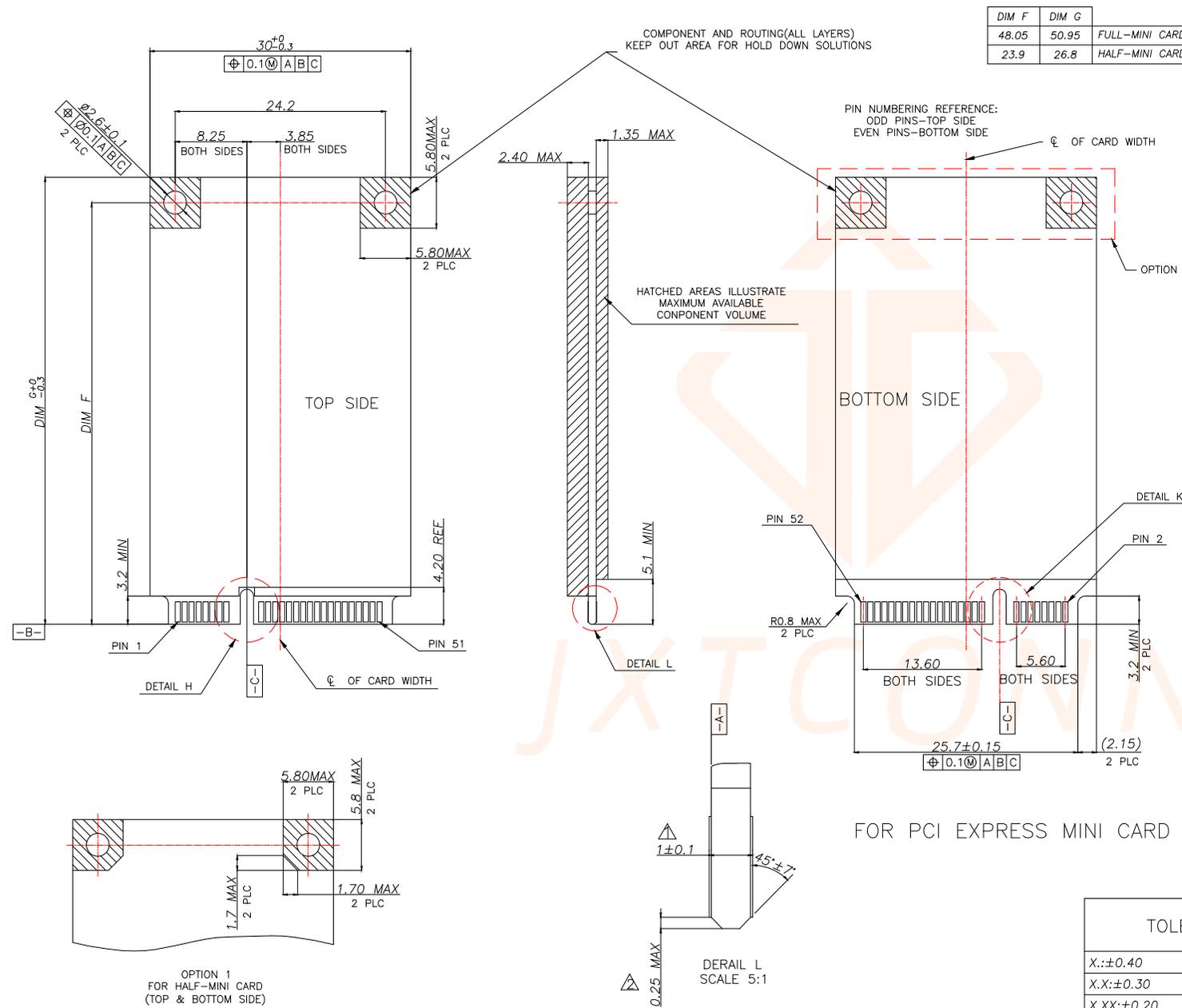


EJECT HEIGHT  
 FOR FULL CARD

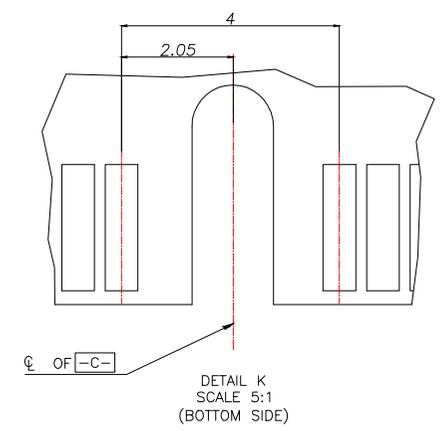
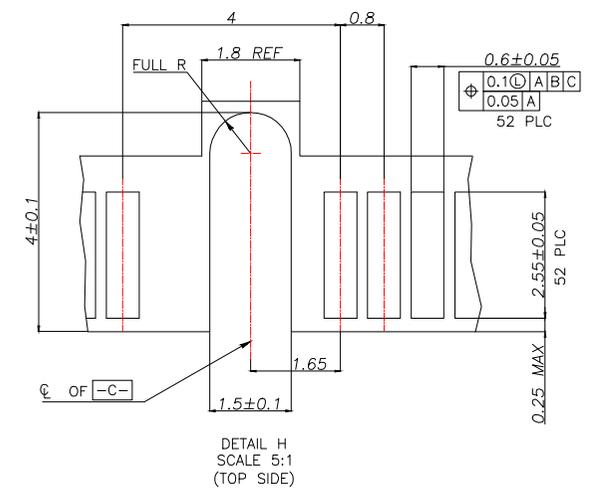
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MATERILA	---	APPD: Ava	
FINISH	---		



REV.	ECN NO.	DESCRIPTION	DATE	DESIGN



DIM F	DIM G	
48.05	50.95	FULL-MINI CARD
23.9	26.8	HALF-MINI CARD



- NOTES:
- ▲ CARD THICKNESS APPLIES ACROSS TABS AND INCLUDES PLATING AND/OR METALLIZATION.
  - ▲ EDGE BEVEL MUST BE PRESENT AND FREE OF CUTTING BURRS FROM PCB AND CONTACT MATERIALS.

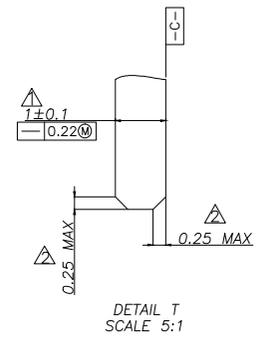
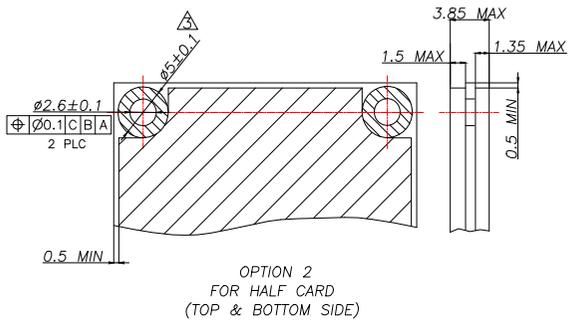
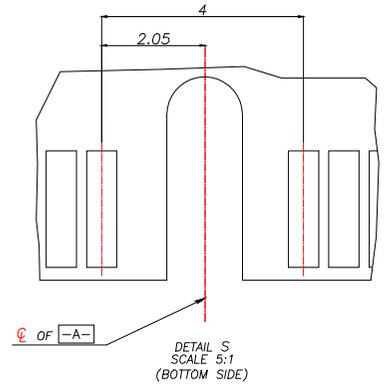
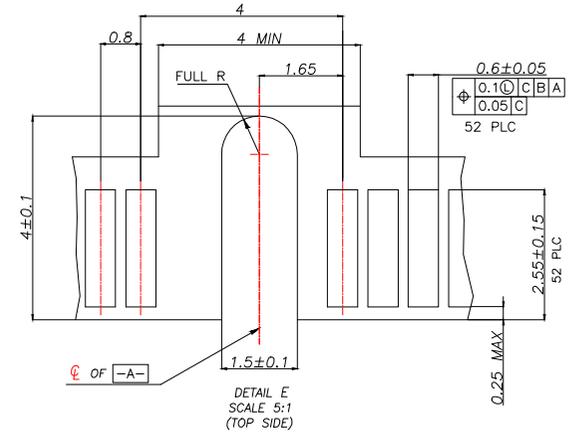
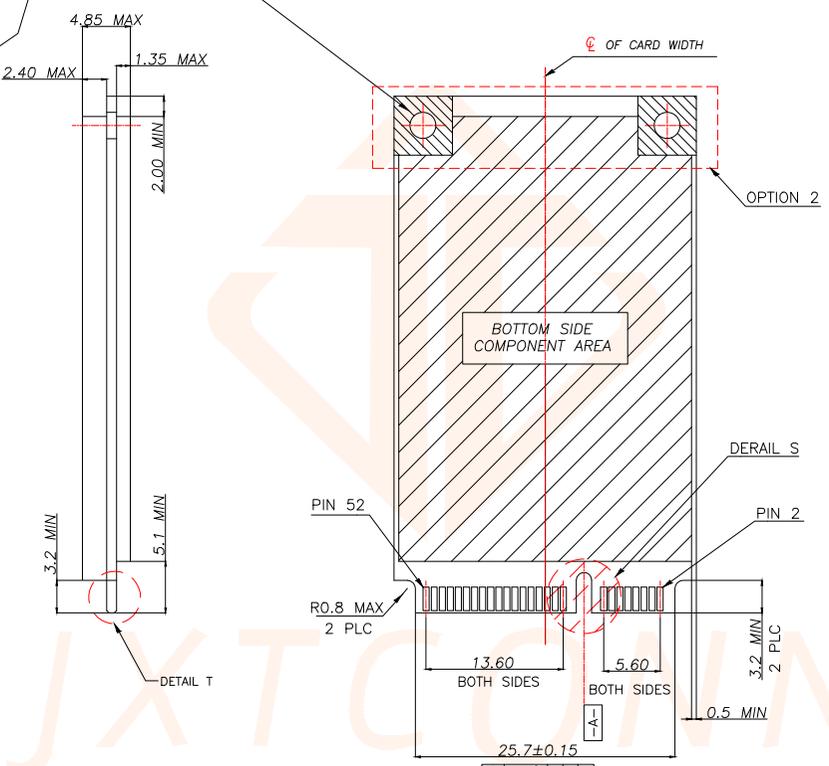
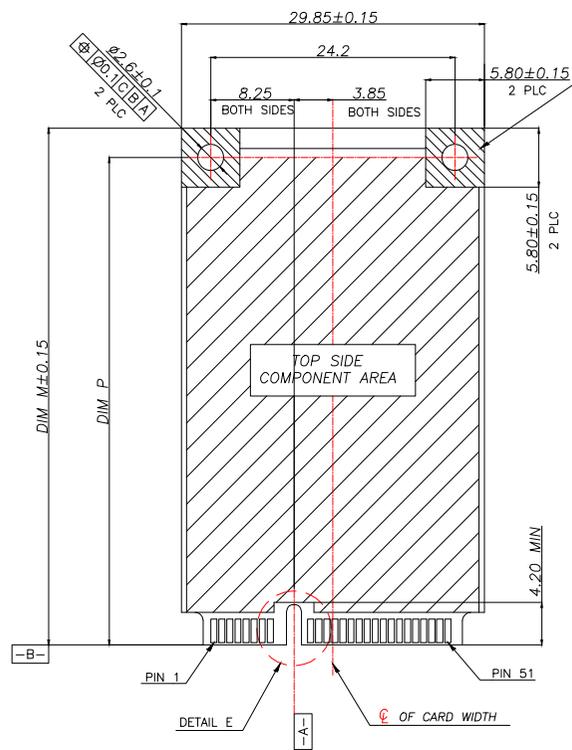
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UNITS	mm	CHED: Olivia	SCALE SHEET N:A 1/1
MATERILA	---	APPD: Ava	
FINISH	---		



REV.	ECN NO.	DESCRIPTION	DATE	DESIGN

DIM P	DIM M	
48.05	50.8	FULL CARD
23.9	26.8	HALF CARD

COMPONENT AND ROUTING (ALL LAYERS)  
KEEP OUT AREA FOR HOLD DOWN SOLUTIONS



- NOTES:
- △ CARD THICKNESS APPLIES ACROSS TABS AND INCLUDES PLATING AND/OR METALLIZATION.
  - △ EDGE BEVEL MUST BE PRESENT AND FREE OF CUTTING BURRS FROM PCB AND CONTACT MATERIALS.
  - △ COMPONENT AND ROUTING (TOP/BOTTOM LAYER) KEEP OUT AREA FOR HOLD DOWN SOLUTIONS.

FOR mSATA

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