

1. Features

- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors(see equivalent circuit)
- The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input.They also have the advantage of almost completely eliminating parasitic effects
- Only the on/off conditions need to be set for operation, making device design easy

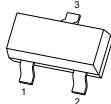
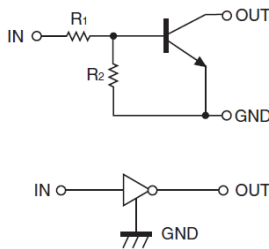
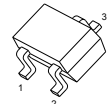
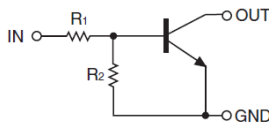
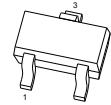
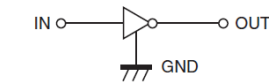
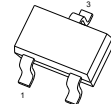
2.MAXIMUM RATINGS (Ta=25℃ unless otherwise noted)

Parameter	Symbol	Limits (DTC143Z xx)						Unit
		M	E	UA	CA	KA	SA	
Supply Voltage	V _{CC}	50						V
Input Voltage	V _{IN}	-5~+30						V
Output Current	I _O	100						mA
Power Dissipation	P _D	100	150	200	200	200	300	mW
Operation Junction and Storage Temperature Range	T _J , T _{stg}	-55~+150						℃

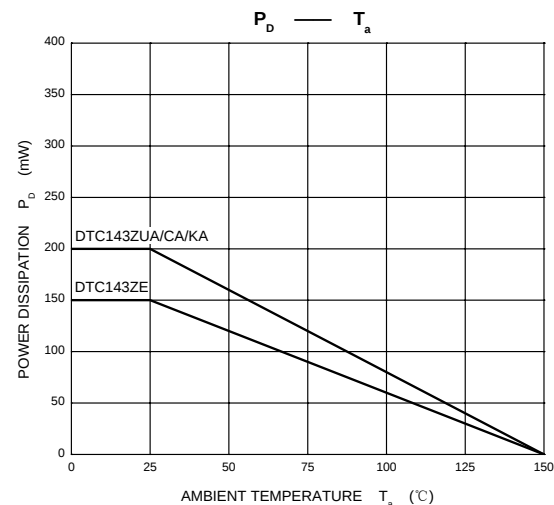
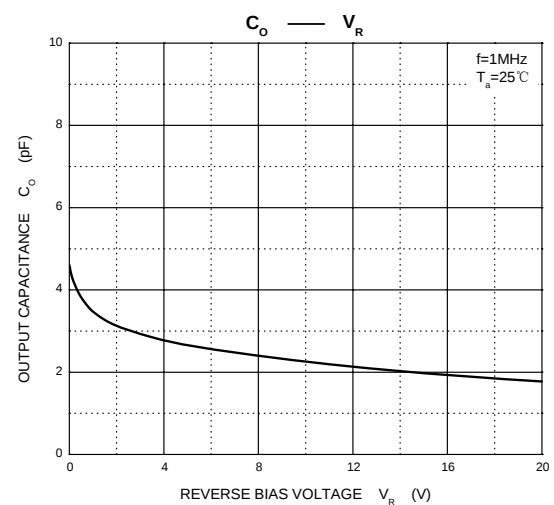
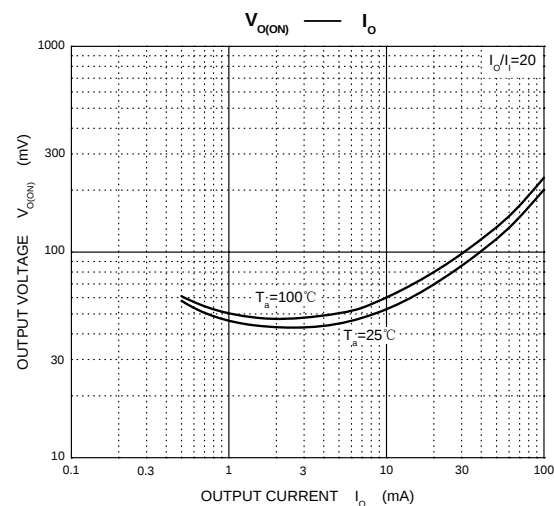
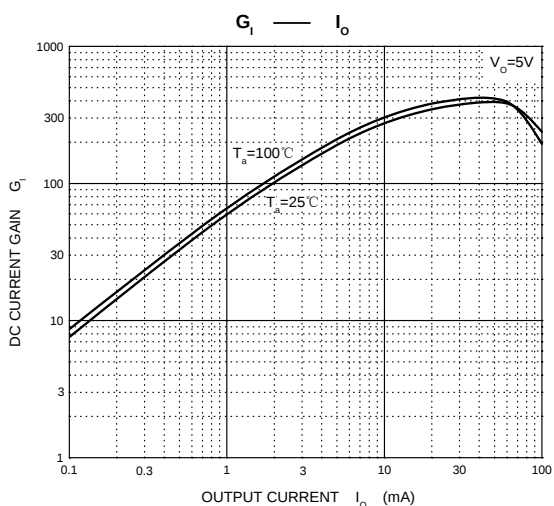
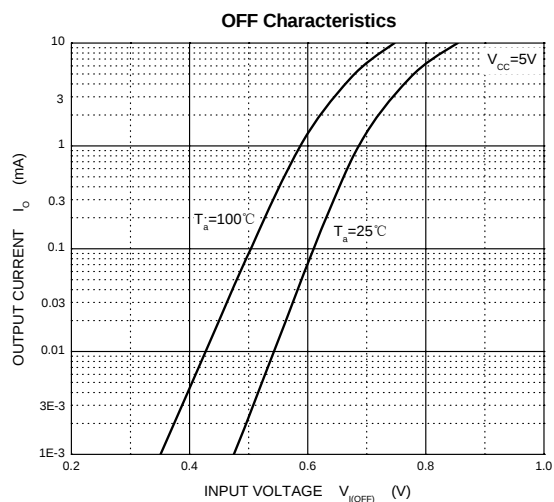
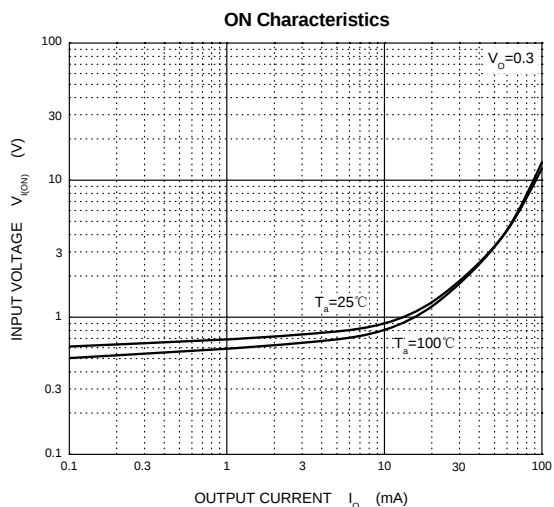
3. Electrical Characteristics (Ta=25℃ unless otherwise specified)

Symbol	Parameter	Condition	Min	Typ	Max	Unit
R ₁	Input resistance		3.29	4.7	6.11	kΩ
R ₂ /R ₁	Resistance ratio		8	10	12	
V _{O(on)}	Output voltage	I _O /I _I =5mA/0.25mA	—	0.1	0.3	V
I _I	Input current	V _I =5V	—	—	1.8	mA
I _{O(off)}	Output current	V _{CC} =50V, V _I =0	—	—	0.5	μA
G ₁	DC current gain	V _O =5V, I _O =10mA	80	—	—	
V _{I(off)}	Input voltage	V _{CC} =5V, I _O =100μA	0.5	—	—	V
V _{I(on)}		V _O =0.3V, I _O =5mA	—	—	1.3	V
f _T	Transition frequency	V _O =10V, I _O =5mA , f=100MHz	—	250	—	MHz

4. Pinning information

Pin	symbol	Part Number	Simplified outline	Equivalent Circuit	Marking	Package	
1	IN	DTC143ZCA			E23	SOT-23	
2	GND						
3	OUT						
1	IN	DTC143ZKA				E23	SOT-23-3
2	GND						
3	OUT						
1	IN	DTC143ZE				E23	SOT-523
2	GND						
3	OUT						
1	IN	DTC143ZUA				E23	SOT-323
2	GND						
3	OUT						

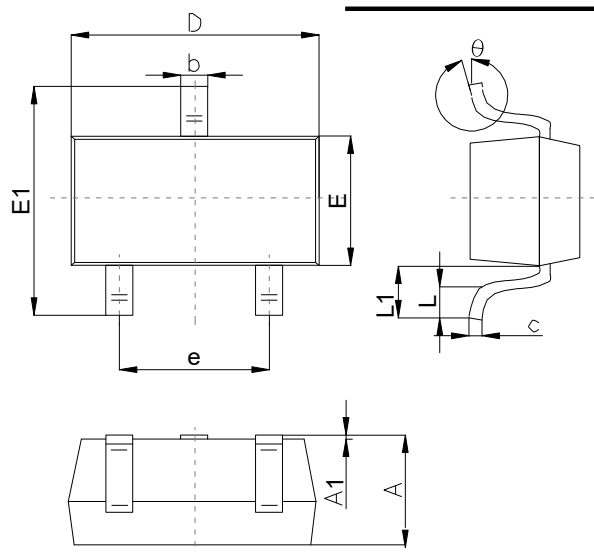
5. Electrical Characteristics Diagrams



The curves above is for reference only.

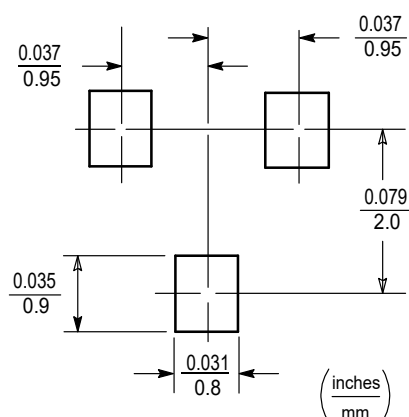
6 . Outline Drawing

SOT-23 Package Outline Dimensions



Symbol	Dimensions In Millimeters		
	Min	Typ	Max
A	0.65		1.40
A1	0.00		0.20
b	0.30		0.55
c	0.08		0.20
D	2.70		3.10
E	1.15		1.65
E1	2.10		2.80
e	1.70		2.10
L	0.15		0.50
L1	0.35		0.70
θ	0°		12°

7 . Suggested Pad Layout

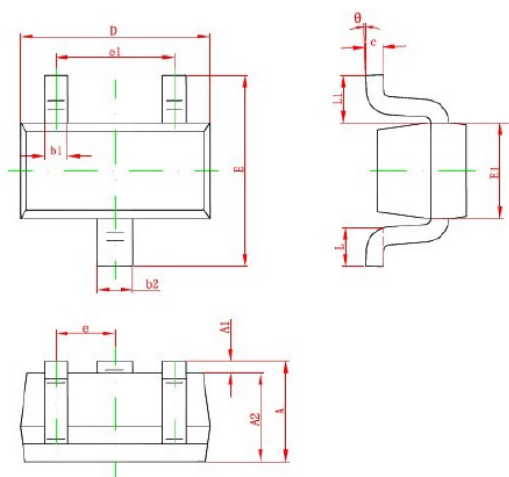


Note:

1. Controlling dimension:in/millimeters.
2. General tolerance: $\pm 0.05\text{mm}$.
3. The pad layout is for reference purposes only.

8 . Outline Drawing

SOT-523 Package Outline Dimensions



Symbol	Dimensions in Millimeters		Dimensions in Inches	
	Min.	Max.	Min.	Max.
A	0.700	0.900	0.028	0.035
A1	0.000	0.100	0.000	0.004
A2	0.700	0.800	0.028	0.031
b1	0.150	0.250	0.006	0.010
b2	0.250	0.350	0.010	0.014
c	0.100	0.200	0.004	0.008
D	1.500	1.700	0.059	0.067
E	1.450	1.750	0.057	0.069
E1	0.700	0.900	0.028	0.035
e	0.500 TYP.		0.020 TYP.	
e1	0.900	1.100	0.035	0.043
L	0.260	0.460	0.010	0.018
L1	0.400 REF.		0.016 REF.	
θ	0°	8°	0°	8°

9.Important Notice and Disclaimer

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