

PRODUCT SPECIFICATION

产品规格书

PRODUCT

产品名称：AMOLED MODULE

MODEL NO.

模块型号：CWS05 1.85inch

REVISION

文件版本：1.0

CUSTOMER

客户：

DATE

日期：2024-06-13

Proposed by			Customer's Approval
Designed	Checked	Approved	

P. 2

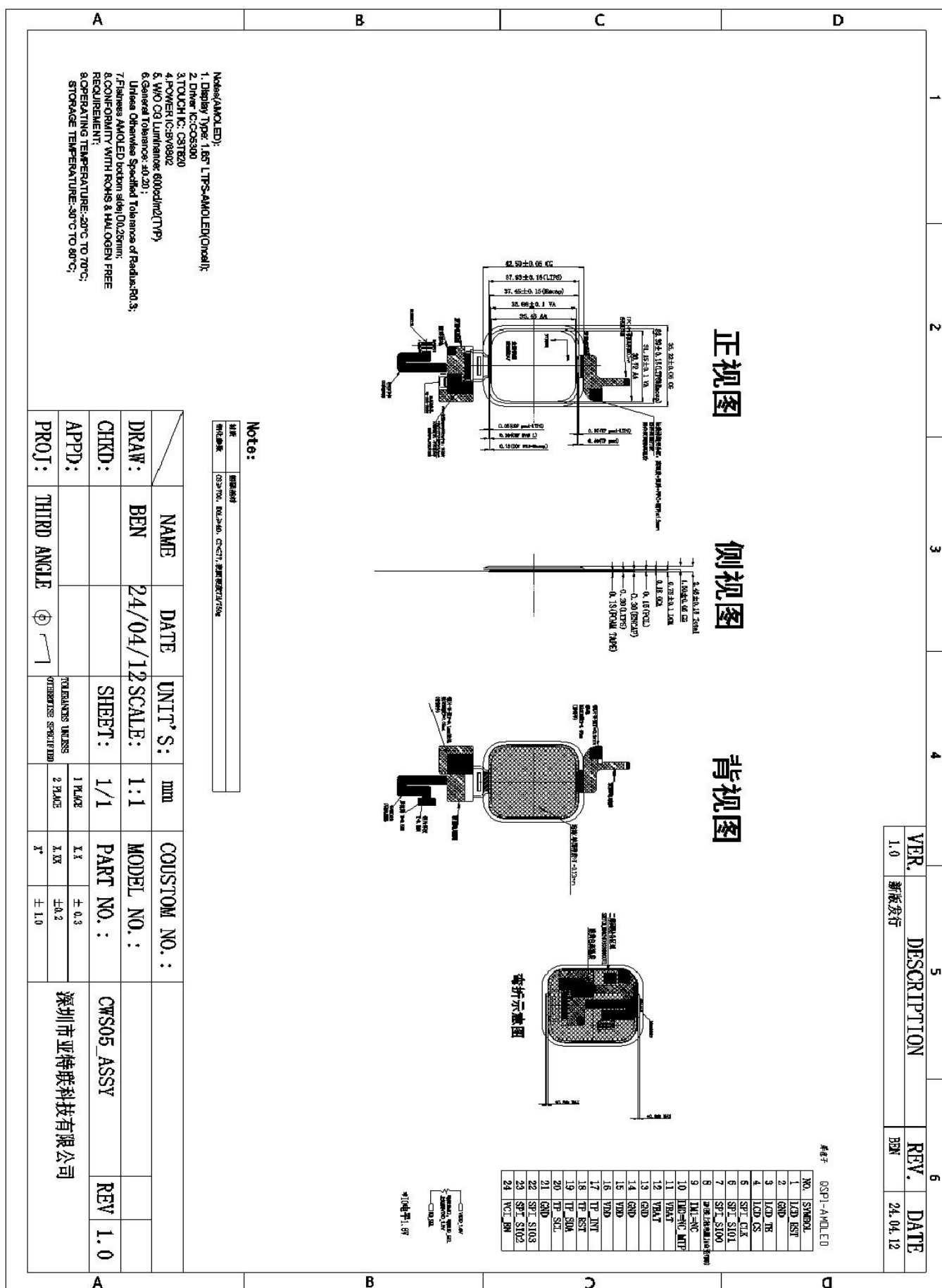
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1. GENERAL INFORMATION

Item	Contents	Unit
Display Mode	AMOLED	/
LTPS Glass Outline (W·H)	42.50(H) x 35.22(W) x 2.46(T)	mm
Module Outline	37.93(H) x 32.39(W)	mm
Active area	35.48 (H) x30.72(W)	mm
Number of Dots	390(W) x RGB x 450(H)	/
Diagonal Inch	1.85	inch
Pixel pitch (W·H)	78 x 78	um
Module Thickness(with LENS)	2.46	mm

2. Outline Dimension Drawing



3. Maximum Ratings

Parameter	Symbol	Spec			Unit	Note
		Min.	Typ.	Max.		
Analog/boost power voltage	VCI	0.3	-	5.5	V	-
I/O voltage	VDDIO	-0.3	-	5.5	V	-
Operating temperature	Top	-20	-	70	°C	-
Storage temperature	Tstg	-30	-	80	°C	-

4. Electrical Specification

4.1 Electrical Characteristics

Power Characteristic: Normal mode

Item	Symbol	Min.	Typ.	Max.	Unit	Remark
AMOLED Power Positive	ELVDD	3.25	3.3	3.35	V	Ref
AMOLED power Negative	ELVSS	-3.35	-3.3	-3.25	V	Ref
Panel GEOA Power	VDD	5.8	6	6.2	V	Ref
Panel GEOA Power	VEE	-6.2	-6	-5.8	V	Ref
Panel reference Power	VINT	-	-3	-	V	Ref
Vdata voltage	VGMP	-	4.4	-	V	Ref
	VGSP	-	1.2	-	V	Ref
Digital Power supply	VDDIO	1.7	1.8	1.9	V	Ref
Analog Power supply	VCI	3.2	3.3	3.4	V	Ref

Power Characteristic: HBM mode

Item	Symbol	Min.	Typ.	Max.	Unit	Remark
AMOLED Power Positive	ELVDD	TBD	TBD	TBD	V	-
AMOLED power Negative	ELVSS	TBD	TBD	TBD	V	Ref
Panel GEOA Power	VDD	5.8	6	6.2	V	Ref
Panel GEOA Power	VEE	-6.2	-6	-5.8	V	Ref
Panel reference Power	VINT	-	-3	-	V	Ref
Vdata voltage	VGMP	-	4.4	-	V	Ref
	VGSP	-	0.8	-	V	Ref
Digital Power supply	VDDIO	1.7	1.8	1.9	V	Ref
Analog Power supply	VCI	3.2	3.3	3.4	V	Ref

1) Normal Mode

Power Supply: VDDIO =1.8V VCI=3.3V ELVSS= -3.3V ELVDD=3.3V

Frame Frequency: $F_{frame} = 60\text{HZ}$ @ 25degC, Brightness 600 nits, Command Mode,

Display Condition	Symbol	Min.	Typ.	Max.	Unit	Remark
100% Pixel On 600nits	IELVDD /ELVSS	-	39.47	45.39	mA	Ref
	IVCI	-	5.8	6.5	mA	Ref
	IVDDIO	-	5.5	5.8	mA	Ref

2) Deep Standby Mode

Display Condition	Symbol	Min.	Typ.	Max.	Unit	Remark
Deep Standby	IVCI	-	-	10	uA	-
	IVDDIO	-	-	10	uA	-

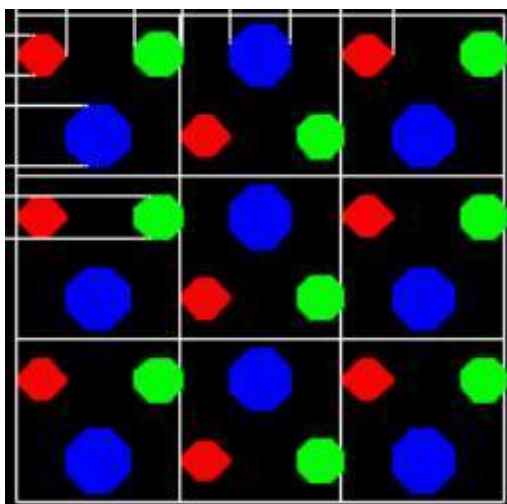
Driver IC: CO5300AF(refer to the datasheet).

Touch IC: CST820 (refer to the datasheet)

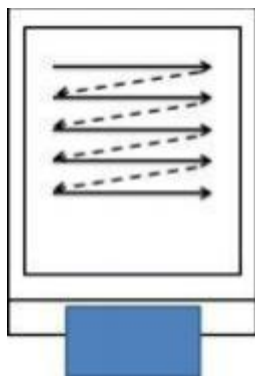
4.2 Interface Description

N0.	Symbol	Description
1	LCD_RST	This signal will reset the device and must be applied to properly initialize the chip.Signal is active low
2	GND	Ground
3	LCD_TE	Tearing effect output pin to synchronize MCU to frame writing,activated by S/W command.
4	LCD_CS	Chip select input pin("low" enable)in and SPI I/F
5	LCD_CLK	A synchronous clock signal in SPI I/F
6	LCD_SI01	QSPI Data1
7	LCD_SI00	QSPI Data0
8	ID	Hardware ID
9	IM1=NC	NC
10	IM0=NC	NC
11	VBAT	PMIC Power supply 3.7V
12	VBAT	PMIC Power supply 3.7V
13	GND	Ground
14	GND	Ground
15	VDD	Power supply for interface system
16	TP_VDD	TP Power supply
17	TP_INT	Touch State change interrupt
19	TP_SDA	Touch I2C data
20	TP_SCL	Touch I2C clock
21	GND	Ground
22	LCD_SI03	QSPI Data3
23	LCD_SI02	QSPI Data2
24	VCI_EN	Power supply for Analog circuit
1	LCD_RST	This signal will reset the device and must be applied to properly initialize the chip.Signal is active low

4.3 Pixel arrangement

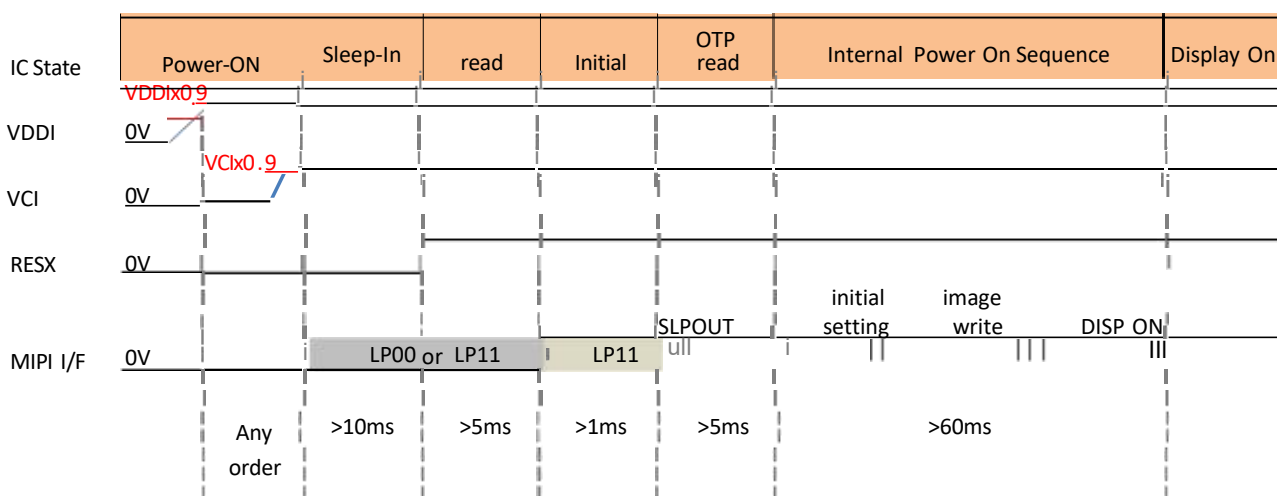


4.4 Graphic memory writing direction

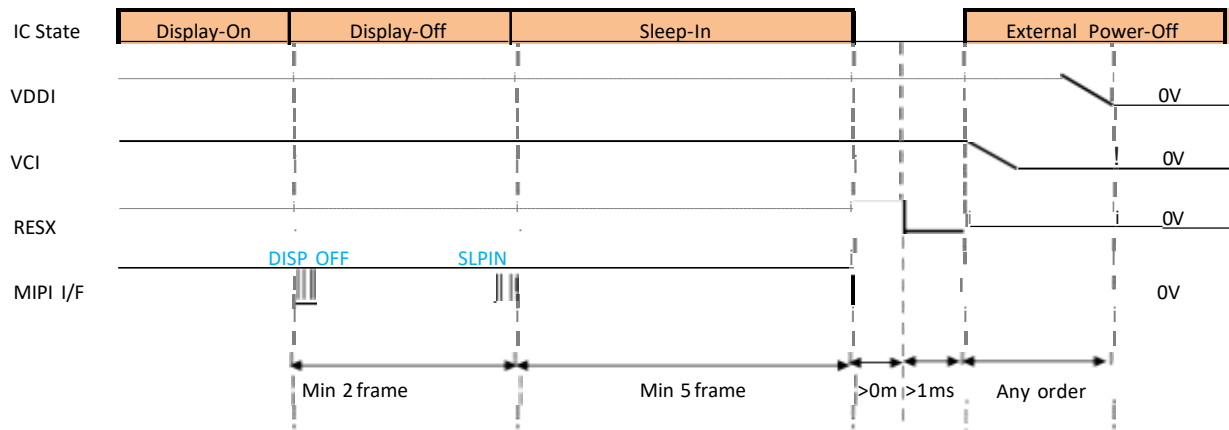


4.5 Recommended Operating Sequence

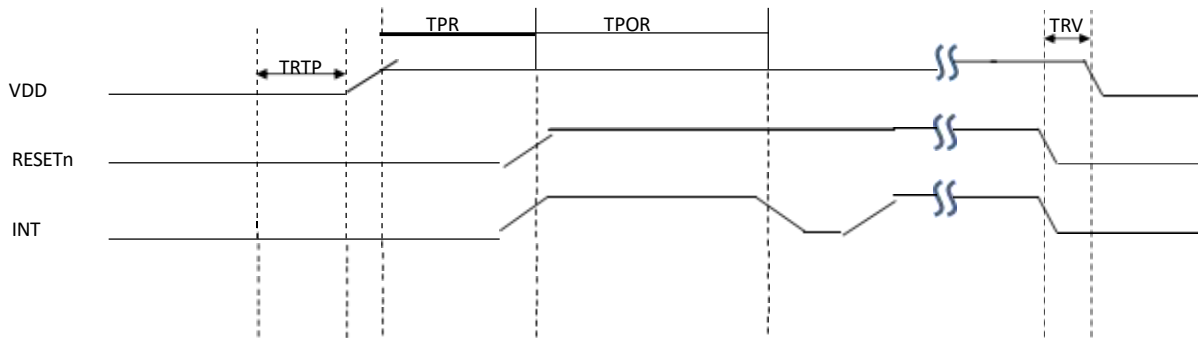
4.5.1 Power On Sequence



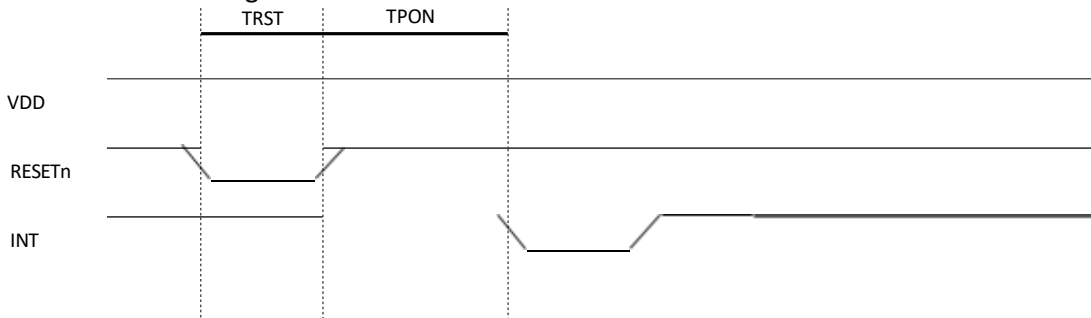
4.5.2 Power Off Sequence



4.5.3 Power on/off sequence(Touch IC)



External reset timing



Parameter	Description	Min	Max	Units
TRTP	Time of resetting to be low before powering on	100		us
TPR	Reset time after 0.9VDD powering on	1		ms
TPOR	Time of starting to report point after powering on		200	ms
TPON	Chip reinitialization time after reset		200	ms
TRST	Reset time		1	ms
TRV	Time of VDD Discharge after reset	0		ms

4.5.4 Timing requirements for RESETB

When RESETB of the reset pin equals to Low, it will be in the condition of reset. When it is in the condition of reset, it will make the device recover the initial set. However, in order to avoid the reset noise cause reset, there is a mechanism to judge about whether the reset is needed or not. The closed interval of Low can be shown as the following.

(Test condition: VDDIO=1.65V~3.6V, VSS=0V, T_A=-20°C~+70°C)

Reset Input Timing

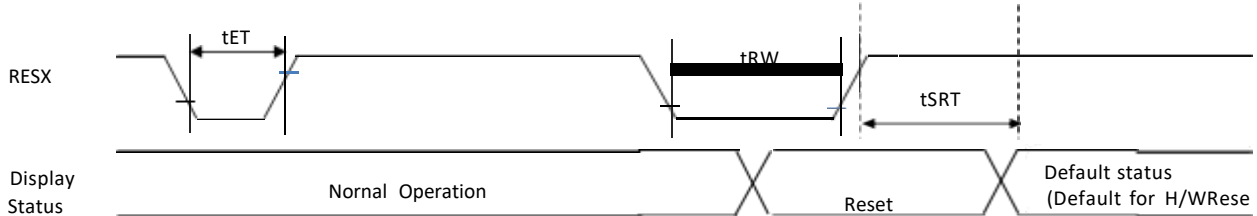


Table: Reset timing

Parameter	Symbol	Pad	Min.	Typ.	Max.	Unit	Note
Reset low pulse width	tRW	RESX	10	-	-	us	-
Secure rereset completion	tSRT	RESX	-	-	5	ms	Reset during Sleep In mode
		RESX	-	-	150		Reset during Sleep Out mode
Reset un-reacted pulse width	tET	RESX			5	us	-

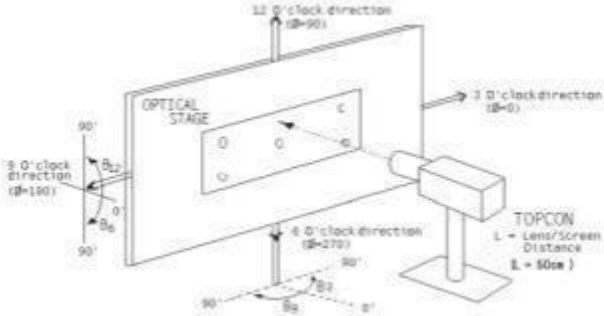
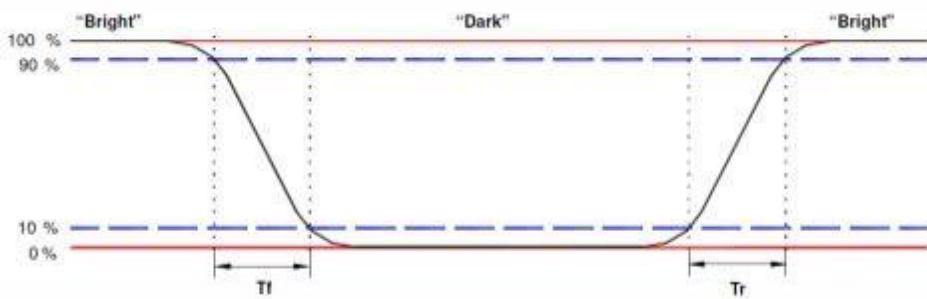
5. Electro-Optical Specification

Test condition: 25°C±3°C, 65±20%RH, darkroom.

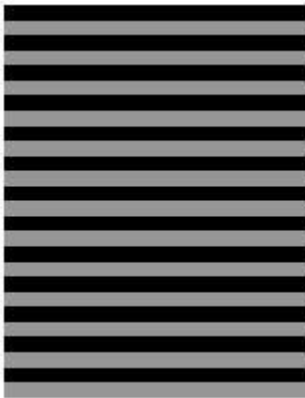
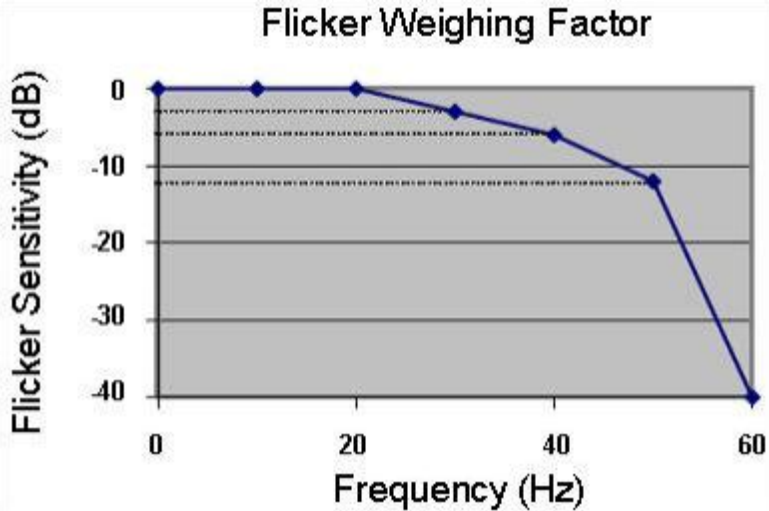
No	Item		Symbol	Condition	Value			Unit	Remark
					Min.	Typ.	Max.		
1	Brightness		L	Full white Without CG	550	600	650	cd/m ²	Note1
			Boost		900	1000		cd/m ²	
2	Brightness Uniformity		UL	Full white	85			%	Note4
3	Contrast Ratio		CR	Normal Θ =Φ=0°	10000			-	Note3.
4	Response time		Ton+ Toff	Normal Θ =Φ=0°		2	3	ms	Note2.
5	Color Coordinate of CIE1931	White	X	Normal Θ =Φ=0°	0.280	0.300	0.320	-	Note1.
			Y		0.290	0.310	0.330		
		Red	X		0.656	0.686	0.716		
			Y		0.280	0.310	0.340		
		Green	X		0.185	0.235	0.285		
			Y		0.670	0.720	0.770		
		Blue	X		0.108	0.143	0.178		
			Y		0.009	0.044	0.079		
6	Color Gamut		NTSC	CIE1931	97	108		%	
7	Viewing			Top/Bottom/Right/Left	85			°	Note3.

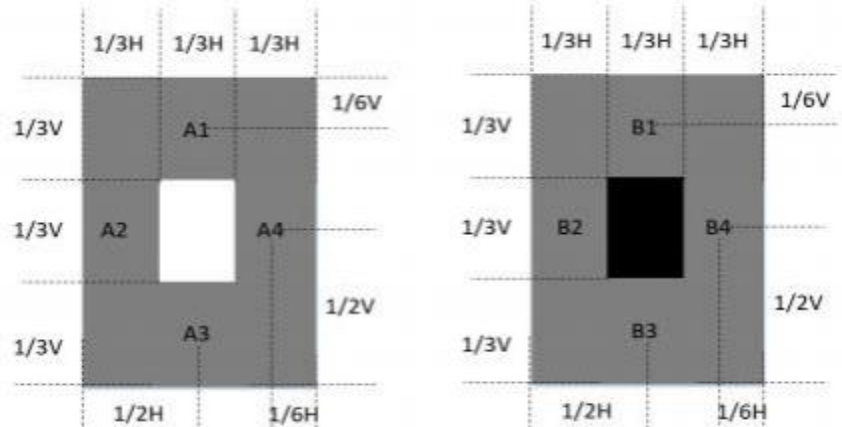
	Angle	CR ratio ≥ 10					
8	Gamma	$\text{Log}(L_v - L_b) = \log(V) + \log(a)$ $V(\text{Gray}) = 48, 72, 104, 132, 164, 192, 224$ $\text{Lum}(\text{gray255}) = 600\text{nit}$	1.9	2.2	2.5	-	
9	Flicker	Normal $\Theta = \Phi = 0^\circ$			-30	dB	Note6.
10	Crosstalk				1.1	%	Note7.
11	Color shift	$\theta L = 30^\circ$		3.5	6	JNCD	
12	OLED Life Time(At 25°C, with white color pattern)	600nits	200			hrs	Note8.
		1000nits	TBD	TBD		hrs	Note8.

See the note in the table below:

No	Item	Details
Note1	Brightness	
Note 2	Response time	

Note 3	Viewing Angle	Contrast Ratio Dark Room C.R=LW/LB LW: full white brightness of display center P0; LB: full black brightness of display center P0.
Note 4	Brightness Uniformity	- The test condition is at 25℃ and measured on the surface of display module. - Measurement equipment: CS2000 or similar equipments. - The luminance uniformity is calculated by using following formula: . $\Delta Bp = Bp (Min.) / Bp (Max.) \times 100 (\%)$ - Bp (Max.) = Maximum brightness in 5 measured spots - Bp (Min.) = Minimum brightness in 5 measured spots.
Note 5	Luminance decrease	Definition of Luminance decrease ratio Test pattern : Full White

	ration	The luminance decrease ratio is calculated by using following formula: $\text{Luminance decrease ratio} = 1 - \frac{\text{Luminance test at left, right, top, bottom} = 30^\circ}{\text{Luminance test at left, right, top, bottom} = 0^\circ}$
Note 6	Flicker	Suggested Instruments: Konica Minolta CA-310 or Klein Instruments K-8  Odd row : L0 Black Even row : L186 gray level Flicker Test Pattern The flicker level is defined by Fast Fourier Transformation (FTT) as follows: $\text{Flicker} = 20 \log_{10} \left(2 \frac{f_{\text{FFTC}}(n)}{f_{\text{FFTC}}(0)} \right) + FS(\text{Hz}) \quad (\text{dB})$ Where fFFTC(n) is the n-th FFT coefficient. fFFTC(0) is the 0-th FFT coefficient which is DC component. FS(Hz) is the flicker sensitivity as a function of frequency. The peak flicker level shall be reported based on the calculation using above formula in which FS(Hz) is determined by the flicker weighing factor shown below. 
Note 7	Crosstalk	There should be no visible cross-talk in normal direction of the display when the two “Cross-talk Test Patterns” below are loaded. ΔBp (Max.) = Maximum value in $\Delta Bp1 \sim \Delta Bp4$. ΔBp (Min.) = Minimum value in $\Delta Bp1 \sim \Delta Bp4$. $\Delta CT = \Delta Bp$ (Max.) / ΔBp (Min.). ΔCT must be less than 1.10

		
Note 8	Life Time	OLED lifetime is defined by the Minimum Duration Time that the luminance is decayed to a specific ratio (ex. 95%) of initial state. Test Pattern under duration period: L255 White

6. Reliability

6.1 Environmental Test

Item	Main spec	No. of failures / No. of examinations
High Temperature Operation	70°C/48hrs	0/10
Low Temperature Operation	-20°C/48hrs	0/10
High Temperature Storage	80°C/48hrs	0/10
Low Temperature Storage	-30°C/48hrs	0/10
High Temperature Humidity Operation	60°C/90% RH/ 48hrs	0/10
Thermal Shock	-20°C~70°C/48hrs	0/10

6.2 Electrical Test

Item	Main spec	Note
Air Discharge	$\pm 4/\pm 6/\pm 8$ kV 150pF/330 Ω (Module level; without CG)	5Points, Each 10times. After one time discharge, panel and gun touch the ground, through the whole test, turn on ion fan. No degradation of OLED performance after this test.
Contact Discharge	$\pm 1/\pm 2$ kV, 150pF/330 Ω (Module level; without CG)	

6.3 Mechanical Test

Test item	Test condition	Note
Packing vibration-proof test	2g, f=10->55->10Hz apply in each of X, Y, and Z direction for 30 min	Package
Packing Drop test	Drop the packing from 60cm height, 6-faces, 3-edges and 1-corner(one time for each)	Package

7. Packing Specification

包装作业规范 Packaging Operation Procedure		编 号: 3MT-EN-POP 版 本: 01 页 数: 1 OF 1 发行日期: 23-05-10
一、基本信息		
客户:	模组规格:	
品名: AMOLED MODULE	包装方式: 真空包装	
二、作业规范		
1、包装数量		
☒ 吸塑盒, 每盒 <u>25</u> pcs; 真空袋, 每包 <u>12</u> 盒; 纸箱, 每箱 <u>2</u> 包, 每箱 <u>600</u> PCS ☐ 其他		
2、真空袋规格 单位: MM		
☒ 433*513 ☐ 400*430 ☐ 120*160		
3、纸箱规格		
☒ 413mm*320mm*250mm ☐ 270mm*270mm*350mm ☐ 290mm*265mm*125mm ☐ 300mm*300mm*250mm		
4、标签		
☒ 合格标签 ☒ RoHS标签 ☒ 月份标签 ☐ 产品标签		
三、作业步骤		
作业示意图	说明和注意事项	
	1、按产品形状, 盖板面向下摆放在吸塑盒定位形状槽内	
	2、每盘摆放25个	
	3、注意摆放方向一致	
	4、注意排线放入对应槽内, 不可再弯折	
	1、12盘一扎, 最上面放一个空盘	
	2、用胶带十字交叉固定好	
	3、贴上RoHS、合格、月份标签	
	4、按每袋一扎, 放入真空袋, 真空包装	
	1、每箱两包	
	2、在纸箱空隙放气泡袋防护	
	3、胶带固定好	
	4、注意纸箱不可有破损、脏污	
编制/日期: Victor/23.05.10		核准/日期:

8. Precautions For Use of OLED Modules

Handling Precautions

The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.

If the display panel is damaged and the Organic Light-Emitting Diode inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, promptly wash it off using soap and water.

Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.

The polarizer covering the display surface of the OLED module is soft and easily scratched. Handle this polarizer carefully.

If the display surface is contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:

- Isopropyl alcohol
- Ethyl alcohol

Solvents other than those mentioned above may damage the polarizer. Especially, do not use the following:

- Water
- Ketone
- Aromatic solvents

Do not attempt to disassemble the OLED Module .

If the logic circuit power is off, do not apply the input signals.

To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.

Be sure to ground the body when handling the OLED Modules.

Tools required for assembly, such as soldering irons, must be properly ground.

To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.

The OLED Module is coated with a film to protect the display surface. Be care when peeling off this protective film since static electricity may be generated.

Storage precautions

When storing the OLED modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.

The OLED modules should be stored under the storage temperature range. If the OLED modules will be stored for a long time, the recommend condition is:

Temperature : 0C ~ 40C Relatively humidity: ≤80%

The OLED modules should be stored in the room without acid, alkali and harmful gas.

Transportation Precautions

The OLED modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.