

承認書

SPECIFICATION FOR APPROVA

<i>CUSTOMER:</i>		
<i>CUSTOMER P/N:</i>		
<i>CUSTOMER PART NO:</i>		
<i>DESCRIPTION:</i>	<i>SMD INDUCTORS</i>	
<i>PRODUCTS NO:</i>	<i>BCRH105R-4R7N</i>	
<i>PFIRST DATE:</i>	<i>2025-8-15</i>	<i>BC REV: X1</i>
<i>DATE:</i>	<i>2025-8-15</i>	

<i>PURCHASER CONFIRMED</i>		
<i>APPROVAL BY</i>	<i>CHECK BY</i>	<i>DRAWN BY</i>

<i>REMARK</i>		

<i>PROVIDER ENGINEER DEPT.</i>		
<i>APPROVAL BY</i>	<i>CHECK BY</i>	<i>DRAWN BY</i>
Vincent	Eason	Daisy

CHENG 誠陽實業有限公司

TAIPEI OFF ICE TAIWAN CHENG YANG COMPONENT CORP

2F-1, NO. 176, Chine-Yi Road., Zhonghe District, New Taipei City, TAIWAN(R.O.C)

新北市中和區建一路176號2樓之一

POSTAL CODE: 23500

TEL NO.:+886-2- 8228-0930 FAX NO.:+886-2-8228-0929 E-mail:h21803@ms29.hinet.net



寶誠電子有限公司

CHINA FACTORY ZHUHAI BAO CHENG ELECTRONICS CO., LTD

Guan Tang Industrial Park, Tang Jia Wan Town, Zhuhai City, Guangdong Province, CHINA

中國廣東省珠海市塘家灣鎮官塘工業區

POSTAL CODE: 519085

TEL NO:86-756-3383187 FAX NO:86-756-3380704 E-mail: baocheng@baocheng.biz

CHENG 昆山誠陽電子有限公司

CHINA FACTORY KUNSHAN CHENG YANG ELECTRONICS CO., LTD

Second Floor, building 5, 2615 Huanqing Road, Kunshan, Jiangsu Province

江蘇省昆山市环庆路2615号5号楼二楼（奥佩克智能制造科技园内）

POSTAL CODE: 215300

TEL NO:86-512-57823500 FAX NO:86-512-57823503 E-mail: kscy@taiwan-chengyang.com.tw

SAMPLE ACKNOWLEDGE CHANGE RESUME

DATE: 2025-8-15

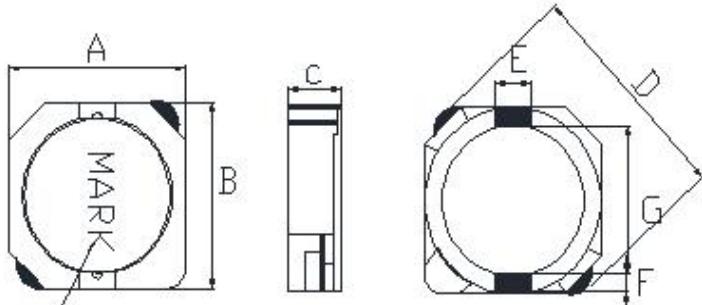
CUSTOMER: 0	CUSTOMER PART NO: 0	PFIRST DATE: 2025-8-15
DESCRIPTION: SMD INDUCTORS	PRODUCTS NO: BCRH105R-4R7N	BC REV: X1

SPECIFICATION FOR APPROVAL

DATE: 2025-8-15

CUSTOMER: 0	CUSTOMER PART NO: 0	PFIRST DATE: 2025-8-15
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1.CONFIGURATION DIMENSIONS

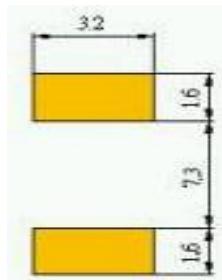


DIMENSIONS (UNIT:mm)

A:	10.50 MAX
B:	10.50 MAX
C:	5.10 MAX
D:	13.50 MAX
E:	3.00 REF
F:	1.20 REF
G:	7.70 REF

注: 印5号 (宽3.8*高2.2) 宋体, 黑色

2.RECOMMEND LAND PATTERN DIMENSIONS



3.ELECTRICAL CHARACTER

Part Number	Inductance (uH)	R _{dc} (mΩ)	⁽⁵⁾ (Irms)(A) Heat Rating Current DC Amps		⁽⁶⁾ (Isat)(A) Saturation Current DC Amps	
	100kHz/0.25v/0A.	Max	Typical		Typical	
BCRH105R-4R7N	4.70 ± 30%	28.0	5.6	△T≤40°C	5.6	△L≤35%

(1) Tolerance of Inductance: N=±30%.

(2) All test data is referenced to 25°C ambient.

(3) Inductance is measured at 100kHz/0.25v/0A.

(4) Operating Temperature Range -40°C to +125°C.

(5) DC current (Irms) (A) that will cause an Approximate △T≤40°C

(6) DC current (Isat) (A) that will cause L0 to drop approximately △L≤35%

(7) The part Temperature (ambient + temp rise) should not exceed 125°C under worst case operating conditions.

Circuit design, component placement, PWB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application.

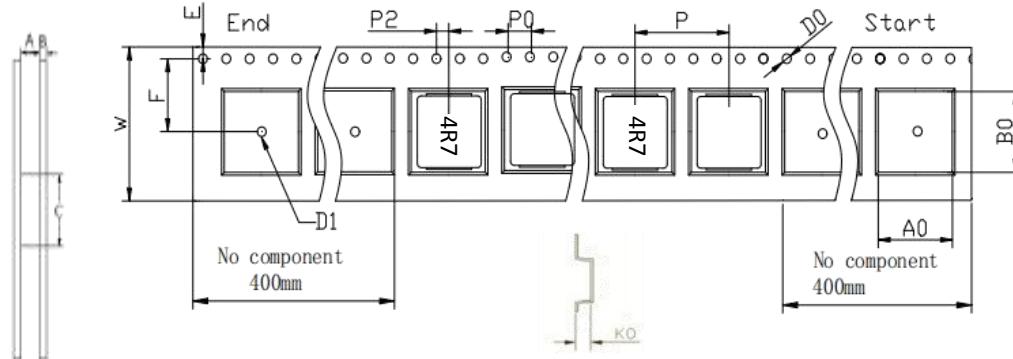
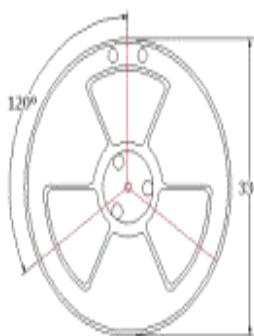
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DESCRIPTION: SMD INDUCTORS	PRODUCTS NO: BCRH105R-4R7N	BC REV: X1

5.PACKAGING INFORMATION

(1) Tape Dimension / Packaging Quantity

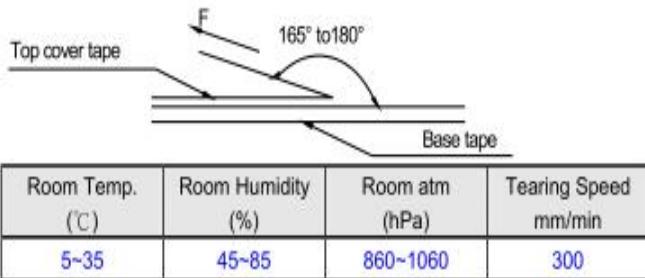


A 24.5 ± 0.50
 B 2.3 ± 0.50
 C 100 ± 0.50
 Q'TY: 800 PCS

DIMENSIONS (UNIT:mm)

W:	24.00 ± 0.30
A0:	10.80 ± 0.10
B0:	11.00 ± 0.10
K0:	5.50 ± 0.10
P:	16.00 ± 0.10
F:	11.50 ± 0.10

(2) Tearing Off Force



The force tearing off cover 10 to 130 grams (0.1N to 1.3N) in the arrow direction under the following conditions.

● Storage conditions/Note things

(1) Storage temperature and humidity conditions :

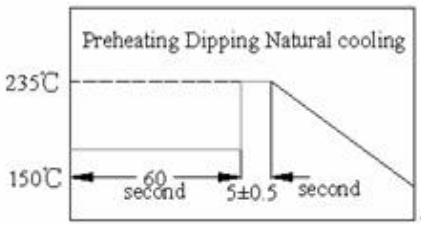
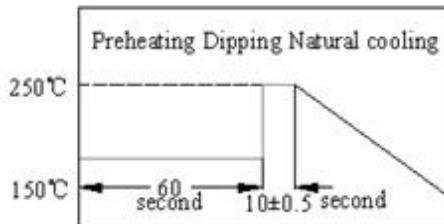
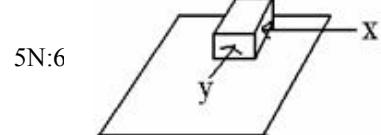
1. Product packing with Carrier tape: $+5^{\circ}\text{C} \sim +40^{\circ}\text{C}$ and less than 60% RH.
2. Product alone: $-20^{\circ}\text{C} \sim +60^{\circ}\text{C}$ and less than 60% RH.

(2) Products should be used within 6 months.

- (3) The packaging material should be kept where no chlorine or sulfur exists in the air.
- (4) Do not touch the electrodes (soldering terminals) with fingers as this may lead to deterioration of solder ability
- (5) The use of tweezers or vacuum pick-ups is strongly recommended for individual components.
- (6) Bulk handling should ensure that abrasion and mechanical shock are minimized.

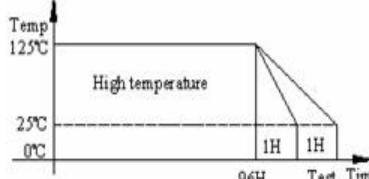
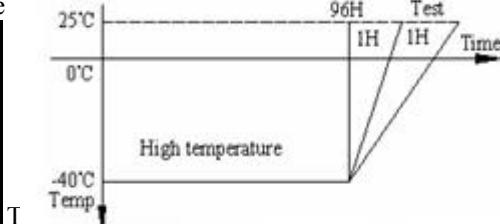
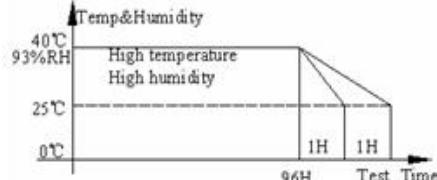
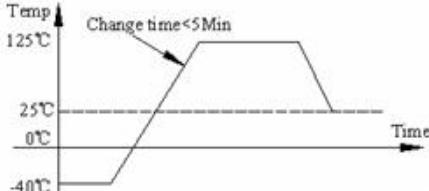
■ GENERAL CHARACTERISTICS

page. 1

Operation Temperature	-40°C to +125°C (Includes temperature when the coil is heated)
External Appearance	On visual inspection, the coil has no external defects.
Solder Ability Test	<p>More than 90% of terminal electrode should be covered with solder.</p> <p>I After fluxing, component shall be dipped in a solder bath at 235°C ± 5°C for 5 ± 0.5 seconds.</p> 
Heat endurance of Solderin	<p>1. Components should have no evidence of electrical and mechanical damage.</p> <p>2. Inductance: within ±10% of initial value.</p> <p>3. Impedance: within ±10% of initial value.</p> <ul style="list-style-type: none"> ● Preheat: 150±5°C 60 seconds. ● Solder temperature: 250±5°C. ● Flux: rosin. ● Dip time: 10±0.5 seconds. 
Terminal Strength	<p>After soldering of X, Y withstanding at below conditions . The terminal should not Peel off. (Refer to figure at below)</p> 
Insulating Resistance	Over 100MΩ at 100V D.C. between coil and core.
Dielectric Strength	No dielectric breakdown at 30V D.C. for 1 minute between coil and core.
VibrationTest	Inductance deviation within +10% after vibration for 1 hour. In each of three orientations at sweep vibration(10~55~10HZ)with 1.5mmP-P amplitudes
Drop test	Inductance deviation within +10% after being dropped once with 981m/s ² (100G) shock Attitude upon a rubber block method shock testing machine, in three different orientations
<p>⑤ Application Notice/Handling</p> <p>1. Storage Conditions</p> <p>1. Storage Conditions</p> <p>To maintain the solder ability of terminal electrodes:</p> <p>(1) Temperature and humidity conditions: less than 40°C and 70% RH.</p> <p>(2) Products should be used within 6 months.</p> <p>(3) The packaging material should be kept where no chlorine or sulfur exists in the air.</p> <p>2. Handling</p> <p>(1) Do not touch the electrodes(soldering terminals)with fingers as this may lead to deterioration of solderability.</p> <p>(2) The use of tweezers or vacuum pick-ups is strongly recommended for individual components.</p> <p>(3) Bulk handling should ensure that abrasion and mechanical shock are minimized.</p>	

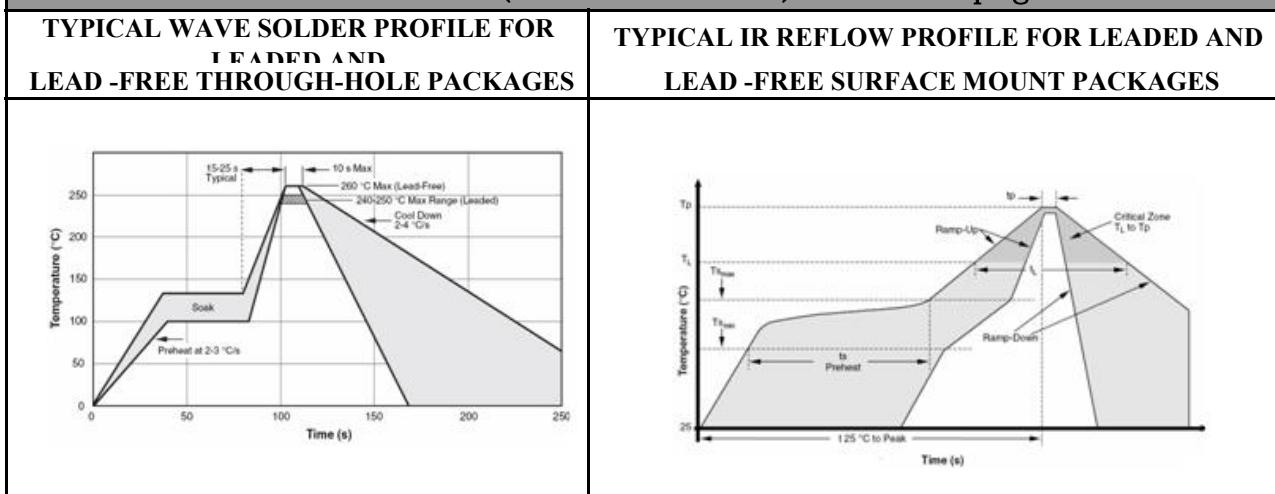
■GENERAL CHARACTERISTICS

page. 2

TEST	Required Characteristics	Test Method/Condition
High Temperature Storage Test Reference documents: MIL-STD-202G Method108A	1. No case deformation or change in appearance 2. $\Delta L/L \leq 10\%$ 3. $\Delta Q/Q \leq 30\%$ 4. $\Delta DCR/DCR \leq 10\%$	 <p>Temperature: $125^{\circ}\text{C} \pm 2^{\circ}\text{C}$ Time: 96 ± 2 hours. Tested not less than 1 hour, nor more than 2 hours at room.</p>
Low Temperature Storage Test Reference documents: IEC 68-2-1A 6.1 6.2	1. No case deformation or change in appearance 2. $\Delta L/L \leq 10\%$ 3. $\Delta Q/Q \leq 30\%$ 4. $\Delta DCR/DCR \leq 10\%$	 <p>Tested not less than 1 hour, nor more than 2 hours at room.</p>
Humidity Test Reference documents: MIL-STD-202G Method103B	1. No case deformation or change in appearance 2. $\Delta L/L \leq 10\%$ 3. $\Delta Q/Q \leq 30\%$ 4. $\Delta DCR/DCR \leq 10\%$	 <p>1. Dry oven at a temperature of $40^{\circ}\text{C} \pm 2^{\circ}\text{C}$ for 96 hours 2. Measurements At the end of this period 3. Exposure: Temperature: $40^{\circ}\text{C} \pm 2^{\circ}\text{C}$. Humidity: 93 ± 2 hours. 4. Tested while the chamber. 5. Tested not less than 1 hour. Nor more than 2 hours at room temperature.</p>
Thermal Shock Test Reference documents: MIL-STD-202G Method107G	1. No case deformation or change in appearance 2. $\Delta L/L \leq 10\%$ 3. $\Delta Q/Q \leq 30\%$ 4. $\Delta DCR/DCR \leq 10\%$	 <p>First -40°C for 30 Minutes, last 125°C for 30 Minutes as 1 cycle. Go through 20 cycles.</p>
<h3>■Application Notice/Handling</h3> <p>(1) Temperature and humidity conditions : less than 40°C and 70% RH. (2) Products should be used within 6 months. (3) The packaging material should be kept where no chlorine or sulfur exists in the air. (4) Do not touch the electrodes (soldering terminals) with fingers as this may lead to deterioration of solder ability (5) The use of tweezers or vacuum pick-ups is strongly recommended for individual components. (6) Bulk handling should ensure that abrasion and mechanical shock are minimized.</p>		

THE CONDITION OF REFLOW (RECOMMENDATION)

page. 3



IPC/JEDEC J-STD-020C, Figure 5-1

Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Average Ramp-Up Rate ($T_{s_{\text{min}}} \text{ to } T_p$)	3 °C/second max.	3 °C/second max.
Preheat		
± Temperature Min ($T_{s_{\text{min}}}$)	100 °C	150 °C
± Temperature Max ($T_{s_{\text{max}}}$)	150 °C	200 °C
± Time ($t_{s_{\text{min}}} \text{ to } t_{s_{\text{max}}}$)	60-120 seconds	60-180 seconds
Time maintained above:		
± Temperature (T_L)	183 °C	217 °C
± Time (t_L)	60-150 seconds	60-150 seconds
Peak/Classification Temperature (T_p)	See Table 4.1	See Table 4.2
Time within 5 °C of actual Peak Temperature (t_p)	10-30 seconds	20-40 seconds
Ramp-Down Rate	6 °C/second max.	6 °C/second max.
Time 25 °C to Peak Temperature	6 minutes max.	8 minutes max.

Table 4. Classification Reflow Profiles (per IPC/JEDEC J-STD-020C, Table 5.2)

Note 1: All temperatures refer to topside of the package, measured on the package body surface.

Package Thickness	Volume mm ³ <350	Volume mm ³ ≥350
<2.5 mm	240 +0/-5 °C	225 +0/-5 °C
≥2.5 mm	225 +0/-5 °C	

Table 5. SnPb Eutectic Process – Package Peak Reflow Temperatures (per IPC/JEDEC J-STD-020C, Table 4.1)

Package Thickness	Volume mm ³ <350	Volume mm ³ 350-2000	Volume mm ³ >2000
<1.6 mm	260 + 0 °C *	260 + 0 °C *	260 + 0 °C *
1.6 mm - 2.5 mm	260 + 0 °C *	250 + 0 °C *	245 + 0 °C *
≥2.5 mm	250 + 0 °C *	245 + 0 °C *	245 + 0 °C *

* Tolerance: Process compatibility is up to and including the stated classification temperature (this means Peak reflow temperature + 0 °C. For example 260 °C + 0 °C) at the rated MSL level.

Table 6. Pb-free Process – Package Classification Reflow Temperatures (per IPC/JEDEC J-STD-020C, Table 4.2)

Note 1: The profiling tolerance is + 0 °C, -X °C (based on machine variation capability) whatever is required to control the profile process but at no time will it exceed -5 °C. Process compatibility at the peak reflow profile temperatures as defined in Table 4.2.

Note 2: Package volume excludes external terminals (balls, bumps, lands, leads) and/or nonintegral heat sinks.

Note 3: The maximum component temperature reached during reflow depends on package thickness and volume. The use of convection reflow processes reduces the thermal gradients between packages. However, thermal gradients due to differences in thermal mass of SMD packages may still exist.

Note 4: Components intended for use in a "lead-free" assembly process shall be evaluated using the "lead-free" classification temperatures and profiles defined in Tables 4.1, 4.2 and 5.2 whether or not lead free.