TAI-TECH KBM01-250800766 P2.

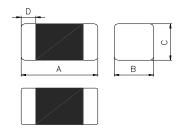
High Current Ferrite Chip Bead(Lead Free)

HCB1005KF-101T10

1.Features

- 1. Monolithic inorganic material construction.
- 2. Closed magnetic circuit avoids crosstalk.
- 3. Suitable for reflow soldering.
- 4. Shapes and dimensions follow E.I.A. spec.
- 5. Available in various sizes.
- 6. Excellent solder ability and heat resistance.
- 7. High reliability.
- 8. 100% Lead(Pb) & Halogen-Free and RoHS compliant.
- 9. Low DC resistance structure of electrode to prevent wasteful electric power consumption.
- 10. Operating Temperature: -55~+125°C(Including self-temperature rise)

2.Dimensions



Chip Size						
A 1.00±0.10						
В	0.50±0.10					
С	0.50±0.10					
D	0.25±0.10					
	•					

Units: mm

3.Part Numbering



E: Packaging T=Taping and Reel, B=Bulk(Bags) F: Rated Current 10=1000mA Termination (Pb Free)

Ag(100%)

Ni(100%)-1.5um (min.)

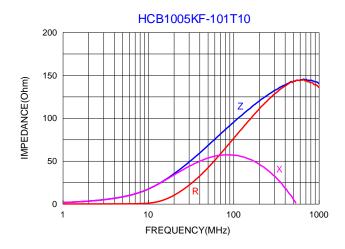
Sn(100%)-3.5um (min.)

4. Specification

Tai-Tech Part Number	Impedance (Ω)	Test Frequency (Hz)	DC Resistance (Ω) max.	Rated Current (mA) max.
HCB1005KF-101T10	100±25%	60mV/100M	0.20	1000

- Rated current: based on temperature rise test
- In compliance with EIA 595
- All test data referenced to 25°C ambient

■ Impedance-Frequency Characteristics



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5. Reliability and Test Condition

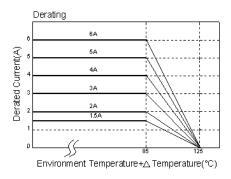
Item			Performan	ce			Te	st Cond	lition	
Series No.	FCB	FCM	HCB	GHB	FCA					
Operating Temperature	-55~+125°C (Including self-temperature rise)									
Transportation Storage Temperature		-55~+125°C (on board)						e condition	ns, please	see the
Impedance (Z)	5.					Agilent42 Agilent E Agilent42 Agilent16	4991 287			
DC Resistance	Refer to stand	dard electrical c	haracteristics li	st		Agilent 4:				
Rated Current						DC Powe Over Rat some risk	ed Curre		ments, thei	e will be
Temperature Rise Test		ated Current < 1A ΔT 20°C Max ated Current ≧ 1A ΔT 40°C Max						owed DC oneasured b	current. by digital su	rface
Life test	Appearance:	· ·	tial value.			times.(IF Reflow P Temperar Applied of Duration: Measurer for 24±2 Precondit	PC/JEDE rofiles) ture: 128 turrent: r 1000±1 d at rochrs.	5±2°C cated curre 2hrs. om temper	rature after	placing w for 3
Load Humidity	DCR : within	Impedance: within±15% of initial value. DCR: within ±15% of initial value and shall not exceed the specification value					times.(IPC/JEDEC J-STD-020F Classification Reflow Profiles) Humidity: 85±3%R.H. Temperature: 85±2°C. Duration:1000hrs Mn.Bead:with100%ratedcurrent, Inductance: with 10% rated current Measured at room temperature after placing for 24±2 hrs			atedcurr current
Thermal shock	Impedance: v	Appearance: no damage. Impedance: within±15%of initial value. DCR: within±15% of initial value and shall not exceed the specification value					tioning: PC/JEDE rofiles) In for 1 cy 5±2°C 25±2°C cof cycles	C J-STD- ycle 30±5 r ≤ 0.5r 30±5mir 5: 500	min	sification
Vibration	Impedance :	Preconditioning: Run through times.(IPC/JEDEC J-STD-020F Reflow Profiles) Appearance: No damage. Impedance: within±15% of initial value DCR: within±15% of initial value and shall not exceed the specification value Total Amplitude:10g Testing Time: 12 hours(20 minute each of 3 orientations).					020F Clas: Hz ~ 2KHz cker	sification : ~ 10Hz		
Bending	Impedance :	: No damage. within±10% of i ±15% of initial v		not exceed the s	specification value	following >=0805in <0805ind Bending >=0805in <0805ind	Shall be mounted on a FR4 substrate of the following dimensions: >=0805inch(2012mm):40x100x1.2mm <0805inch(2012mm):40x100x0.8mm Bending depth: >=0805inch(2012mm):1.2mm <0805inch(2012mm):0.8mm Duration of 10 sec for a min.			
Shock	Impedance :	: No damage. within±10% of i ±15% of initial v		not exceed the s	specification value	times.(IP Reflow P Test conc Type SMD Lead 3 shock	Peak Value (g's) 50 50 ks in	Normal duration (D) (ms)	h reflow f 020F Class Wave form Half-sine Half-sine irection a	Velocity change (Vi)ft/sec 11.3

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Item	Performance	Test Condition		
Solderability	More than 95% of the terminal electrode should be covered with solder.	a.Method B, 4 hrs @155°C dry heat @235°C±5°C Test time:5+0/-0.5 seconds. b. Method D category 3. (steam aging 8hours ±15 min) @ 260°C±5°C Test time: 30+0/-0.5 seconds.		
Resistance to Soldering Heat	Appearance: No damage. Impedance: within±15% of initial value DCR: within ±15% of initial value and shall not exceed the specification value	Number of heat cycles: 1 Temperature (°C) Time ramp/immersion and emersion rate 260 ±5 (solder temp) 10 ±1 25mm/s ±6 mm/s Depth: completely cover the termination		
Terminal strength	Appearance: No damage. Impedance: within±15% of initial value DCR: within±15% of initial value and shall not exceed the specification value	Preconditioning: Run through reflow for 3 times.(IPC/JEDEC J-STD-020F Classification Reflow Profiles) Component mounted on a PCB apply a force >0805inch(2012mm):1kg <=0805inch(2012mm):0.5kg to the side of a device being tested. This force shall be applied for 60 +1 seconds. Also the force shall be applied for 60 +1 seconds. Also the component being tested.		

**Derating Curve

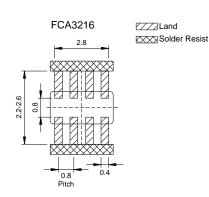
For the ferrite chip bead which withstanding current over 1.5A, as the operating temperature over 85°C, the derating current information is necessary to consider with. For the detail derating of current, please refer to the Derated Current vs. Operating Temperature curve.

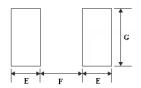


6. Soldering and Mounting

6-1. Recommended PC Board Pattern

	Chip Size							Land Patterns For Reflow Soldering		
Series	Туре	A(mm)	B(mm)	C(mm)	D(mm)	E(mm)	F(mm)	G(mm)		
FCB	<mark>1005</mark>	1.0±0.10	0.50±0.10	0.50±0.10	0.25±0.10	<mark>0.50</mark>	<mark>0.40</mark>	<mark>0.60</mark>		
FCM	1608	1.6±0.15	0.80±0.15	0.80±0.15	0.30±0.20	0.80	0.85	0.95		
HCB	2012	2.0±0.20	1.25±0.20	0.85±0.20	0.50±0.30	1.05	1.00	1.45		
GHB	2012	2.0±0.20	1.25±0.20	1.25±0.20	0.50±0.30	1.03				
FCI	3216	3.2±0.20	1.60±0.20	1.10±0.20	0.50±0.30	1.05	2.20	1.80		
FHI	3225	3.2±0.20	2.50±0.20	1.30±0.20	0.50±0.30	1.05	2.20	2.70		
FCH	4516	4.5±0.20	1.60±0.20	1.60±0.20	0.50±0.30	1.05	3.30	1.80		
HCI	4532	4.5±0.20	3.20±0.20	1.50±0.20	0.50±0.30	1.05	3.30	3.40		





PC board should be designed so that products can prevent damage from mechanical stress when warping the board.

6-2. Soldering

Mildly activated rosin fluxes are preferred. TAI-TECH terminations are suitable for re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.

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6-2.1 Soldering Reflow:

Recommended temperature profiles for lead free re-flow soldering in Figure 1. Table 1.1&1.2 (J-STD-020F)

6-2.2 Soldering Iron:

Products attachment with a soldering iron is discouraged due to the inherent process control limitations. In the event that a soldering iron must be employed the following precautions are recommended. (Figure 2.)

·Preheat circuit and products to 150°C ·350°C tip temperature (max)

·Never contact the ceramic with the iron tip ·1.0mm tip diameter (max)

Use a 20 watt soldering iron with tip diameter of 1.0mm

·Limit soldering time to 4~5sec.

Fig.1 Soldering Reflow

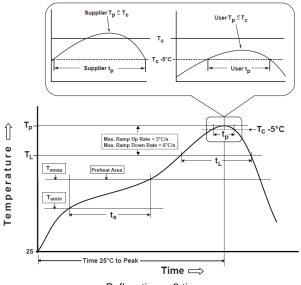
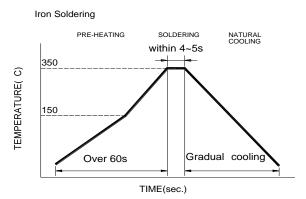


Fig.2 Iron soldering temperature profiles



Iron Soldering times: 1 times max

Reflow times: 3 times max

Table (1.1): Reflow Profiles

Profile Type:	Pb-Free Assembly
	150℃ 200℃ 60-120seconds
Ramp-up rate(T _L to T _p)	3°C/second max.
$\label{eq:Liquidus} \begin{array}{l} \text{Liquidus temperature}(T_L) \\ \text{Time}(t_L) \\ \text{maintained above } T_L \end{array}$	217℃ 60-150 seconds
Classification temperature(T _c)	See Table (1.2)
$\label{eq:tp} \mbox{Time}(t_p) \mbox{ at Tc-} \mbox{ 5^{\circ}\!$	< 30 seconds
Ramp-down rate(T _p to T _L)	6°C /second max.
Time 25℃ to peak temperature	8 minutes max.

Tp: maximum peak package body temperature, Tc: the classification temperature.

For user (customer) Tp should be equal to or less than Tc.

Table (1.2) Package Thickness/Volume and Classification Temperature (Tc)

	Package Thickness	Volume mm ³ <350	Volume mm ³ 350-2000	Volume mm ³ >2000
PB-Free Assembly	<1.6mm	260℃	260℃	260℃
	1.6-2.5mm	260℃	250℃	245℃
	≥2.5mm	250℃	245℃	245℃

Reflow is referred to standard IPC/JEDEC J-STD-020F。

6-2.3 Solder Volume:

Accordingly increasing the solder volume, the mechanical stress to product is also increased. Exceeding solder volume may cause the failure of mechanical or electrical performance. Solder shall be used not to be exceed as shown in right side:

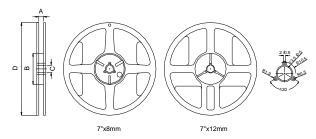
Minimum fillet height = soldering thickness + 25% product height

Upper limit Recommendable

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7.Packaging Information

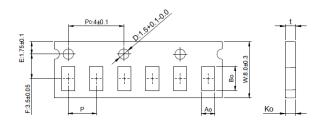
7-1. Reel Dimension



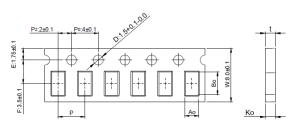
Туре	A(mm)	B(mm)	C(mm)	D(mm)	
7"x8mm	9.0±0.5	60±2	13.5±0.5	<mark>178±2</mark>	
7"x12mm	13.5±0.5	60±2	13.5±0.5	178±2	

7-2.1 Tape Dimension / 8mm

■Material of taping is paper

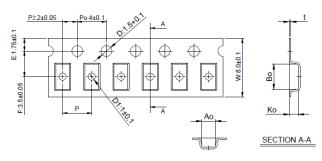


Size	Size Bo(mm)		Ko(mm)	P(mm)	t(mm)	
<mark>100505</mark>	1.12±0.03	0.62±0.03	0.60±0.03	2.0±0.05	0.60±0.03	



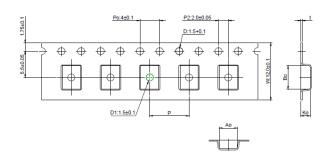
Size	Bo(mm)	Ao(mm)	Ko(mm)	P(mm)	t(mm)
160808	1.80±0.05	0.96+0.05/-0.03	0.95±0.05	4.0±0.10	0.95±0.05
201209	2.10±0.05	1.30±0.05	0.95±0.05	4.0±0.10	0.95±0.05

■Material of taping is plastic



Size	Bo(mm)	Ao(mm)	Ko(mm)	P(mm)	t(mm)	D1(mm)
201212	2.10±0.10	1.28±0.10	1.28±0.10	4.0±0.10	0.22±0.05	1.0±0.10
321611	3.35±0.10	1.75±0.10	1.25±0.10	4.0±0.10	0.23±0.05	1.0±0.10
322513	3.42±0.10	2.77±0.10	1.55±0.10	4.0±0.10	0.22±0.05	1.0±0.10
321609	3.40±0.10	1.77±0.10	1.04±0.10	4.0±0.10	0.22±0.05	1.0±0.10

7-2.2 Tape Dimension / 12mm



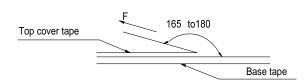
Size	Bo(mm)	Ao(mm)	Ko(mm)	P(mm)	t(mm)	D1(mm)
451616	4.70±0.10	1.75±0.10	1.75±0.10	4.0±0.10	0.24±0.05	1.5±0.10
453215	4.70±0.10	3.45±0.10	1.60±0.10	8.0±0.10	0.24±0.05	1.5±0.10

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7-3. Packaging Quantity

Chip Size	453215	451616	322513	321611	321609	201212	201209	160808	<mark>100505</mark>
Chip / Reel	1000	2000	2500	3000	3000	2000	4000	4000	<mark>10000</mark>
Inner box	4000	8000	12500	15000	15000	10000	20000	20000	<mark>50000</mark>
Middle box	20000	40000	62500	75000	75000	50000	100000	100000	<mark>250000</mark>
Carton	40000	80000	125000	150000	150000	100000	200000	200000	500000

7-4. Tearing Off Force



The force for tearing off cover tape is 15 to 60 grams in the arrow direction under the following conditions.

Room Temp.	Room Humidity	Room atm	Tearing Speed	
(°C)	(%)	(hPa)	mm/min	
5~35	45~85	860~1060	300	

Application Notice

·Storage Conditions(component level)

To maintain the solder ability of terminal electrodes:

- 1. TAI-TECH products meet IPC/JEDEC J-STD-020F standard-MSL, level 1.
- 3. Recommended products should be used within 12 months from the time of delivery.
- 4. The packaging material should be kept where no chlorine or sulfur exists in the air.

$\cdot Transportation \\$

- 1. Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
- 2. The use of tweezers or vacuum pick up is strongly recommended for individual components.
- 3. Bulk handling should ensure that abrasion and mechanical shock are minimized.