

1. Description

The EST02N10 is N-Channel enhancement MOS Field Effect Transistor. Uses advanced trench technology and design to provide excellent $R_{DS(ON)}$ with low gate charge. Device is suitable for use in DC-DC conversion, power switch and charging circuit. Standard Product EST02N10 is Pb-free.

2. Features

- 100V, $R_{DS(ON)}=95m\Omega(TYP.) @V_{GS}=10V$
- $R_{DS(ON)}=135m\Omega(TYP.) @V_{GS}=4.5V$
- Use trench MOSFET technology
- High density cell design for low $R_{DS(on)}$
- Material: Halogen free
- Reliable and rugged
- Avalanche Rated
- Low leakage current

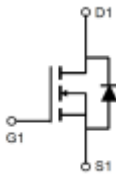
3. Applications

- PWM applications
- Load switch
- Power management in portable/desktop PCs
- DC/DC conversion

4. Ordering Information

Part Number	Package	Marking	Material	Packing	Quantity per reel	Flammability Rating	Reel Size
EST02N10	SOT-23	ES5N10	Halogen free	Tape & Reel	3,000 PCS	UL 94V-0	7 inches

5. Pin Configuration and Functions

Pin	Function	Outline	Circuit Diagram
1	Gate		
2	Source		
3	Drain		

6. Specification

Absolute Maximum Rating & Thermal Characteristics

Ratings at 25 °C ambient temperature unless otherwise specified.

Parameter		Symbol	Limit	Unit
Drain-Source Voltage		BV_{DSS}	100	V
Gate-Source Voltage		V_{GS}	±20	V
Continuous Drain Current	$T_A=25^{\circ}\text{C}$	I_D	3	A
	$T_A=100^{\circ}\text{C}$		2.2	
Maximum Power Dissipation		P_D	3.1	W
Pulsed Drain Current ^A		I_{DM}	12	A
Operating Junction Temperature		T_J	-55 to 150	°C
Lead Temperature		T_L	260	°C
Storage Temperature Range		T_{stg}	-55 to 150	°C

Thermal resistance ratings

Single Operation				
Parameter	Symbol	Typical	Maximum	Unit
Junction-to-Ambient Thermal Resistance ^B	$R_{\theta JA}$		40.3	°C/W

Note:

A. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.

B. $R_{\theta JA}$ is measured with the device mounted on a 1inch² pad of 2oz copper FR4 PCB.

Electrical Characteristics

At TA = 25°C unless otherwise specified

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
OFF CHARACTERISTICS						
Drain-to-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =250uA	100			V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =100V, V _{GS} =0V			1	uA
Gate-to-source Leakage Current	I _{GSS}	V _{DS} =0V, V _{GS} =±20V			±100	nA
ON CHARACTERISTICS						
Gate Threshold Voltage	V _{GS(TH)}	V _{GS} =V _{DS} , I _D =250uA	1.0	1.65	2.5	V
Drain-to-source On-resistance	R _{DS(on)}	V _{GS} =10V, I _D =3A		95	130	mΩ
		V _{GS} =4.5V, I _D =1A		135	190	
CHARGES, CAPACITANCES AND GATE RESISTANCE						
Input Capacitance	C _{ISS}	V _{GS} =0V, f=1MHz, V _{DS} =50V		200		pF
Output Capacitance	C _{OSS}			30		
Reverse Transfer Capacitance	C _{RSS}			3		
Total Gate Charge	Q _{G(TOT)}	V _{GS} =0 to 10V, V _{DS} =50V, I _D =3A		4		nC
Gate-to-Source Charge	Q _{GS}			0.9		
Gate-to-Drain Charge	Q _{GD}			1.1		
SWITCHING CHARACTERISTICS						
Turn-On Delay Time	t _{d(ON)}	V _{GS} =10V, V _{DD} =50V, I _D =3A, R _G =3Ω		13		ns
Rise Time	t _r			19		
Turn-Off Delay Time	t _{d(OFF)}			20		
Fall Time	t _f			28		
BODY DIODE CHARACTERISTICS						
Forward Voltage	V _{SD}	V _{GS} =0V, I _S =3A			1.2	V

7. Typical Characteristic

Figure 1: Output Characteristics

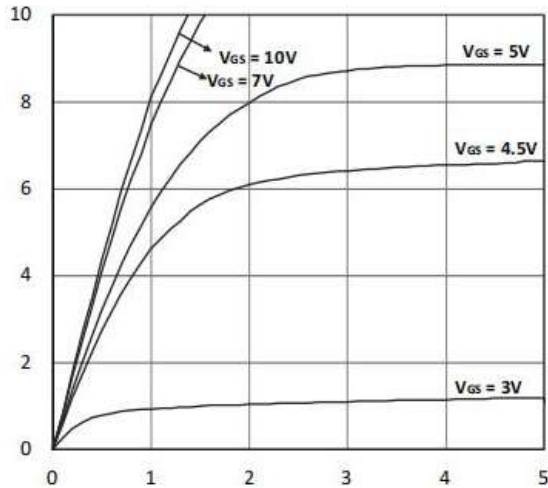


Figure 2: Typical Transfer Characteristics

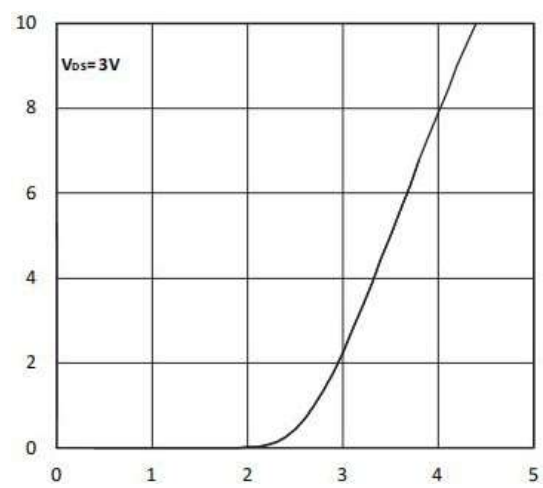


Figure 3: On-resistance vs. Drain Current

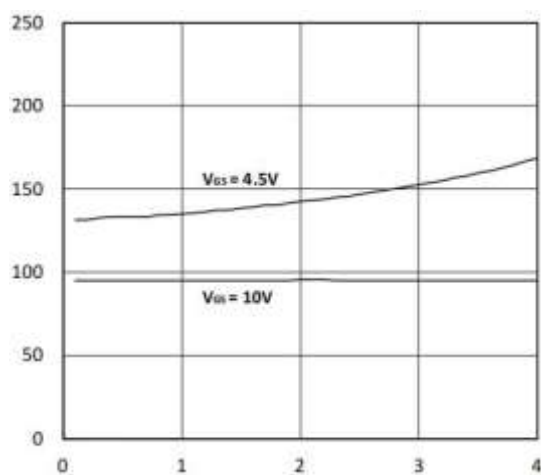


Figure 4: Body Diode Characteristics

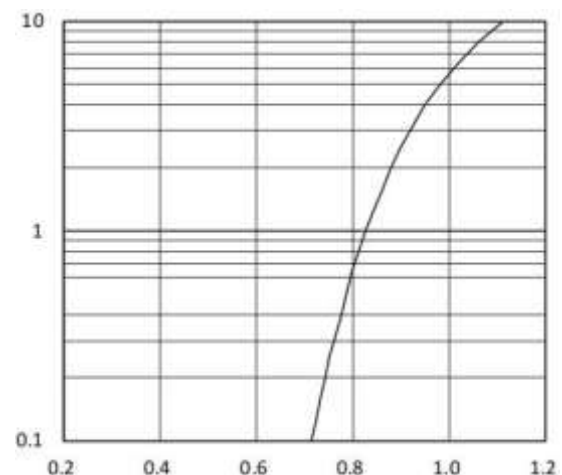


Figure 5: Capacitance Characteristics

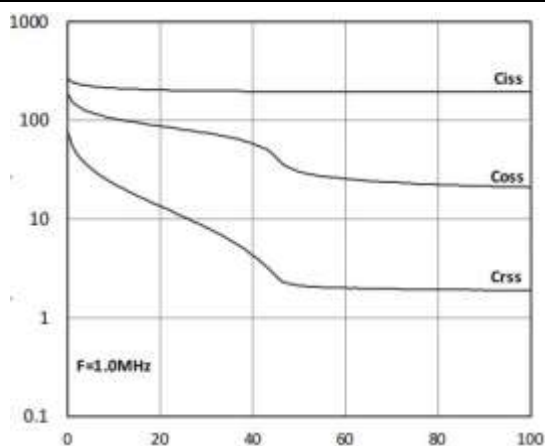
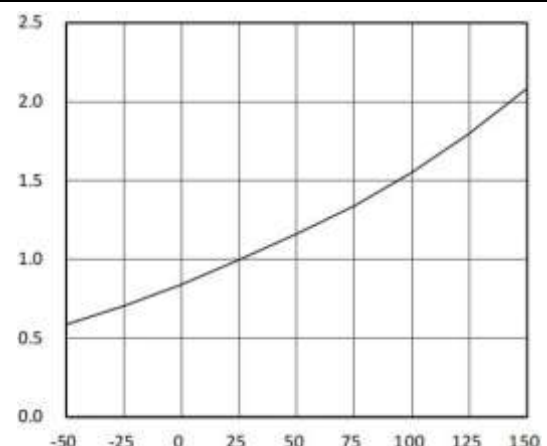
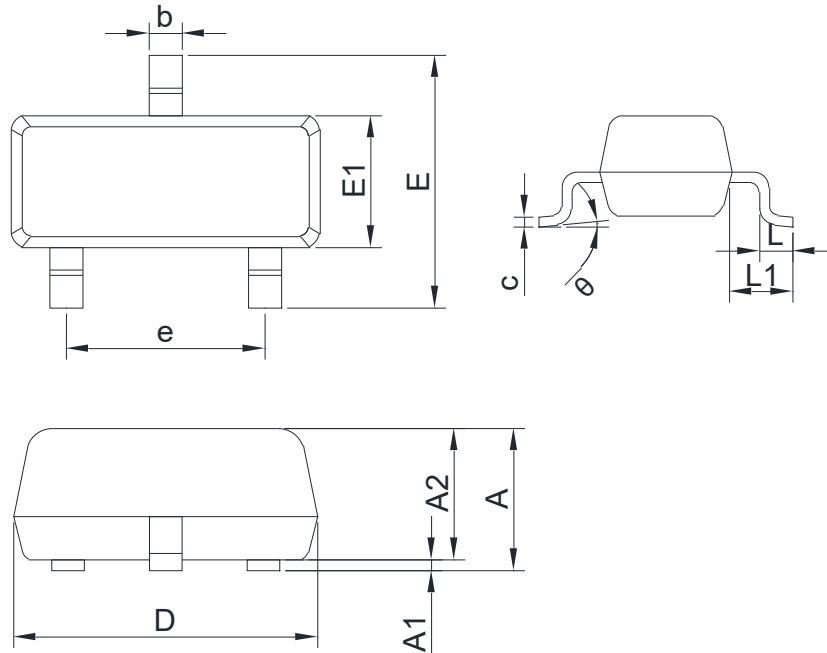


Figure 6: Normalized on Resistance vs. Junction Temperature



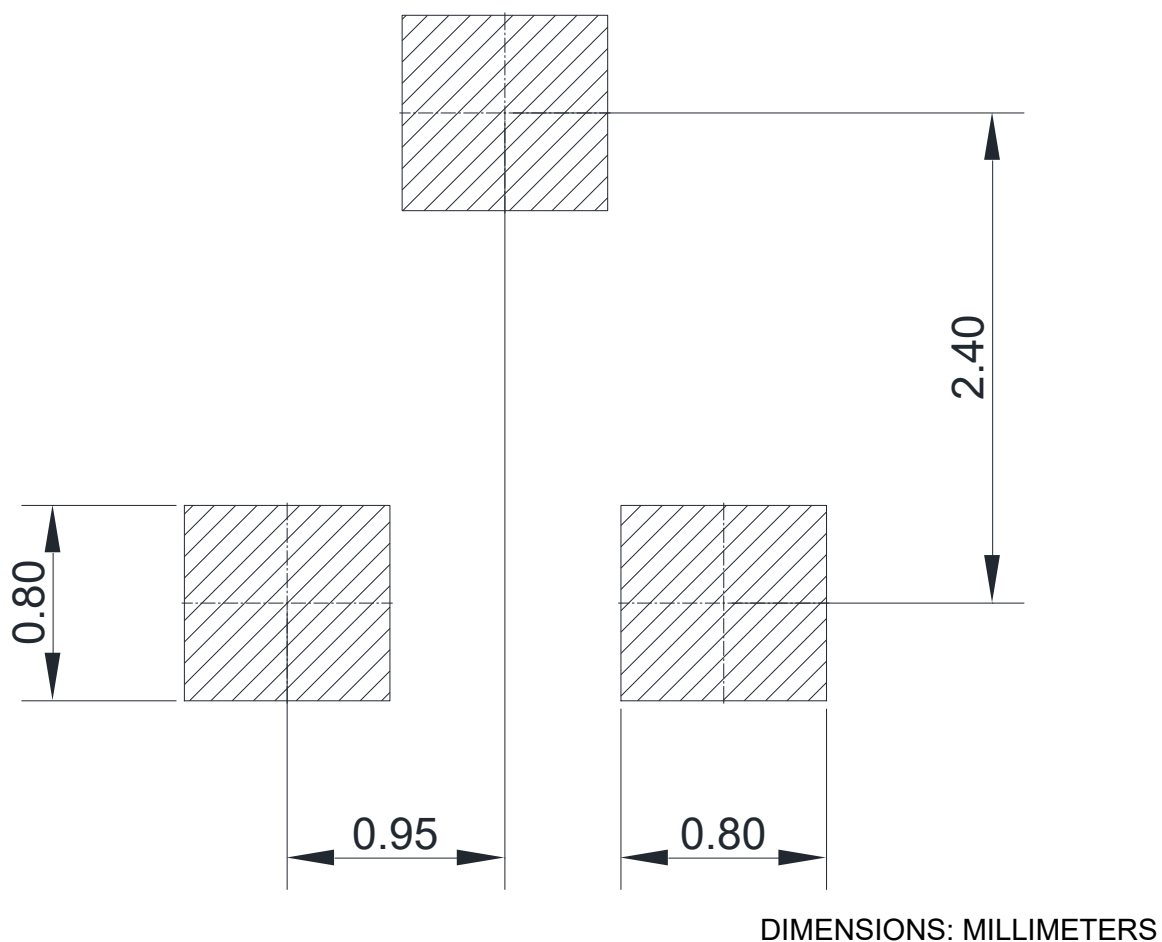
8. Dimension (SOT-23)

POD(Z)



COMMON DIMENSIONS CUNITS MEASURE=MILLIMETER					
SYMBOL	MIN	MAX	SYMBOL	MIN	MAX
A	0.90	1.20	E	2.25	2.55
A1	0.00	0.10	E1	1.20	1.40
A2	0.90	1.10	e	1.80	2.00
b	0.30	0.50	L	0.30	0.50
c	0.07	0.18	L1	0.475	0.625
D	2.80	3.04	θ	0°	8°

9. Recommended Soldering Footprint



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