

Discription

The HDVIULC64SC6 is a 5-channel ultra low capacitance rail clamp ESD protection diodes array. Each channel consists of a pair of ESD diodes that steer positive or negative ESD current to either the positive or negative rail. A zener diode is integrated in to the array between the positive and negative supply rails. In the typical applications, the negative rail pin (assigned as GND) is connected with system ground. The Positive ESD current is steered to the ground through an ESD diode and Zener diode and the positive ESD voltage is clamped to the zener voltage.

6 5 mg-rapes 3

SOT-23-6L

6 5 4

Circuit Diagram

Features

- ★ 5 channels of ESD protection;
- ★ Provides ESD protection to IEC61000-4-2 level 4
 - ±27kV air discharge
 - ±15kV contact discharge;
- ★ Channel I/O to GND capacitance: 0.4pF(Max)
- ★ Channel I/O to I/O capac itance: 0.8pF(Max)
- ★ Low clamping voltage;
- ★ Low operating voltage;
- ★ Improved zener structure;
- ★ Optimized package for easy high speed data lines PCB layout;
- ★ RoHS compliant.

Ordering Information

Product ID	Pack	Qty(PCS)
HDVIULC64SC6	SOT23-6L	3000

Absolute Ratings(Tamb = 25°C)

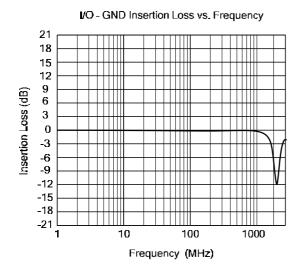
Characteristics	Symbol	Ratings	Unit
Peak Pulse Power(8/20µs)	P _{PP}	55	W
Peak Pulse Current(8/20µs)	I _{PP}	4	А
ESD per IEC 61000-4-2(Air)	V _{ESD1}	±20kV	kV
ESD per IEC 61000-4-2(Contact)	V _{ESD2}	±15kV	kV
Operating Temperature Range	Topr	-55 ~ +125	°C
Storage Temperature Range	Tstg	-55 ~ +150	°C

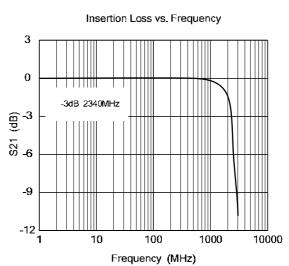


Electrical Characteristics

Characteristics	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Reverse Working	V _{RWM}	Any I/O pin to GND			5	V
Voltage					5	V
Reverse Breakdown	V_{BR}	I _t =1mA;	6			V
Voltage		Any I/O pin to GND				
Reverse Leakage	I _R	V _{RWM} =5V, T=25°C;			1	μA
Current		Any I/O pin to GND				
Desitive Clamping		I _{PP} =4A, t _P =8/20 µs;				
Positive Clamping	V _{C1}	Positive pulse;		8.5	12.0	V
Voltage		Any I/O pin to GND				
Nogative Clamping	V _{C2}	I _{PP} =4A, t _P =8/20μs;				V
Negative Clamping Voltage		Negative pulse;		1.8		
		Any I/O pin to GND				
Junction Capacitance	C _{J1}	V _R =0V, f=1MHz;	0.3	0.3	0.4	pF
Between Channel		Between I/O pins		0.3		
Junction Capacitance	C	V _R =0V, f=1MHz;		0.0	0.8	pF
Between I/O And GND	C_{J2}	Any I/O pin to GND		0.6	0.8	þΓ

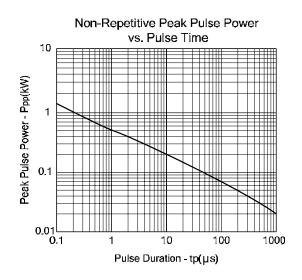
Typical Characteristics

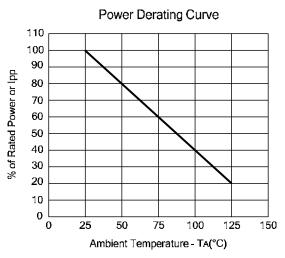


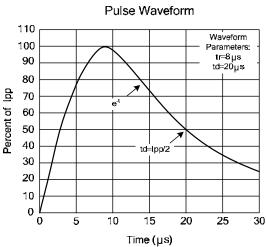


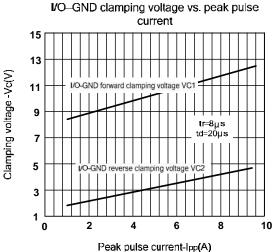


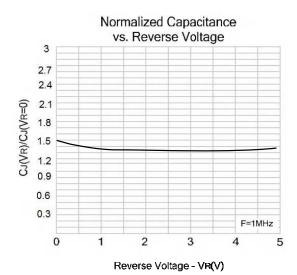
TYPICAL ELECTRICAL CHARACTERISTICS CURVE



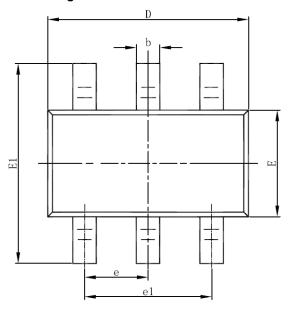


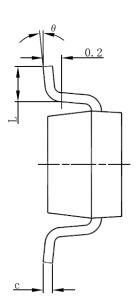


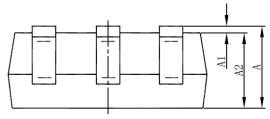




SOT23-6L Package Information







Symbol	Dimensions In Millimeters		Dimensions In Inches		
	Min	Max	Min	Max	
Α	1.050	1.250	0.041	0.049	
A1	0.000	0.100	0.000	0.004	
A2	1.050	1.150	0.041	0.045	
b	0.300	0.500	0.012	0.020	
С	0.100	0.200	0.004	0.008	
D	2.820	3.020	0.111	0.119	
E	1.500	1.700	0.059	0.067	
E1	2.650	2.950	0.104	0.116	
е	0.950	0.950(BSC)		0.037(BSC)	
e1	1.800	2.000	0.071	0.079	
L	0.300	0.600	0.012	0.024	
θ	0°	8°	0°	8°	

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